* SD+usb: new sd card slot, pinout for 1 line sdmmc1 pc8, pc12, pd2, sd detect pull-up input

SWO for debug pb3 testpoint

* Codec: new pinout, resistor arrays fixed
* Memory: new qspi flash, buffering scheme for ui spi, new pinout spi1->psram, spi2->ui
* Ui: new encoder buffered design, changed all power to 5v, new big registers easy to solder, new connector & buffered spi

UPDATE PCB fix sheet symbols

CHECK display footprint

Think about memory options, SPI & UART availability for all scenarios