



TECHNISCHE
UNIVERSITÄT
WIEN

DISSERTATION

Cool Science

ausgeführt am Atominstutut



der Technische Universität Wien
Fakultät für Physik

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Wien, am 25.04.2020

“The Setesh guard’s nose drips.”
TEAL’C

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¹ 1 Deflection Electronics

² 1.1 Demands on the setup

³ For the QuaK experiment it is paramount to be able to deflect the beam precisely
⁴ and with the right frequency. As previously mentioned, our deflection system simply
⁵ consists of two pairs of parallel plates between which a voltage is applied. Controlling
⁶ this voltage allows us to control the deflection of the beam. Various aspects are
⁷ important here (illustrated in figure 1.1):

Optional: inser
Foto of CRT's
flection plates -
fig:DeflectionSe

⁸ **Offset:** Although the deflection of the beam is controlled by the voltage between the
⁹ plates, it is necessary to be able to set their mean potential as well. During normal
¹⁰ operation this offset voltage is at 96 V for the x-direction and at 78 V for the
¹¹ y-direction.

¹² **Amplitude:** The deflection coefficients in the x and y planes are 19 V cm^{-1} and
¹³ 11.5 V cm^{-1} respectively (see [2]). We therefore need to be able to supply approxi-
¹⁴ mately 70 V.

¹⁵ **Frequency:** The final goal is to be able to deflect the beam at the hyperfine splitting
¹⁶ frequency of ${}^{39}\text{K}$, which is 461.7 MHz. This is likely to prove impossible with
¹⁷ this CRT-model, observaions at the highest frequency we have tried so far will
¹⁸ be discussed in section (Missing).

Insert Reference
last section

¹⁹ **Waveform:** Ultimategly we want the cold atoms to experience a field that oscillates like
²⁰ a sine wave. As a first try it is therefore reasonable to apply a sinusoidal voltage.

²¹ **Lissajous curves:** Having the ability to control the deflection in both the x- and the
²² y- axis, allows us to have our beam draw out Lissajous Curves. By applying sine
²³ waves of equal frequency to both pairs of deflection plates and by being able to
²⁴ control the phase between them we can have the beam oscillate on a straight line
²⁵ or a cricle. This allows us to generate either a linearly or cirularly polarized field.

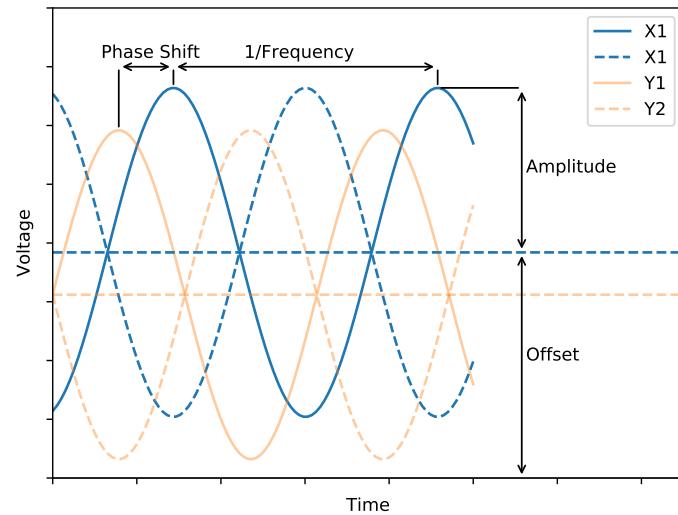


Figure 1.1

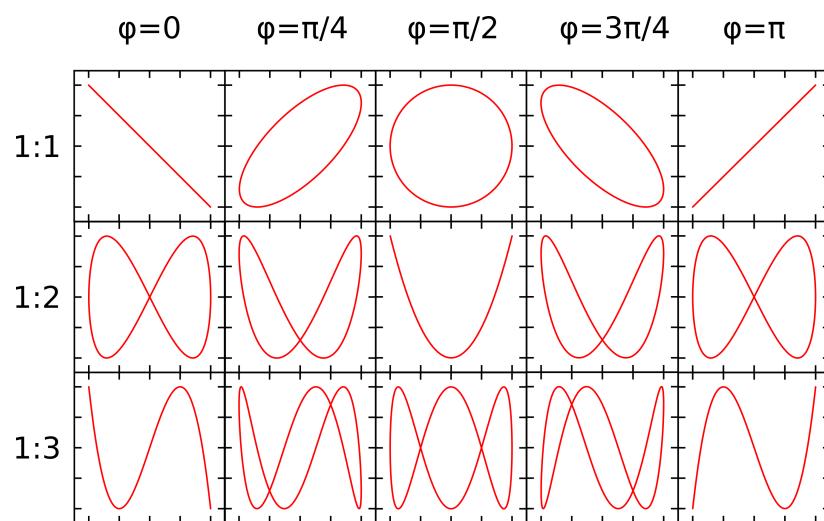


Figure 1.2: from [Wikipedia Lissajous]

¹ 1.2 Implementation

² A first setup with which we can try to obtain the desired voltages is depicted in figure
³ [1.3](#). On the very left we have a signal generator that is capable of producing the right
⁴ frequency (461.7 MHz) this signal is then split up into an x- and a y-branch. One of
⁵ the two branches is connected to a phase shifter, which is able to delay the input signal
⁶ by up to 200°, allowing us to set any desired phase shift between x- and y-deflection
⁷ and to correct for inadvertent delays from the other electronics. Both the x- and
⁸ the y-signal are then amplified using (amplifier). [In the final step, a center tapped](#)
⁹ transformer allows us to produce two voltages for the plates X1 and X2 (or Y1 and Y2
¹⁰ respectively) with a phase shift of exactly 180° between them. By setting the center
¹¹ tap to the desired offset potential, we should get the voltage curves described above.
¹² To understand this setup in more detail, it is useful to examine its most important
¹³ parts more closely:

¹⁴ **Amplifier:** Up to now we have used the XXX [and the YYY](#) amplifier, they amplify
¹⁵ the signal by a fixed gain of (How much?) [, inputs and outputs can simply be](#)
¹⁶ connected via BNC cables, the amplifier is powered by a linear power supply
¹⁷ with a DC voltage of 24 V via two banana plugs in the front. As we want to be
¹⁸ control the shapes of the Lissajous curves (and as the deflection coefficients for
¹⁹ the x- and y- plates differ) it is desirable to be able to adjust the gain of the
²⁰ amplifiers in future versions of the setup.

²¹ **Center Tapped Transformer:** The center tapped transformer we use is the Mini-
²² Circuits TC8-1G2+ ([\[TC8-1G2\]](#)), a transformer for frequencies of 2 MHz to
²³ 500 MHz, with a winding ratio of 1 to 8. Figure [1.6](#) shows how the center tapped
²⁴ transformer is implemented. The in- and outputs, as well as the bias voltage
²⁵ can be connected via BNC cables. As usual, the shields of all of these cables are
²⁶ connected to ground, furthermore they are connected to each other and to the
²⁷ housing. As a safety feature, both outputs X1 and X2 are directly connected
²⁸ to the bias through an arrangement of diodes: Two connections, each with a
²⁹ normal diode and a Zener diode facing in opposite directions. The breakdown
³⁰ voltage of the Zener diode is 200 V, during normal operation the voltage on it
³¹ stays below this value and none of the connections let any current through as
³² one of the two diodes is always blocking it. However if one of the plates in the
³³ CRT accidentally comes in contact with high voltage, the connection with the
³⁴ appropriately oriented Zener diode opens up, preventing a voltage spike on the
³⁵ center tapped transformer and thereby protecting the electronics connected to
³⁶ its primary circuit.

³⁷ At the point of writing there are still some problems with the behaviour of the
³⁸ center tapped transformer, the capacitances of the diodes introduce an undesired

Find out which
amplifier

which model?

which model?

Check amplifier
specifications

Optional: Go to
lab and measure
their performance
with an Oszil.

I don't really know
whether a high
resistance bias or
low resistance bias
is better.

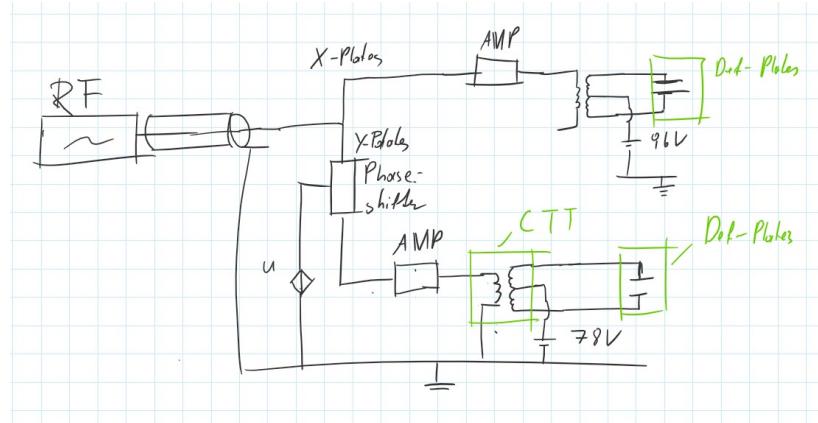


Figure 1.3: Deflection circuit

phaseshift between the two signals. Figure 1.5a shows the circuit's behaviour at 465 MHz without its diodes, here the signals are shifted by $120 \text{ ps} = 20^\circ = 0.35 \text{ rad}$. Additionally, applying a bias voltage leads to differing amplitudes, as can be seen in figure 1.5b.

Phase Shifter: To control the phase shift between the x- and y-deflection plates, we use a Mini-Circuits [JSPHS-661] phase shifter. This part was put in a housing ([Hammond1455D601RD]) on a separate PCB and can be connected via BNC cables. Figure 1.4a shows how the phase shifter is connected and figure 1.4b shows the corresponding PCB layout. Note that again the shields of the BNC cables are connected among each other and to the housing. The JSPHS-661+ is designed for frequencies in the range 400 MHz to 600 MHz. By applying a DC voltage of 0 V to 12 V to the bias connector, it is possible to introduce a phase shift of up to 200° to the signal.

(figure) _____
 (Performance) _____

- 1
- 2
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14 Insert fotos of finished circuits in housings
 15

Optional: Go to the Lab, set the whole thing up, look at its performance on the scope

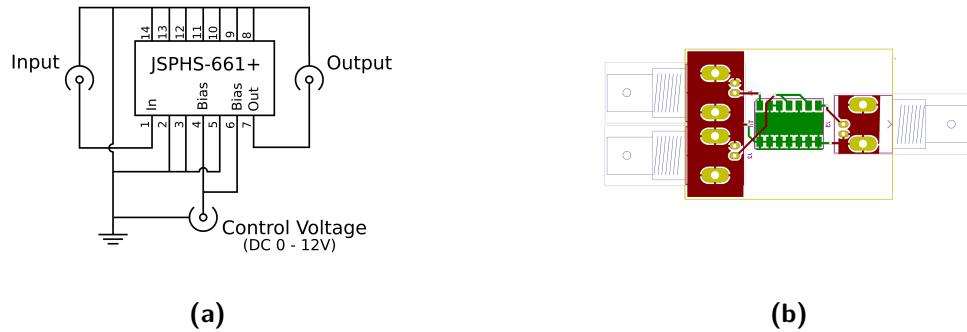


Figure 1.4



(a) Signal of center tapped transformer without diodes, unbiased at 465 MHz
 (b) Signal of center tapped transformer without diodes, unbiased at 465 MHz

Figure 1.5

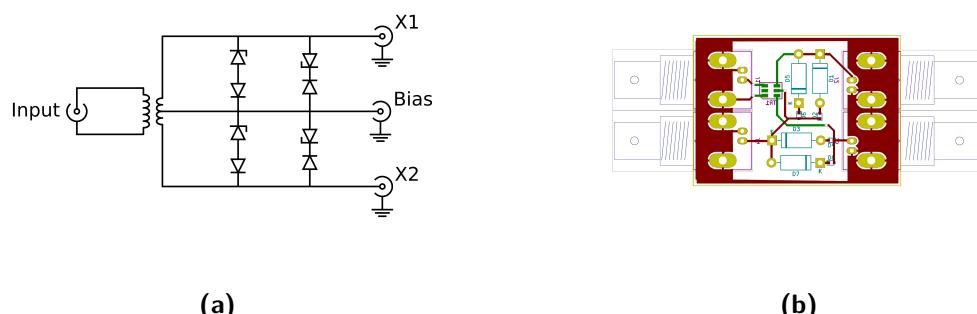


Figure 1.6

Todo list

1

- Optional: insert Foto of CRT's deflection plates - fig:DeflectionSetup 1 2
- Insert Reference to last section 1 3
- Find out which amplifier 3 4
- which model? 3 5
- which model? 3 6
- Check amplifier specifications 3 7
- Optional: Go to the lab and measure their performance with an Oszi. 3 8
- I don't really know whether a high resistance bias or a low resistance bias is better. 9 10
- Optional: Go back to the Lab and fix the problem with the diodes? 4 11
- Insert fotos of finished circuits in housings 4 12
- Optional: Go back to the Lab, set up the whole thing and look at its performance on the oszi 4 13
- 4 14

References

15

- ¹ [1] Wolfgang Demtröder. *Experimentalphysik 3*. Springer-Lehrbuch. Berlin, Heidelberg: Springer-Verlag Berlin Heidelberg, 2009. Chap. 2.6.1.
- ² [2] Frank Philipse. *D14363GY123*. URL: <https://frank.pocnet.net/sheets/186/d/D14363GY123.pdf> (visited on 03/10/2020).
- ³ [3] Jerry C. Whitaker and Fla. [Verlag] CRC Press Boca Raton. *Power vacuum tubes handbook*. Third edition. Electronics handbook series. Boca Raton, FL: CRC Press. Chap. 3.5.2.1.
- ⁴ [4] Chuck deVere. *Cathode-Ray Tubes*. 1969.