

Q1: Consider 4 processes A, B, C, D scheduled on a CPU in round robin fashion with a time quantum of 5-time units. The processes are assumed to have arrived in the order A, B, C and D, all at time T=0. There are exactly two context switching from A to B, one context switching from B to C, and no context switching from B to D and B to A. Which one of the following is possible as CPU bursts (in time units) of these processes?

Round robin fashion with a time quantum of 5-time units. Arrival= T=0.

There are exactly two context switching from A to B, one context switching from B to C, and no context switching from B to D and B to A.

5 < CPU burst (A). CPU burst (B) < 10 & 0 < CPU burst (C). CPU burst (D) < = 5

A	В		C	D	A	В
0	5	10	X	у	Z	q

Q2: Consider 4 processes P, Q, R, S scheduled in round robin fashion with a time quantum of 4-time units on a CPU. The processes are assumed to have arrived in the order P, Q, R and S all at time T=0. There are exactly one context switching each from S to Q, and from R to Q. There is exactly two context switching from Q to R and no context switching from S to P. Which one of the following is **not** possible as CPU bursts (in time units) of these processes? ?

(a)
$$P=4$$
, $Q=10$, $R=6$, $S=2$ (b) $P=3$, $Q=9$, $R=5$, $S=4$

(b)
$$P= 3$$
, $Q= 9$, $R=5$, $S=4$

(c)
$$P=4$$
, $Q=12$, $R=8$, $S=4$ (d) $P=2$, $Q=6$, $R=6$, $S=4$

(d)
$$P= 2$$
, $Q= 6$, $R=6$, $S=4$

4 processes P, Q, R, S in round robin fashion with a time quantum of 4-time units Arrival – T=0. There are exactly one context switching each from S to Q, and from R to Q. There is exactly two context switching from Q to R and no context switching from S to P.

Summary: P<=4; Q>8; 4<R<=8; S<=4

(a)
$$P=4$$
, $Q=10$, $R=6$, $S=2$ (b) $P=3$, $Q=9$, $R=5$, $S=4$

(c)
$$P=4$$
, $Q=12$, $R=8$, $S=4$ (d) $P=2$, $Q=6$, $R=6$, $S=4$

FCFS CPU scheduling

Q3: Consider a ready queue which gets a new process of CPU burst 5 cycles in clock cycle X (X is a multiple of 10) and a new process of CPU burst 6 cycles in clock cycle Y (Y is a multiple of 11), FCFS scheduling is used. Neglect overhead of context switching. First process enters ready Q at clock cycle 10 and moves to running state at the same cycle itself. What is the average waiting time for first 20 process that enters the ready queue?

Let us assume processes A1, A2, A3 .., A9, A10 arrives at clock cycles 10, 20, 30,..,100, respectively. Similarly processes B1, B2, B3,...B9, B10 arrives at clock cycles 11, 22, 33,...,99, 110, respectively.

Gantt chart

A1	B1	A2	B2	A3	B3	A4	B4	A5	B5	
10	15	21	26	32	37	43	48	54	59	65
A6	B6	A7	B7	A8	B8	A9	B9	A10	B10)
	<u>'</u>	•	•	•		<u>'</u>	•	109		

FCFS CPU scheduling

Let us assume processes A1, A2, A3 .., A9, A10 arrives at clock cycles 10, 20, 30,..,100, respectively. Similarly processes B1, B2, B3,...B9, B10 arrives at clock cycles 11, 22, 33,...,99, 110, respectively.

Gantt chart

A1	B1	A2	B2	A3	В3	A4	B4	A5	B5	
10	15	21	26	32	37	43	48	54	59	65
A6	В6	A7	B7	A8	B8	A9	B9	A10	B10	
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Waiting time of A's = (10-10) + (21-20) + (32-30) + ... + (109-100)

$$= 0 + 1 + 2 + \dots + 9 = 45$$

Waiting time of B's = (15-11) + (26-22) + (37-33) + ... + (114-110)

$$= 4 + 4 + \dots + 4 = 40$$

Average waiting time = (45+40)/20 = 4.25

Q4. Consider 4 processes P, Q, R, S scheduled in round robin fashion with a time quantum of 3-time units on a CPU. The processes are assumed to have arrived in the order P, Q, R and S, all at time T=0. There are exactly one context switching each from Q to P, R to P and S to P. There is exactly three context switching from P to Q. After three processes complete their execution, the last process will not take more than one time quantum to complete. Answer the following:-

- (a) Average Waiting time
- (b) Average turnaround time

P, Q, R, S in round robin fashion with a time quantum of 3-time units. T=0 arrival There are exactly one context switching each from Q to P, R to P and S to P. There is exactly three context switching from P to Q. After three processes complete their execution, the last process will not take more than one time quantum to complete.

P Q R S P Q R P Q 3 3 1/2/3 3 3 1/2/3 3 1/2/3	ъ								
3 3 1/2/3 3 1/2/3 3 1/2/3 3 1/2/3	Р	QR	S	P	Q	R	P	Q	P
	3	3 3	1/2/3	3	3	1/2/3	3	1/2/3	1/2/3

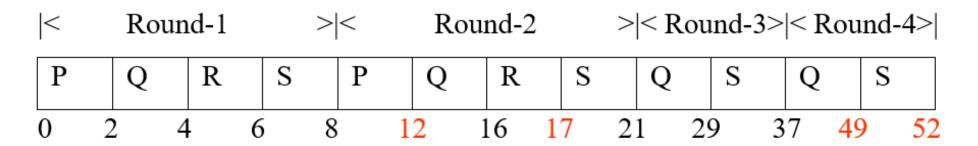
Q5: Consider 4 processes P, Q, R, S that were to be scheduled on a CPU using a dynamic round robin scheduling with a time quantum of 2^x clock cycles, where x indicates the round number starting with 1, and then 2, and then 3, and so on for subsequent rounds. ie, time quantum increases in each round. Each process will get at most one slot of running state in each round. The arrival time of P, Q, R and S are at clock cycles 0, 1, 2 and 3, respectively. It was found that P and R completed execution at clock cycles 12 and 17, respectively. Turnaround time of S is one clock cycle more than the turnaround time of Q. If average turnaround time of the 4 processes is 31 cycles, with the help of a neat Gantt chart answer the following.

- a) How long is the CPU burst of each of the processes?
- b) How many preemptive context switching happened overall?
- c) What is the waiting time for process R?

- Dynamic round robin scheduling, $T = 2^x$ clock cycles, where x = 1, 2, 3...
- Each process will get at most one slot of running state in each round.
- Arrival P \rightarrow 0, Q \rightarrow 1, R \rightarrow 2 and S \rightarrow 3
- P and R completed execution at clock cycles 12 and 17, respectively.
- Turnaround time of S is one clock cycle more than the turnaround time of Q.
- Average turnaround time of the 4 processes is 31 cycles.

$$TAT = 12 + y + (17-2) + (y+1) = 31 \times 4$$
 $\rightarrow y = 48$

So Q should complete at 48 + 1 = 49, and S should complete on 49 + 3 = 52



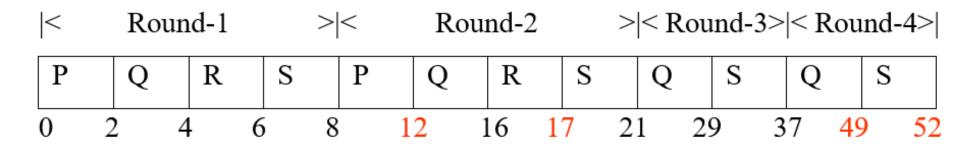
(a) How long is the CPU burst of each of the processes?

Turnaround time {T_completion-T_arrival} for each process as follows,

$$P \rightarrow 12-0 = 12 : Q \rightarrow 49-1 = 48 : R \rightarrow 17-2 = 15 : S \rightarrow 52-3 = 49$$

Avg. Turnaround time= (12+48+15+49)/4 = 31

CPU burst $P \rightarrow 6$, $Q \rightarrow 26$, $R \rightarrow 3$, $S \rightarrow 17$ {Overall 52 cycles of execution}



- (b) How many preemptive context switching happened overall?
- 8 times. @ clock cycles 2, 4, 6, 8, 16, 21, 29, 37
- (c) What is the waiting time for process R?

 $T_{\text{waiting }}(R) = T_{\text{completion}}(R)_{\text{Tarrival}}(R)_{\text{CPU burst}}(R) = 17-2-3 = 12 \text{ cycles}$



Thank You