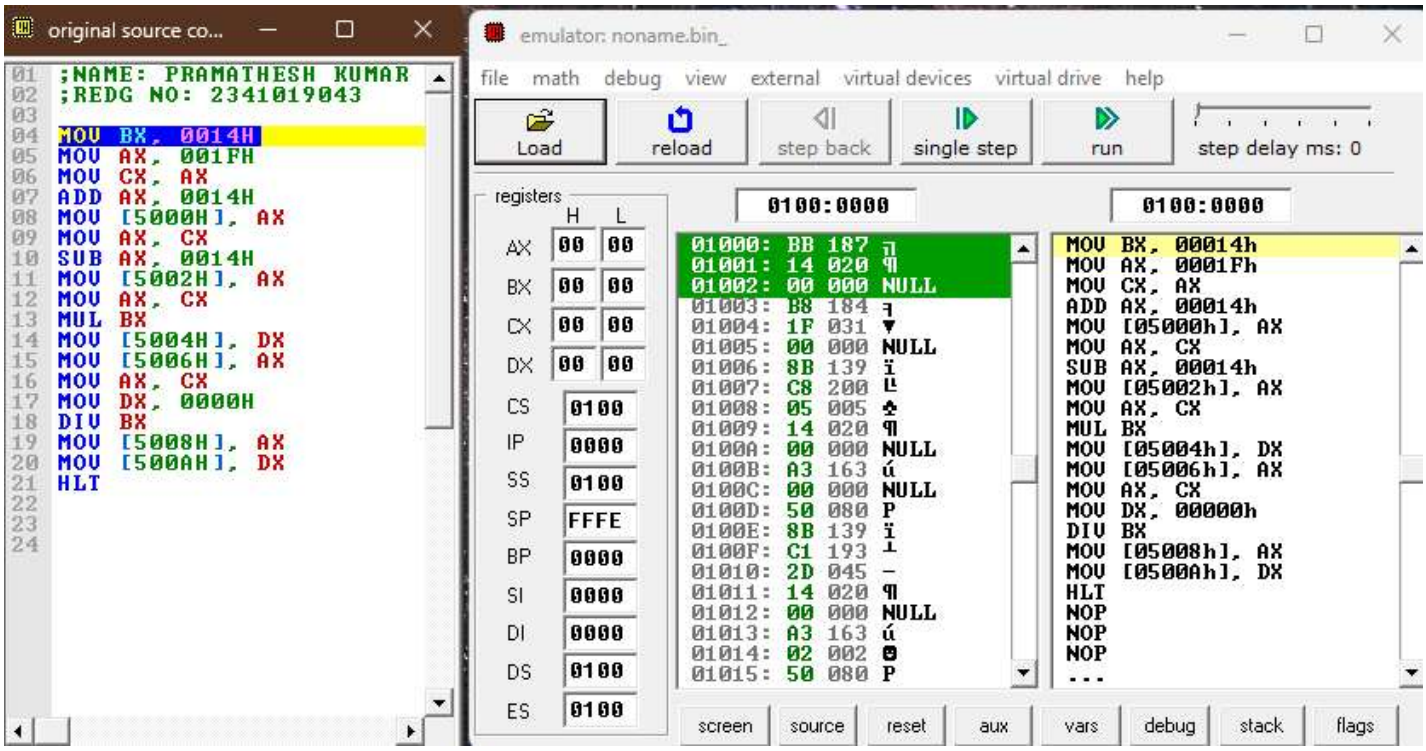
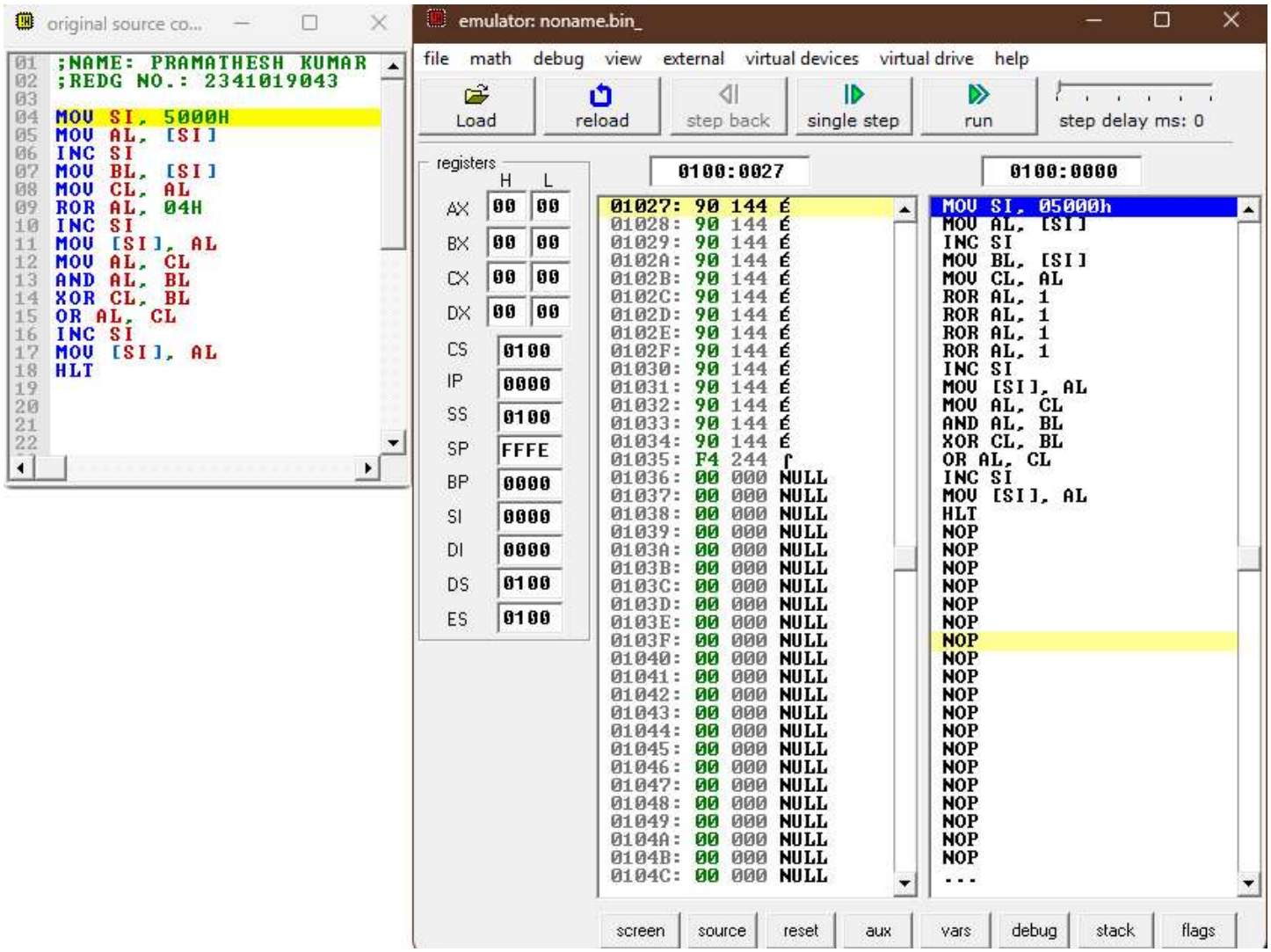
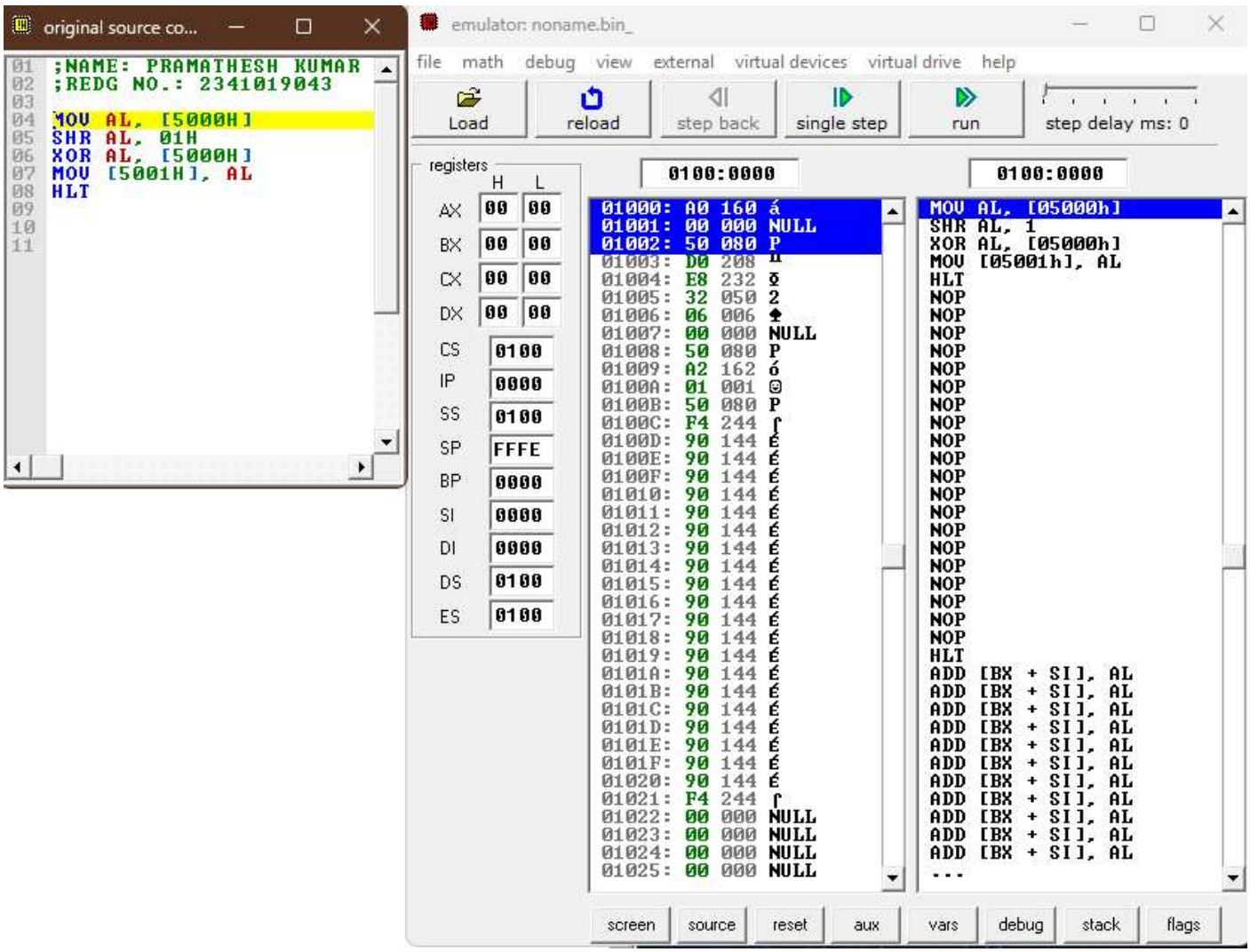
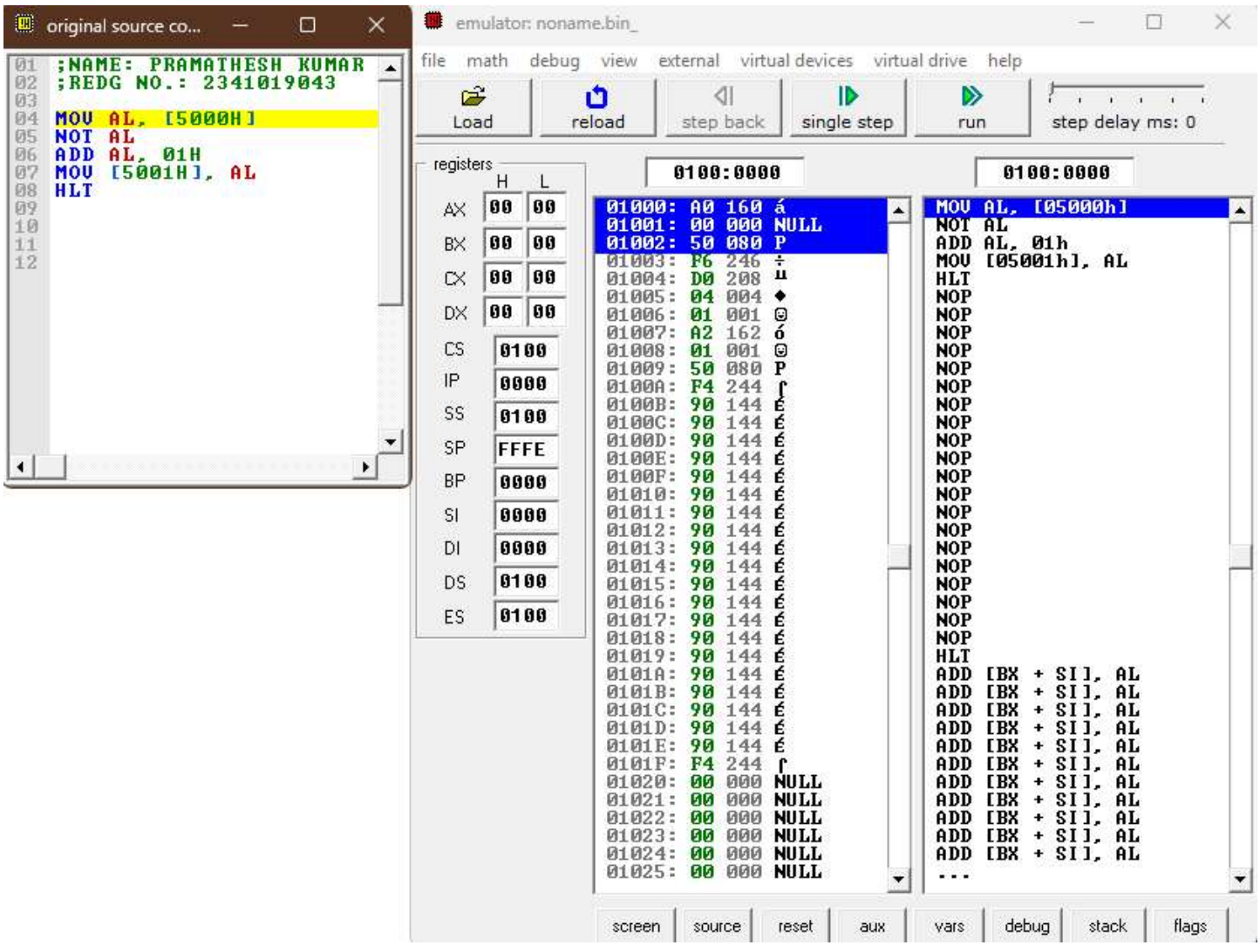
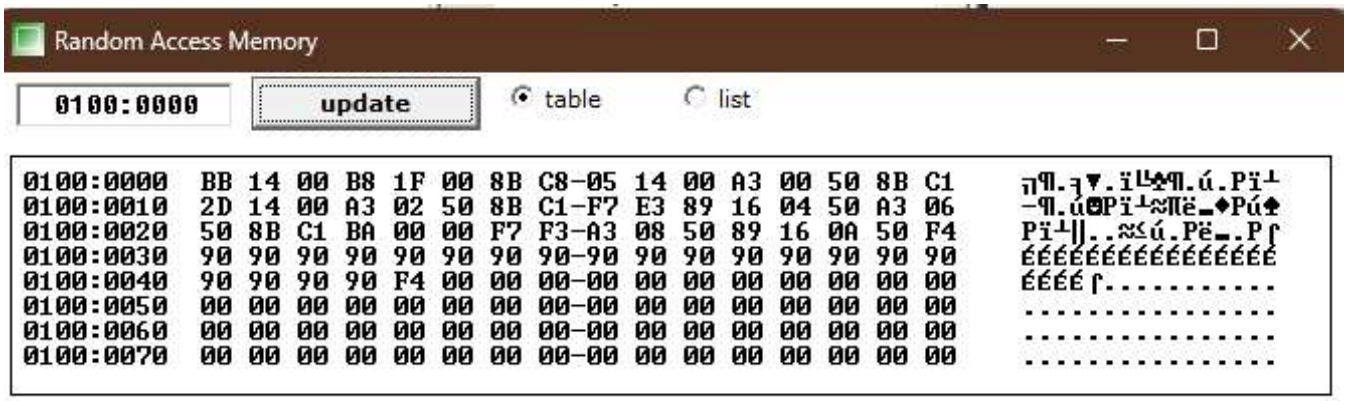
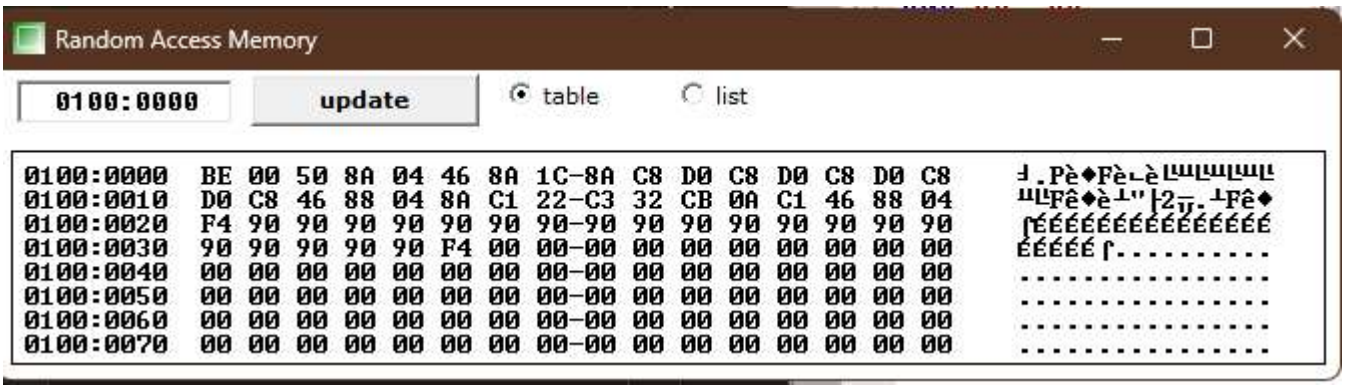

original source co...
;NAME: PRAMATHESH KUMAR
;REDG NO: 2341019043
MOV BX, 0014H
MOV AX, 001FH
MOV CX, AX
ADD AX, 0014H
MOV [5000H], AX
MOV AX, CX
SUB AX, 0014H
MOV [5002H], AX
MOV AX, CX
MUL BX
MOV [5004H], DX
MOV [5006H], AX
MOV AX, CX
MOV DX, 0000H
DIV BX
MOV [5008H], AX
MOV [500AH], DX
HLT
emulator: noname.bin_
file math debug view external virtual devices virtual drive help
Load
reload
step back
single step
run
step delay ms: 0
registers
H L
AX 00 00
BX 00 00
CX 00 00
DX 00 00
CS 0100
IP 0000
SS 0100
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0100
ES 0100
0100:0000
01000: BB 187
01001: 14 020
01002: 00 000 NULL
01003: B8 184
01004: 1F 031
01005: 00 000 NULL
01006: 8B 139
01007: C8 200
01008: 05 005
01009: 14 020
0100A: 00 000 NULL
0100B: A3 163
0100C: 00 000 NULL
0100D: 50 080 P
0100E: 8B 139
0100F: C1 193
01010: 2D 045 -
01011: 14 020
01012: 00 000 NULL
01013: A3 163
01014: 02 002
01015: 50 080 P
0100:0000
MOV BX, 00014h
MOV AX, 0001Fh
MOV CX, AX
ADD AX, 00014h
MOV [05000h], AX
MOV AX, CX
SUB AX, 00014h
MOV [05002h], AX
MOV AX, CX
MUL BX
MOV [05004h], DX
MOV [05006h], AX
MOV AX, CX
MOV DX, 00000h
DIV BX
MOV [05008h], AX
MOV [0500Ah], DX
HLT
NOP
NOP
NOP
...
screen source reset aux vars debug stack flags

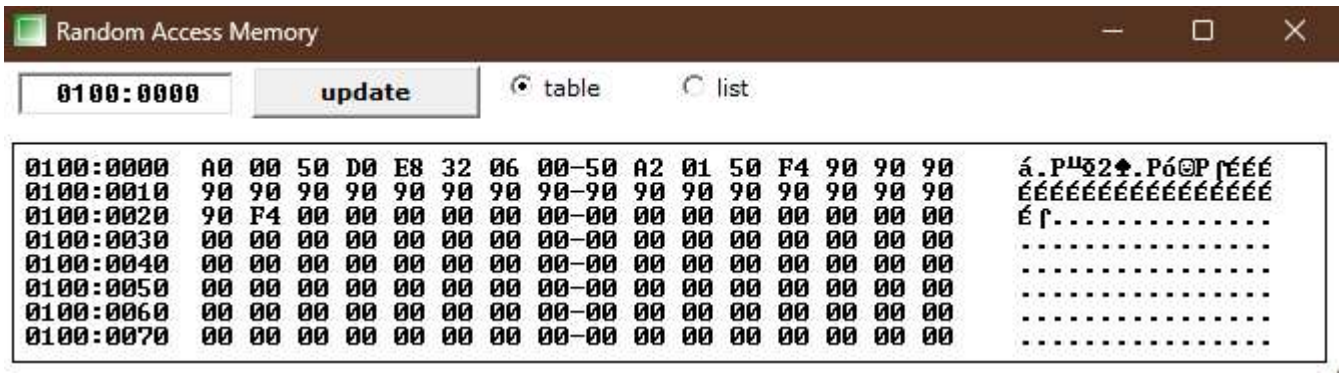

original source co...
;NAME: PRAMATHESH KUMAR
;REDG NO.: 2341019043
MOV SI, 5000H
MOV AL, [SI]
INC SI
MOV BL, [SI]
MOV CL, AL
ROR AL, 04H
INC SI
MOV [SI], AL
MOV AL, CL
AND AL, BL
XOR CL, BL
OR AL, CL
INC SI
MOV [SI], AL
HLT
emulator: noname.bin_
file math debug view external virtual devices virtual drive help
Load
reload
step back
single step
run
step delay ms: 0
registers
H L
AX 00 00
BX 00 00
CX 00 00
DX 00 00
CS 0100
IP 0000
SS 0100
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0100
ES 0100
0100:0027
01027: 90 144 É
01028: 90 144 É
01029: 90 144 É
0102A: 90 144 É
0102B: 90 144 É
0102C: 90 144 É
0102D: 90 144 É
0102E: 90 144 É
0102F: 90 144 É
01030: 90 144 É
01031: 90 144 É
01032: 90 144 É
01033: 90 144 É
01034: 90 144 É
01035: F4 244 ⌠
01036: 00 000 NULL
01037: 00 000 NULL
01038: 00 000 NULL
01039: 00 000 NULL
0103A: 00 000 NULL
0103B: 00 000 NULL
0103C: 00 000 NULL
0103D: 00 000 NULL
0103E: 00 000 NULL
0103F: 00 000 NULL
01040: 00 000 NULL
01041: 00 000 NULL
01042: 00 000 NULL
01043: 00 000 NULL
01044: 00 000 NULL
01045: 00 000 NULL
01046: 00 000 NULL
01047: 00 000 NULL
01048: 00 000 NULL
01049: 00 000 NULL
0104A: 00 000 NULL
0104B: 00 000 NULL
0104C: 00 000 NULL
0100:0000
MOV SI, 05000h
MOV AL, [SI]
INC SI
MOV BL, [SI]
MOV CL, AL
ROR AL, 1
ROR AL, 1
ROR AL, 1
ROR AL, 1
INC SI
MOV [SI], AL
MOV AL, CL
AND AL, BL
XOR CL, BL
OR AL, CL
INC SI
MOV [SI], AL
HLT
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
...
screen source reset aux vars debug stack flags

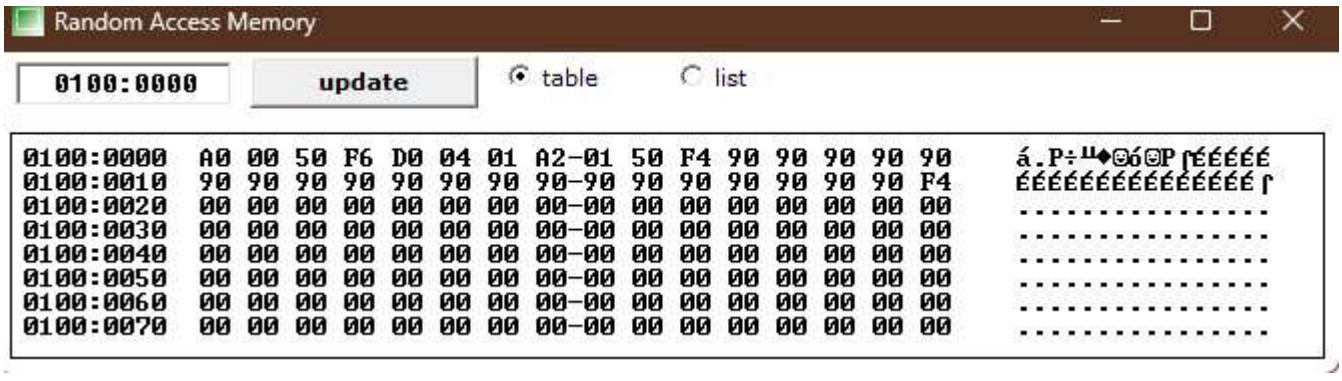

original source co...
;NAME: PRAMATHESH KUMAR
;REDG NO.: 2341019043
MOV AL, [5000H]
SHR AL, 01H
XOR AL, [5000H]
MOV [5001H], AL
HLT
emulator: noname.bin_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
H L
AX 00 00
BX 00 00
CX 00 00
DX 00 00
CS 0100
IP 0000
SS 0100
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0100
ES 0100
0100:0000
0100:0000
01000: A0 160 á
01001: 00 000 NULL
01002: 50 080 P
01003: D0 208 ╨
01004: E8 232 Φ
01005: 32 050 2
01006: 06 006 ♠
01007: 00 000 NULL
01008: 50 080 P
01009: A2 162 ó
0100A: 01 001 ☺
0100B: 50 080 P
0100C: F4 244 ⌠
0100D: 90 144 É
0100E: 90 144 É
0100F: 90 144 É
01010: 90 144 É
01011: 90 144 É
01012: 90 144 É
01013: 90 144 É
01014: 90 144 É
01015: 90 144 É
01016: 90 144 É
01017: 90 144 É
01018: 90 144 É
01019: 90 144 É
0101A: 90 144 É
0101B: 90 144 É
0101C: 90 144 É
0101D: 90 144 É
0101E: 90 144 É
0101F: 90 144 É
01020: 90 144 É
01021: F4 244 ⌠
01022: 00 000 NULL
01023: 00 000 NULL
01024: 00 000 NULL
01025: 00 000 NULL
MOV AL, [05000h]
SHR AL, 1
XOR AL, [05000h]
MOV [05001h], AL
HLT
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
HLT
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
...
screen source reset aux vars debug stack flags


original source co...
;NAME: PRAMATHESH KUMAR
;REDG NO.: 2341019043
MOV AL, [5000H]
NOT AL
ADD AL, 01H
MOV [5001H], AL
HLT
emulator: noname.bin_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
H L
AX 00 00
BX 00 00
CX 00 00
DX 00 00
CS 0100
IP 0000
SS 0100
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0100
ES 0100
0100:0000
0100:0000
01000: A0 160 á
01001: 00 000 NULL
01002: 50 080 P
01003: F6 246 ÷
01004: D0 208 ╨
01005: 04 004 ♦
01006: 01 001 ☺
01007: A2 162 ó
01008: 01 001 ☺
01009: 50 080 P
0100A: F4 244 ⌠
0100B: 90 144 É
0100C: 90 144 É
0100D: 90 144 É
0100E: 90 144 É
0100F: 90 144 É
01010: 90 144 É
01011: 90 144 É
01012: 90 144 É
01013: 90 144 É
01014: 90 144 É
01015: 90 144 É
01016: 90 144 É
01017: 90 144 É
01018: 90 144 É
01019: 90 144 É
0101A: 90 144 É
0101B: 90 144 É
0101C: 90 144 É
0101D: 90 144 É
0101E: 90 144 É
0101F: F4 244 ⌠
01020: 00 000 NULL
01021: 00 000 NULL
01022: 00 000 NULL
01023: 00 000 NULL
01024: 00 000 NULL
01025: 00 000 NULL
MOV AL, [05000h]
NOT AL
ADD AL, 01h
MOV [05001h], AL
HLT
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
HLT
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
ADD [BX + SI], AL
...
screen source reset aux vars debug stack flags


Random Access Memory
0100:0000
update
table
list
0100:0000 BB 14 00 B8 1F 00 8B C8-05 14 00 A3 00 50 8B C1
0100:0010 2D 14 00 A3 02 50 8B C1-F7 E3 89 16 04 50 A3 06
0100:0020 50 8B C1 BA 00 00 F7 F3-A3 08 50 89 16 0A 50 F4
0100:0030 90 90 90 90 90 90 90 90-90 90 90 90 90 90 90 90
0100:0040 90 90 90 90 F4 00 00 00-00 00 00 00 00 00 00 00
0100:0050 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00
0100:0060 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00
0100:0070 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00


Random Access Memory
0100:0000
update
table
list
0100:0000 BE 00 50 8A 04 46 8A 1C-8A C8 D0 C8 D0 C8 D0 C8
0100:0010 D0 C8 46 88 04 8A C1 22-C3 32 CB 0A C1 46 88 04
0100:0020 F4 90 90 90 90 90 90 90-90 90 90 90 90 90 90 90
0100:0030 90 90 90 90 90 F4 00 00-00 00 00 00 00 00 00 00
0100:0040 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00
0100:0050 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00
0100:0060 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00
0100:0070 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00


Random Access Memory
0100:0000
update
table
list
0100:0000 A0 00 50 D0 E8 32 06 00-50 A2 01 50 F4 90 90 90
0100:0010 90 90 90 90 90 90 90 90-90 90 90 90 90 90 90 90
0100:0020 90 F4 00 00 00 00 00 00-00 00 00 00 00 00 00 00
0100:0030 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00
0100:0040 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00
0100:0050 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00
0100:0060 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00
0100:0070 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00


Random Access Memory
0100:0000
update
table
list
0100:0000 A0 00 50 F6 D0 04 01 A2-01 50 F4 90 90 90 90 90
0100:0010 90 90 90 90 90 90 90 90-90 90 90 90 90 90 90 F4
0100:0020 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00
0100:0030 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00
0100:0040 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00
0100:0050 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00
0100:0060 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00
0100:0070 00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00