

#### Shri Vile Parle Kelavani Mandal's

## DWARKADAS J. SANGHVI COLLEGE OF ENGINEERING



(Autonomous College Affiliated to the University of Mumbai) NAAC Accredited with "A" Grade (CGPA: 3.18)

Academic Year: 2022-2023

Name:	Prerna Sunil Jadhav
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Class:	T. Y. B.Tech (Computer Engineering)
Course:	Processor Organization and Architecture (POA)
Course Code:	DJ19CEL502
Experiment No.:	10

**AIM:** Assembly program for DOS Interrupt.

## CODE:

DATA SEGMENT MSG DB

MSG DB "Enter a character: \$"

data ends

**CODE SEGMENT** 

assume cs: code, ds: data

start:

MOV AX, data

MOV DS, AX

lea DX, MSG

MOV AH, 09h

INT 21H

MOV AH, 01

int 21h

mov dl, al

mov ah, 02

int 21h

mov ah, 4ch

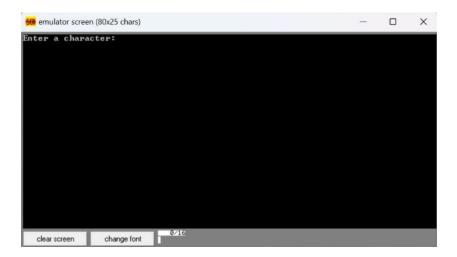
int 21h

**CODE ENDS** 

**END START** 

## OUTPUT:

Displaying a String:





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