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## EXPERIMENT 4

AIM: Write a program to implement Error

Detection and correction mechanism

(i) Hamming code    ii) CRC

### THEORY:

#### ERROR DETECTION

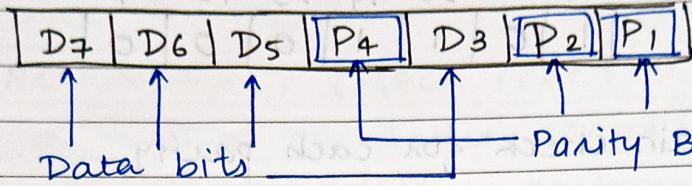
- Error is a condition when the receiver's information doesn't match with sender's info.
- whenever a message is transmitted, it may get scrambled by noise or data may get corrupted.
- To avoid this, error detecting codes are added to help us detect if any error occurred
- Basic approach used is the use of the redundancy bits, where additional bits are added to detect errors. Some popular ones are:  
1. CRC , 2. Checksum , 3. simple parity check etc.

## ERROR CORRECTION

- Error correction codes are used to detect and correct the errors when data is transmitted
- For connecting the error, one has to know the exact position of the error. To achieve this, we have to add some additional bits
- Eg: Hamming code

## HAMMING CODE

- This technique is capable of detecting and correcting errors that occur in the data
- In this technique, Parity bits are used. The number of redundant bits can be calculated using:  $2^r \geq m+r+1$  where,  $r$  is the redundant bit,  $m$  = data bit
- Suppose the no. of data bits is 7 ( $m=7$ ), then  $r=4$  i.e., Parity bits = 4, since  $2^4 \geq 7+4+1$ .
- There are 2 types of parity bits
  1. Even parity bits: In this case, if the count of 1's is odd the parity value is set to 1, making the total count of 1's even.
  2. Odd parity bit: In this case, if the count of 1's is even, the parity value is set to 1, making odd no. of 1's.
- For eg; if we have length of data = 7
  - 1) No. of databits = 7
  - 2) Redundant bits = 4
  - 3) Position of Redundant bits =  $\frac{1}{1}, \frac{1}{2}, \frac{2}{4}, \frac{3}{8}$



For Eg: Data given: 1110, even parity

1	1	1	P4	0	P2	P1
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To find  $P_1$ :

P1	D3	D5	D7
0	0	1	1

← Because  $D_5, D_7$  has 1  
so we can put  $P_1, 0$

To find  $P_2$ :

P2	D3	D6	D7
0	0	1	1

← same as above reasoning

To find  $P_4$ :

P4	D5	D6	D7
1	1	1	1

← To make even no. of 1's  
 $P_4 \Rightarrow 1$

Now, our data to be transmitted is -

1	1	1	1	0	0	0
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Suppose that  $D_6$  is flipped, now the data to be transmitted will be -

D <sub>7</sub>	D <sub>6</sub>	D <sub>5</sub>	P <sub>4</sub>	D <sub>3</sub>	P <sub>2</sub>	P <sub>1</sub>
1	0	1	1	0	0	0

Now again check for each parity,

For P<sub>1</sub>,

P <sub>1</sub>	D <sub>3</sub>	D <sub>5</sub>	D <sub>7</sub>
0	0	1	1

→ All good, even no. of 1's

For P<sub>2</sub>,

P <sub>2</sub>	D <sub>3</sub>	D <sub>6</sub>	D <sub>7</sub>
0	0	0	1

If any error put parity 1  
→ Here there should be even 1's but there are odd 1's present

For P<sub>4</sub>,

P <sub>4</sub>	D <sub>5</sub>	D <sub>6</sub>	D <sub>7</sub>
1	1	0	1

→ Same error as above

No change for P<sub>1</sub>, but P<sub>2</sub> → 1 and P<sub>4</sub> → 0

∴ (P<sub>4</sub>, P<sub>2</sub>, P<sub>1</sub>) = (0, 1, 0) i.e., 6th bit has error and we need to correct it

∴ The corrected data is

D <sub>7</sub>	D <sub>6</sub>	D <sub>5</sub>	P <sub>4</sub>	D <sub>3</sub>	P <sub>2</sub>	P <sub>1</sub>
1	1	1	1	0	0	0

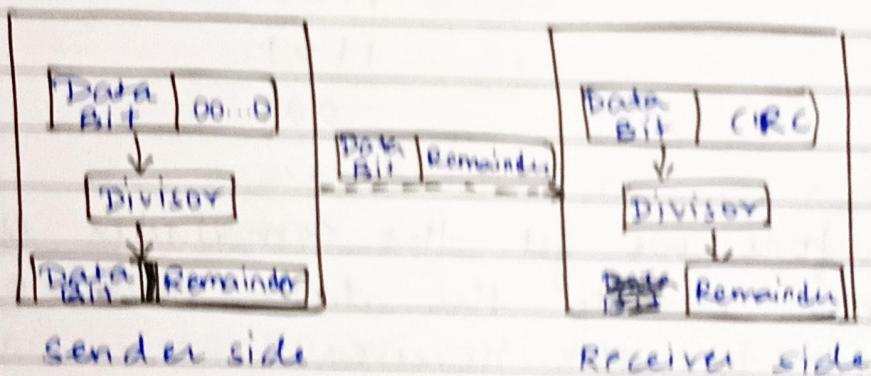
## CYCLED REDUNDANCY CHECK (CRC)

- \* It is a method to detect errors in the data.
- \* This is performed by performing a binary division on the transmitted data at the sender's side and verifying the same at the receiver's side.
- \* For eg:

Given:  $x^3 + x + 1$  (polynomial eqn)

Convert it into binary format  
[111]

- \* Working



- \* If data to be sent = 100100 / key = 1101 then we would append  $(n-1)$  zeros to the data where  $n$  is the length of key

⇒ 100100 000

- \* Now we would perform binary division operation of 100100000 using 1101 as divisor.

we get,

$$\begin{array}{r} 111101 \\ 1101 ) 100100\ 000 \\ \underline{1101} \downarrow \quad | \quad | \quad | \\ 1000 \\ 1101 \downarrow \quad | \quad | \quad | \\ 1010 \\ 1101 \downarrow \quad | \quad | \quad | \\ 1110 \\ 1101 \downarrow \quad | \quad | \quad | \\ 0110 \\ 0000 \\ \hline 1100 \\ 1101 \\ \hline 001 \end{array}$$

- Therefore we get the remainder 001 and hence encode the data 100100001 and send to the receiver
- Assuming there was some error in the data transmission and the received data was 10000001.
- Now at receivers end we would again perform binary division. to check for error and then correct it if any.

$$\begin{array}{r}
 111010 \\
 \hline
 1101 ) 100000001 \\
 1101 \downarrow \quad | \quad | \quad | \\
 1010 \\
 1101 \downarrow \\
 1110 \\
 1101 \downarrow \\
 0110 \\
 0000 \downarrow \\
 1100 \\
 1101 \downarrow \\
 0011 \\
 0000 \\
 \hline
 011
 \end{array}$$

- Since the remainder is not all zero, the error is detected.

WHY CRC IS ADDED AT TRAILER OF THE FRAME?

- Having CRC in the trailer helps to compute the CRC while transmitting the packet and then split the final CRC at the end
- If we put CRC in the header, it will handle each byte twice, once for checksum and once for transmitting
- Placing the CRC at the end of a frame reduces packet latency and reduces h/w buffering requirement.

## CONCLUSION

Error detection and correction mechanism  
are studied and implemented.

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