

DWARKADAS J. SANGHVI COLLEGE OF ENGINEERING



(Autonomous College Affiliated to the University of Mumbai) NAAC Accredited with "A" Grade (CGPA: 3.18)

Academic Year: 2022-2023

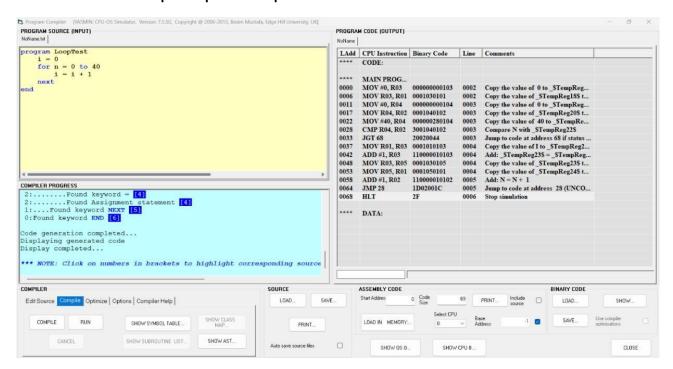
Name:	Prerna Sunil Jadhav	
Sap Id:	60004220127	
Class:	S. Y. B.Tech (Computer Engineering)	
Course:	Operating System Laboratory	
Course Code:	DJ19CEL403	
Experiment No.: 07		

USING THE CPU-OS SIMULATOR TO ANALYZE AND SYNTHESIZE THE FOLLOWING: AIM:

- A. PROCESS SCHEDULING ALGORITHMS.
- B. THREAD CREATION AND SYNCHRONIZATION.
- C. DEADLOCK PREVENTION AND AVOIDANCE.

A. PROCESS SCHEDULING ALGORITHMS.

a. Compiled a process loop test





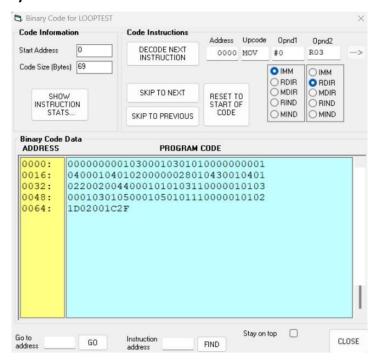
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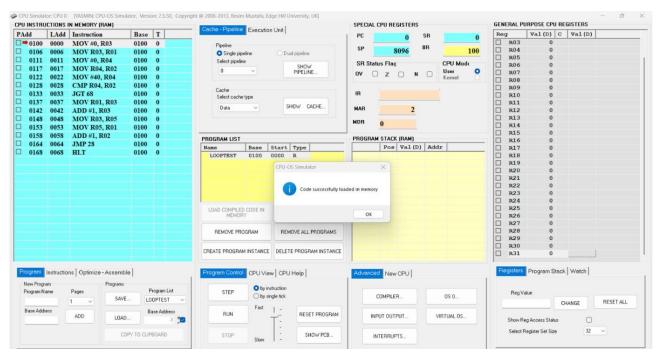


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b. Binary Code



c. Loaded into memory





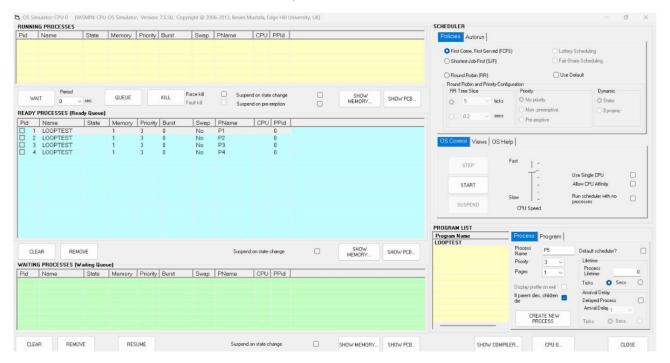
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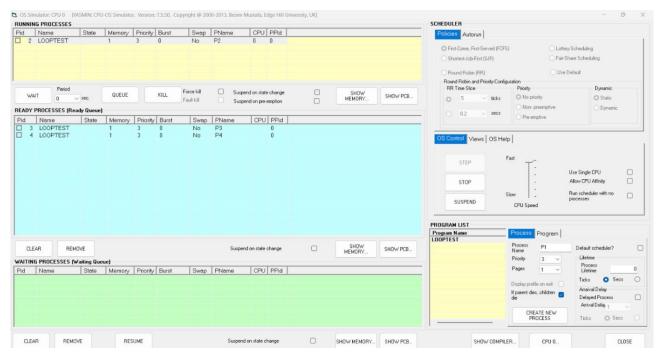


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d. Created 4 instances of the process



e. FCFS scheduling





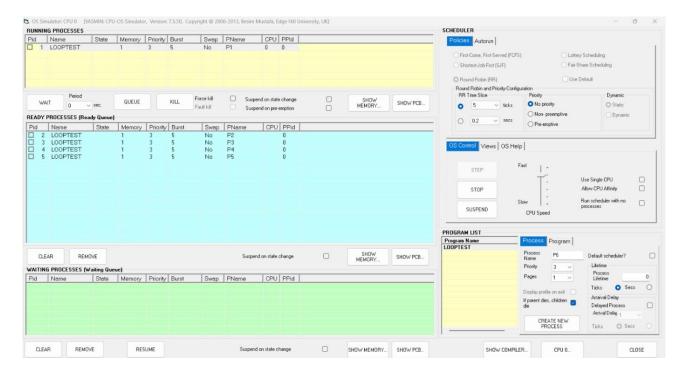
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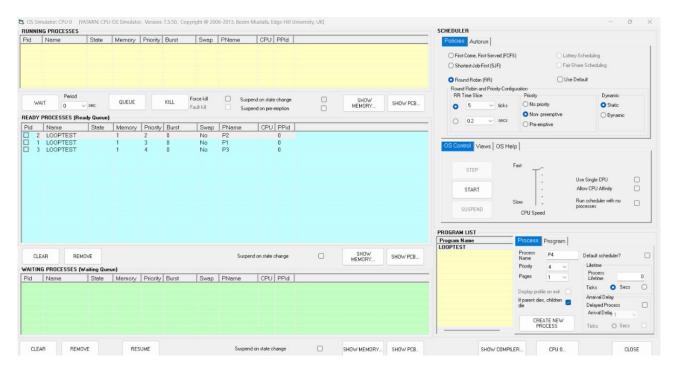


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f. Round Robin Scheduling with no priority



g. Round Robin with priority and non preemptive





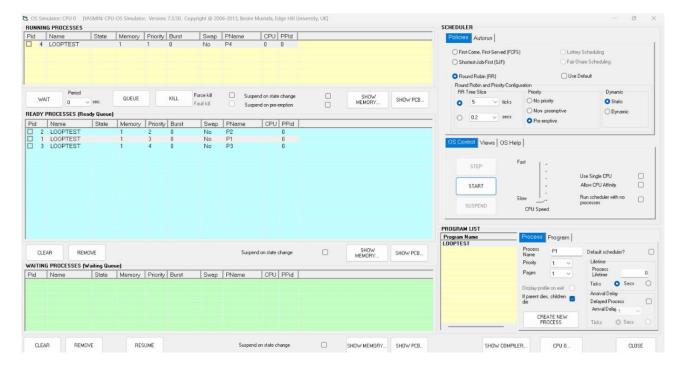
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h. Round Robin with priority and preemptive



Observation:

- FCFS (First-Come-First-Serve), Non-Preemptive Round Robin, and Preemptive Round Robin are CPU scheduling algorithms.
- ♣ FCFS is simple but may cause long waiting times.
- ♣ Non- Preemptive Round Robin has fixed time slices but may waste CPU time.
- Preemptive Round Robin provides fair CPU utilization and



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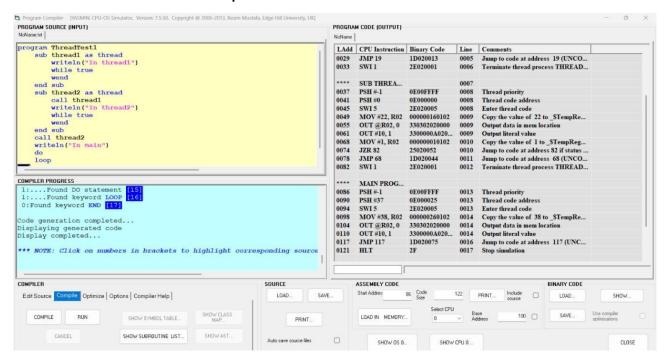
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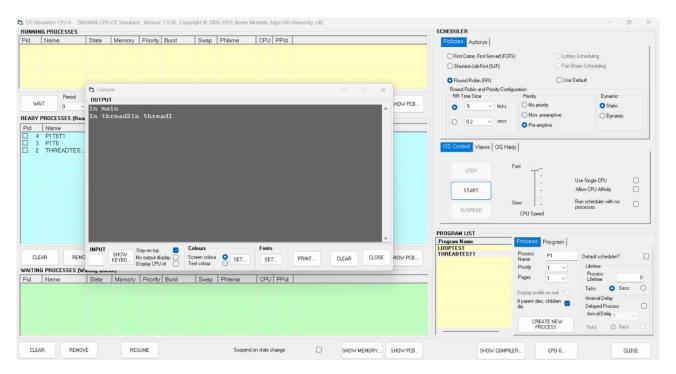
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B. Thread creation and synchronization.

a. Loaded and Compiled Thread Test 1



b. Console Output





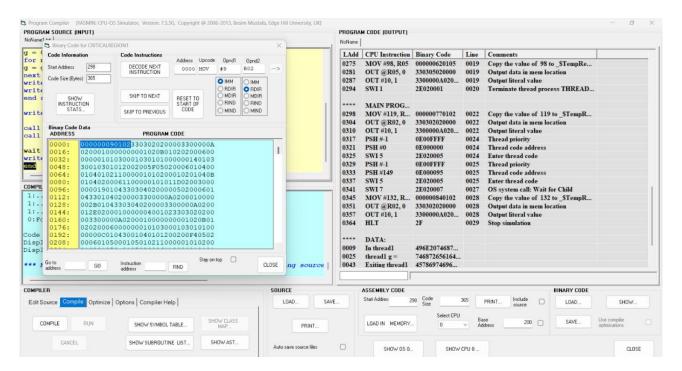
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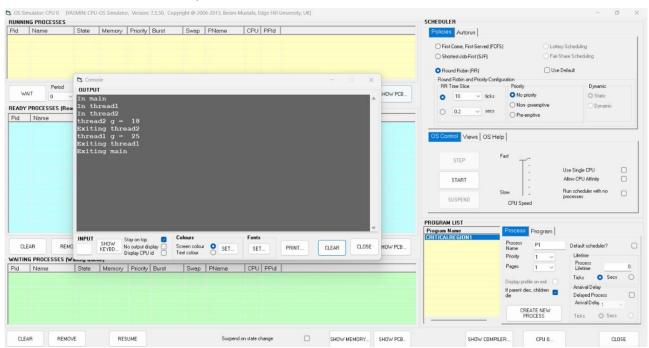


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c. Loaded and Compiled CriticalRegion1



d. Console Output





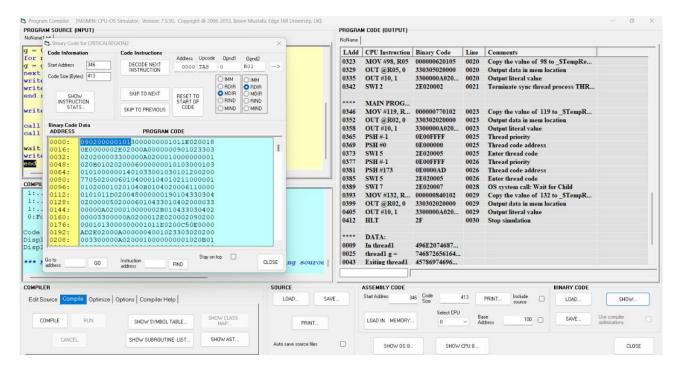
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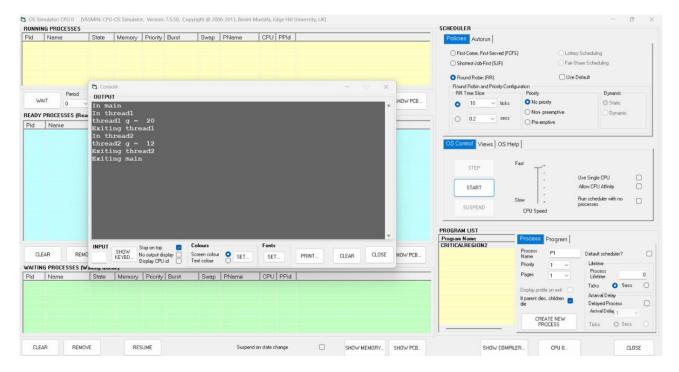
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e. Loaded and Compiled CriticalRegion2



f. Console Output





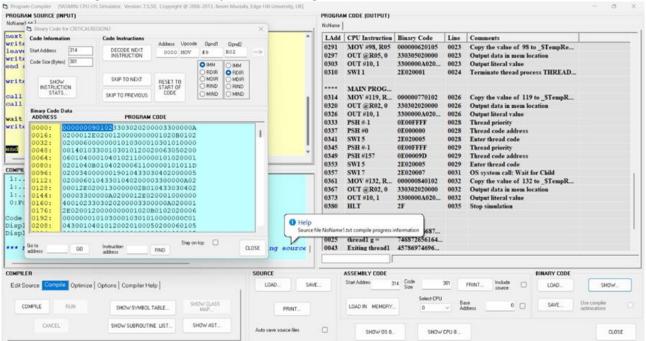
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g. Loaded and Compiled CriticalRegion3

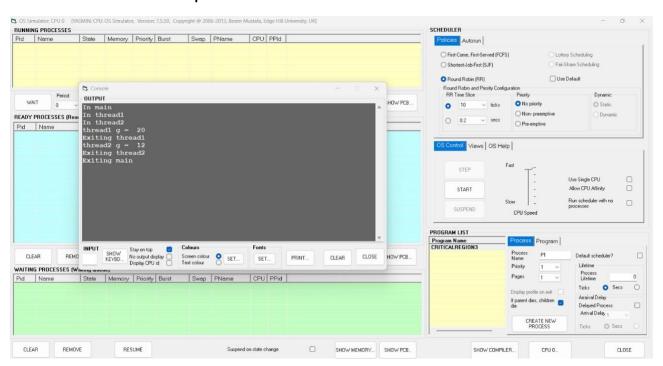


ENTER keyword Assembly code

LAdd	CPU Instruction	Binary Code	Line	Comments	
0019	SWI 18	2E020012	0005	OS system call: Enter critical region	
LEAVE keyword Assembly code					

0130 SWI 19 2E020013 0011 OS system call: Exit critical region

h. Console Output





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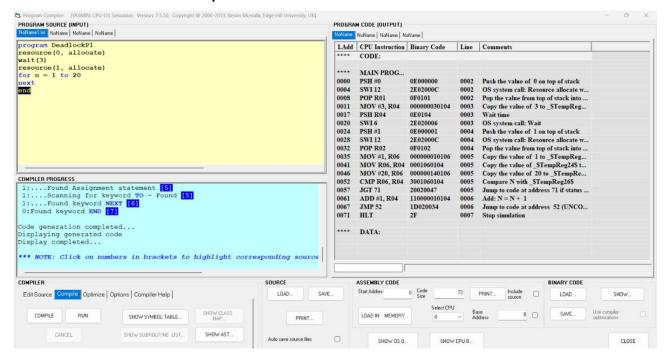




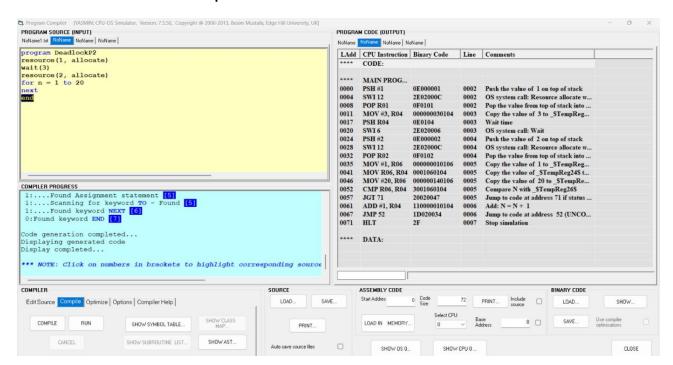
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C. Deadlock prevention and avoidance.

a. Loaded and Compiled P1



b. Loaded and Compiled P2





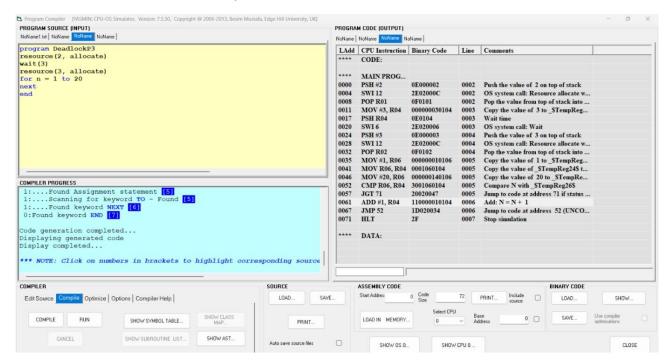
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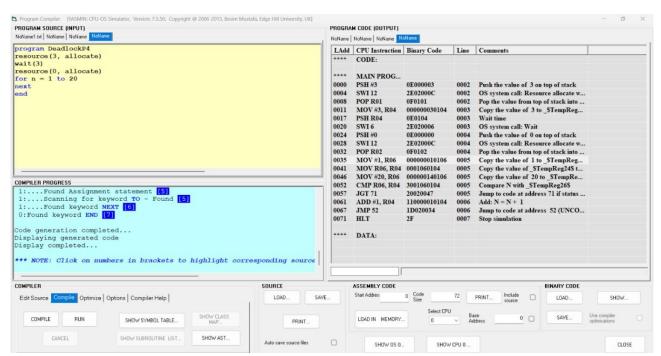


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c. Loaded and Compiled P3

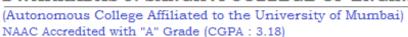


d. Loaded and Compiled P4





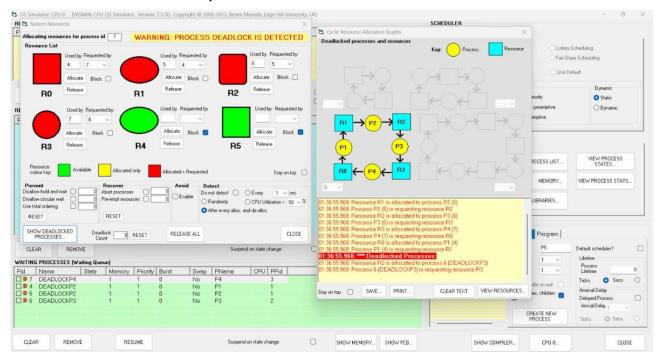
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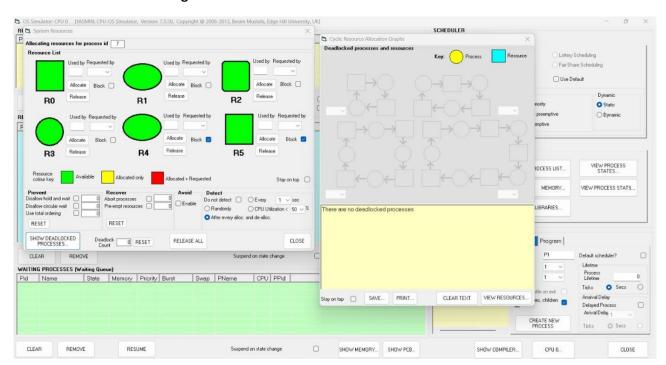


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e. Deadlock Graph



f. Releasing a resource





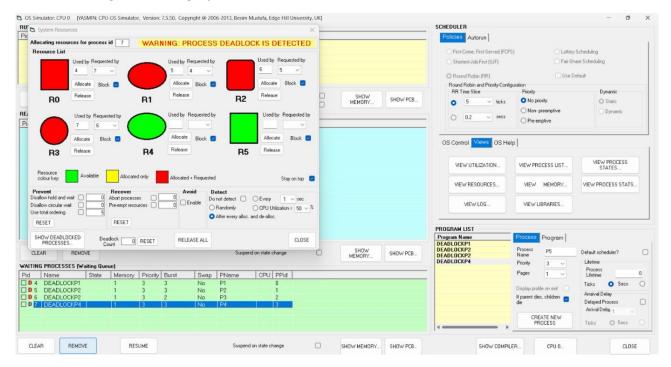
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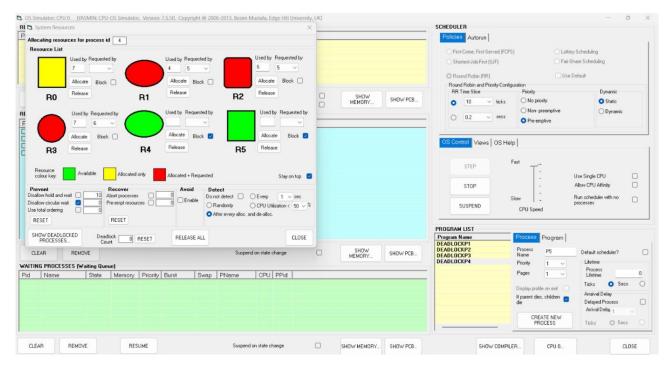
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g. Removing a process



h. Disallow hold and wait





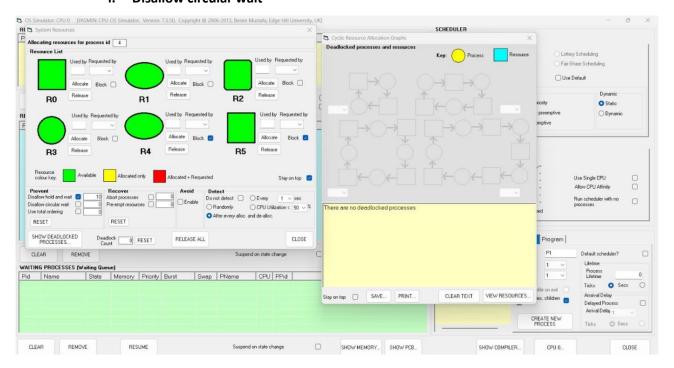
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i. Disallow circular wait



j. Total Ordering

