R10\_R20 <=10\_20

M10\_M20 <=16\_26

LW格式：“Ri Rj imm”表示mem[Ri+imm]->Rj

伪指令 结果

LW R10 R11 1 R10<=18 000111 01010 01011 0000000000000001

LW R12 R13 1 R12<=20 000111 01100 01101 0000000000000001

ADD R14 R12 R12 R14<=40 000000 01110 01100 01100 00000000000

LW R10 R11 2 R10<=19 000111 01010 01011 0000000000000010

LW R12 R13 2 R12<=21 000111 01100 01101 0000000000000010

ADD R14 R12 R10 R14<=40 000000 01110 01100 01010 00000000000

LW R10 R11 3 R10<=20 000111 01010 01011 0000000000000011

LW R12 R13 3 R12<=22 000111 01100 01101 0000000000000011

ADD R14 R10 R12 R14<=42 000000 01110 01010 01100 00000000000

LW R10 R11 4 R10<=21 000111 01010 01011 0000000000000100

LW R12 R13 4 R12<=23 000111 01100 01101 0000000000000100

ADD R14 R10 R10 R14<=42 000000 01110 01010 01010 00000000000

ADD R15 R16 R17 R15<=33 000000 01111 10000 10001 00000000000

ADD R18 R19 R20 R18<=39 000000 10010 10011 10100 00000000000

ADD R15 R18 R18 R15<=78 000000 01111 10010 10010 00000000000

ADD R15 R16 R17 R15<=33 000000 01111 10000 10001 00000000000

ADD R18 R19 R20 R18<=39 000000 10010 10011 10100 00000000000

ADD R15 R18 R15 R15<=72 000000 01111 10010 01111 00000000000

ADD R15 R16 R17 R15<=33 000000 01111 10000 10001 00000000000

ADD R18 R19 R20 R18<=39 000000 10010 10011 10100 00000000000

ADD R18 R15 R18 R18<=72 000000 10010 01111 10010 00000000000

ADD R15 R16 R17 R15<=33 000000 01111 10000 10001 00000000000

ADD R18 R19 R20 R18<=39 000000 10010 10010 10100 00000000000

ADD R18 R15 R15 R18<=66 000000 10010 01111 01111 00000000000

32bits

00011101010010110000000000000001

00011101100011010000000000000001

00000001110011000110000000000000

00011101010010110000000000000010

00011101100011010000000000000010

00000001110011000101000000000000

00011101010010110000000000000011

00011101100011010000000000000011

00000001110010100110000000000000

00011101010010110000000000000100

00011101100011010000000000000100

00000001110010100101000000000000

00000001111100001000100000000000

00000010010100111010000000000000

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00000001111100100111100000000000

00000001111100001000100000000000

00000010010100111010000000000000

00000010010011111001000000000000

00000001111100001000100000000000

00000010010100101010000000000000

00000010010011110111100000000000

8bits

00011101

01001011

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00000001

00011101

10001101

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01010010

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