R10\_R20 <=10\_20

LW格式：“Ri Rj imm”表示mem[Ri+imm]->Rj

伪指令 结果

LW R10 R11 1 R10<=18 000111 01010 01011 0000000000000001

ADD R12 R13 R14 R12<=27 000000 01100 01101 01110 00000000000

ADD R15 R12 R12 R15<=54 000000 01111 01100 01100 00000000000

LW R10 R11 2 R10<=19 000111 01010 01011 0000000000000010

ADD R12 R13 R14 R12<=27 000000 01100 01101 01110 00000000000

ADD R15 R12 R10 R15<=46 000000 01111 01100 01010 00000000000

LW R10 R11 3 R10<=20 000111 01010 01011 0000000000000011

ADD R12 R13 R14 R12<=27 000000 01100 01101 01110 00000000000

ADD R15 R10 R12 R15<=47 000000 01111 01010 01100 00000000000

LW R10 R11 4 R10<=21 000111 01010 01011 0000000000000100

ADD R12 R13 R14 R12<=27 000000 01100 01101 01110 00000000000

ADD R15 R10 R10 R15<=42 000000 01111 01010 01010 00000000000

ADD R16 R17 R18 R16<=35 000000 10000 10001 10010 00000000000

LW R19 R20 1 R19<=33 000111 10011 10100 0000000000000001

ADD R10 R19 R19 R10<=66 000000 01010 10011 10011 00000000000

ADD R16 R17 R18 R16<=35 000000 10000 10001 10010 00000000000

LW R19 R20 2 R19<=34 000111 10011 10100 0000000000000010

ADD R10 R19 R16 R10<=69 000000 01010 10011 10000 00000000000

ADD R16 R17 R18 R16<=35 000000 10000 10001 10010 00000000000

LW R19 R20 3 R19<=35 000111 10011 10100 0000000000000011

ADD R10 R16 R19 R10<=70 000000 01010 10000 10011 00000000000

ADD R16 R17 R18 R16<=35 000000 10000 10001 10010 00000000000

LW R19 R20 4 R19<=36 000111 10011 10100 0000000000000100

ADD R10 R16 R16 R10<=70 000000 01010 10000 10000 00000000000

32bits

00011101010010110000000000000001

00000001100011010111000000000000

00000001111011000110000000000000

00011101010010110000000000000010

00000001100011010111000000000000

00000001111011000101000000000000

00011101010010110000000000000011

00000001100011010111000000000000

00000001111010100110000000000000

00011101010010110000000000000100

00000001100011010111000000000000

00000001111010100101000000000000

00000010000100011001000000000000

00011110011101000000000000000001

00000001010100111001100000000000

00000010000100011001000000000000

00011110011101000000000000000010

00000001010100111000000000000000

00000010000100011001000000000000

00011110011101000000000000000011

00000001010100001001100000000000

00000010000100011001000000000000

00011110011101000000000000000100

00000001010100001000000000000000

8bits

00011101

01001011

00000000

00000001

00000001

10001101

01110000

00000000

00000001

11101100

01100000

00000000

00011101

01001011

00000000

00000010

00000001

10001101

01110000

00000000

00000001

11101100

01010000

00000000

00011101

01001011

00000000

00000011

00000001

10001101

01110000

00000000

00000001

11101010

01100000

00000000

00011101

01001011

00000000

00000100

00000001

10001101

01110000

00000000

00000001

11101010

01010000

00000000

00000010

00010001

10010000

00000000

00011110

01110100

00000000

00000001

00000001

01010011

10011000

00000000

00000010

00010001

10010000

00000000

00011110

01110100

00000000

00000010

00000001

01010011

10000000

00000000

00000010

00010001

10010000

00000000

00011110

01110100

00000000

00000011

00000001

01010000

10011000

00000000

00000010

00010001

10010000

00000000

00011110

01110100

00000000

00000100

00000001

01010000

10000000

00000000