Number	Name	Description	I/O Type
1	DOWNBOND	Connect to ground	GND
2	DGND	Digital ground. Connect to ground	GND
3	LE_OUT[2]	Leading edge output bit 2	Output
4	LE_OUT[1]	Leading edge output bit 1	Output
5	LE_OUT[0]	Leading edge output bit 0	Output
6	AD[7]	Addr/Mode and Data bit 7. Addr/Mode is latched on rising edge of the STB, while data is latched on falling edge.	Bi-Directional
7	AD[6]	Addr/Mode and Data bit 6. Addr/Mode is latched on rising edge of the STB, while data is latched on falling edge.	Bi-Directional
8	AD[5]	Addr/Mode and Data bit 5. Addr/Mode is latched on rising edge of the STB, while data is latched on falling edge.	Bi-Directional
9	AD[4]	Addr/Mode and Data bit 4. Addr/Mode is latched on rising edge of the STB, while data is latched on falling edge.	Bi-Directional
10	AD[3]	Addr/Mode and Data bit 3. Addr/Mode is latched on rising edge of the STB, while data is latched on falling edge.	Bi-Directional
11	AD[2]	Addr/Mode and Data bit 2. Addr/Mode is latched on rising edge of the STB, while data is latched on falling edge.	Bi-Directional
12	AD[1]	Addr/Mode and Data bit 1. Addr/Mode is latched on rising edge of the STB, while data is latched on falling edge.	Bi-Directional
13	AD[0]	Addr/Mode and Data bit 0. Addr/Mode is latched on rising edge of the STB, while data is latched on falling edge.	Bi-Directional
14	AVSS (COMMON)	Connect to ground. This is for the common channel	GND
15	AVDD(COMMON)	Connect to 3.3 VDC. This is for the common channel	POWER
16	AVSS(CH4-7)	Connect to GND.	
17	AVDD(CH4-7)	Connect to 3.3 VDC.	
18	AVSS(CH0-3)	Connect to GND.	
19	AVDD(CH0-3)		
20	DOWNBOND		
21	AGND		
22	AIN[0]		
23	AIN[1]		
24	AIN[2]		

25 AIN[3] 26 AIN[4] 27 AIN[6] 28 AIN[6] 29 AIN[7] 30 AVSS (PADS) 31 AVDD(PADS) 32 AIN[8] 33 AIN[9] 34 AIN[10] 35 AIN[11] 36 AIN[12] 37 AIN[13] 38 AIN[14] 39 AIN[15] 40 MULTIPLICITY 41 DOWNBOND 42 NC 43 AGND 44 AVDD(CH12-15) 45 AVSS(CH12-15) 46 AVDS(CH8-11) 47 AVSS(CH8-11) 48 NEG_POL 49 STB 50 AGND_DISABLE 51 LE_OUT[6] 54 LE_OUT[6] 55 LE_OUT[7]			
27 AIN[5] 28 AIN[6] 29 AIN[7] 30 AVSS (PADS) 31 AVDD(PADS) 32 AIN[8] 33 AIN[9] 34 AIN[10] 35 AIN[11] 36 AIN[12] 37 AIN[13] 38 AIN[14] 39 AIN[15] 40 MULTIPLICITY 41 DOWNBOND 42 NC 43 AGND 44 AVDD(CH12-15) 45 AVSS(CH12-15) 46 AVDD(CH8-11) 47 AVSS(CH8-11) 48 NEG_POL 49 STB 50 AGND_DISABLE 51 LE_OUT[6] 54 LE_OUT[6]	25	AIN[3]	
28	26	AIN[4]	
29 AIN[7] 30 AVSS (PADS) 31 AVDD(PADS) 32 AIN[8] 33 AIN[9] 34 AIN[10] 35 AIN[11] 36 AIN[12] 37 AIN[13] 38 AIN[14] 39 AIN[15] 40 MULTIPLICITY 41 DOWNBOND 42 NC 43 AGND 44 AVDD(CH12-15) 45 AVSS(CH12-15) 46 AVDD(CH8-11) 47 AVSS(CH8-11) 48 NEG_POL 49 STB 50 AGND_DISABLE 51 LE_OUT[6] 53 LE_OUT[6]	27	AIN[5]	
30 AVSS (PADS) 31 AVDD(PADS) 32 AIN[8] 33 AIN[9] 34 AIN[10] 35 AIN[11] 36 AIN[12] 37 AIN[13] 38 AIN[14] 39 AIN[15] 40 MULTIPLICITY 41 DOWNBOND 42 NC 43 AGND 44 AVDD(CH12-15) 45 AVSS(CH12-15) 46 AVDD(CH8-11) 47 AVSS(CH8-11) 48 NEG_POL 49 STB 50 AGND_ISABLE 51 LE_OUT[6] 53 LE_OUT[6]	28	AIN[6]	
31 AVDD(PADS) 32 AIN[8] 33 AIN[9] 34 AIN[10] 35 AIN[11] 36 AIN[12] 37 AIN[13] 38 AIN[14] 39 AIN[15] 40 MULTIPLICITY 41 DOWNBOND 42 NC 43 AGND 44 AVDD(CH12-15) 45 AVSS(CH12-15) 46 AVDD(CH8-11) 47 AVSS(CH8-11) 48 NEG_POL 49 STB 50 AGND_DISABLE 51 LE_OUT[3] 52 LE_OUT[4] 53 LE_OUT[6]	29	AIN[7]	
32	30	AVSS (PADS)	
33 AIN[9] 34 AIN[10] 35 AIN[11] 36 AIN[12] 37 AIN[13] 38 AIN[14] 39 AIN[15] 40 MULTIPLICITY 41 DOWNBOND 42 NC 43 AGND 44 AVDD(CH12-15) 45 AVSS(CH12-15) 46 AVDD(CH8-11) 47 AVSS(CH8-11) 48 NEG_POL 49 STB 50 AGND_DISABLE 51 LE_OUT[3] 52 LE_OUT[6] 54 LE_OUT[6]	31	AVDD(PADS)	
34	32	AIN[8]	
35	33	AIN[9]	
36	34	AIN[10]	
37 AIN[13] 38 AIN[14] 39 AIN[15] 40 MULTIPLICITY 41 DOWNBOND 42 NC 43 AGND 44 AVDD(CH12-15) 45 AVSS(CH12-15) 46 AVDD(CH8-11) 47 AVSS(CH8-11) 48 NEG_POL 49 STB 50 AGND_DISABLE 51 LE_OUT[3] 52 LE_OUT[4] 53 LE_OUT[6]	35	AIN[11]	
38	36	AIN[12]	
39	37	AIN[13]	
40 MULTIPLICITY 41 DOWNBOND 42 NC 43 AGND 44 AVDD(CH12-15) 45 AVSS(CH12-15) 46 AVDD(CH8-11) 47 AVSS(CH8-11) 48 NEG_POL 49 STB 50 AGND_DISABLE 51 LE_OUT[3] 52 LE_OUT[4] 53 LE_OUT[6]	38	AIN[14]	
41 DOWNBOND 42 NC 43 AGND 44 AVDD(CH12-15) 45 AVSS(CH12-15) 46 AVDD(CH8-11) 47 AVSS(CH8-11) 48 NEG_POL 49 STB 50 AGND_DISABLE 51 LE_OUT[3] 52 LE_OUT[4] 53 LE_OUT[6]	39	AIN[15]	
42 NC 43 AGND 44 AVDD(CH12-15) 45 AVSS(CH12-15) 46 AVDD(CH8-11) 47 AVSS(CH8-11) 48 NEG_POL 49 STB 50 AGND_DISABLE 51 LE_OUT[3] 52 LE_OUT[4] 53 LE_OUT[6]	40	MULTIPLICITY	
43 AGND 44 AVDD(CH12-15) 45 AVSS(CH12-15) 46 AVDD(CH8-11) 47 AVSS(CH8-11) 48 NEG_POL 49 STB 50 AGND_DISABLE 51 LE_OUT[3] 52 LE_OUT[4] 53 LE_OUT[6]	41	DOWNBOND	
44 AVDD(CH12-15) 45 AVSS(CH12-15) 46 AVDD(CH8-11) 47 AVSS(CH8-11) 48 NEG_POL 49 STB 50 AGND_DISABLE 51 LE_OUT[3] 52 LE_OUT[4] 53 LE_OUT[5] 54 LE_OUT[6]	42	NC	
45 AVSS(CH12-15) 46 AVDD(CH8-11) 47 AVSS(CH8-11) 48 NEG_POL 49 STB 50 AGND_DISABLE 51 LE_OUT[3] 52 LE_OUT[4] 53 LE_OUT[5] 54 LE_OUT[6]	43	AGND	
46 AVDD(CH8-11) 47 AVSS(CH8-11) 48 NEG_POL 49 STB 50 AGND_DISABLE 51 LE_OUT[3] 52 LE_OUT[4] 53 LE_OUT[5] 54 LE_OUT[6]	44	AVDD(CH12-15)	
47 AVSS(CH8-11) 48 NEG_POL 49 STB 50 AGND_DISABLE 51 LE_OUT[3] 52 LE_OUT[4] 53 LE_OUT[5] 54 LE_OUT[6]	45	AVSS(CH12-15)	
48 NEG_POL 49 STB 50 AGND_DISABLE 51 LE_OUT[3] 52 LE_OUT[4] 53 LE_OUT[5] 54 LE_OUT[6]	46	AVDD(CH8-11)	
49 STB 50 AGND_DISABLE 51 LE_OUT[3] 52 LE_OUT[4] 53 LE_OUT[5] 54 LE_OUT[6]	47	AVSS(CH8-11)	
50 AGND_DISABLE 51 LE_OUT[3] 52 LE_OUT[4] 53 LE_OUT[5] 54 LE_OUT[6]	48	NEG_POL	
51 LE_OUT[3] 52 LE_OUT[4] 53 LE_OUT[5] 54 LE_OUT[6]	49	STB	
52 LE_OUT[4] 53 LE_OUT[5] 54 LE_OUT[6]	50	AGND_DISABLE	
53 LE_OUT[5] 54 LE_OUT[6]	51	LE_OUT[3]	
54 LE_OUT[6]	52	LE_OUT[4]	
	53	LE_OUT[5]	
	54	LE_OUT[6]	
	55		

		1
GLOBAL_EN		
RST_L		
TESTPOINT		
OR		
DOWNBOND		
DGND		
DVDD		
CFD[8]		
CFD[14]		
CFD[13]		
CFD[12]		
DO[11]		
DO[10]		
DO[9]		
DO[15]		
DO[7]		
DO[6]		
DO[5]		
DO[4]		
DO[3]		
DO[2]		
DO[1]		
DO[0]		
WRITE		
DVDD		
	RST_L TESTPOINT OR DOWNBOND DGND DVDD CFD[8] CFD[14] CFD[13] CFD[12] DO[11] DO[10] DO[9] DO[15] DO[7] DO[6] DO[6] DO[5] DO[4] DO[3] DO[2] DO[1] DO[0] WRITE	RST_L TESTPOINT OR DOWNBOND DGND DVDD CFD[8] CFD[14] CFD[13] CFD[12] DO[11] DO[10] DO[9] DO[15] DO[7] DO[6] DO[5] DO[4] DO[3] DO[3] DO[2] DO[1] DO[2] DO[1] DO[3] DO[2] DO[1] DO[0] WRITE