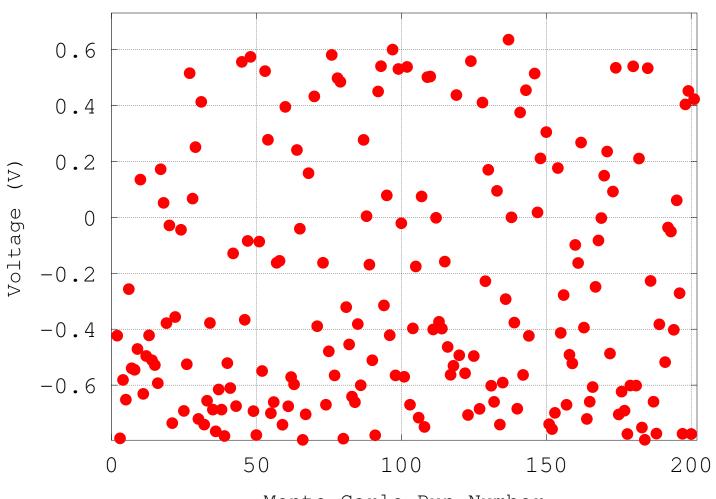
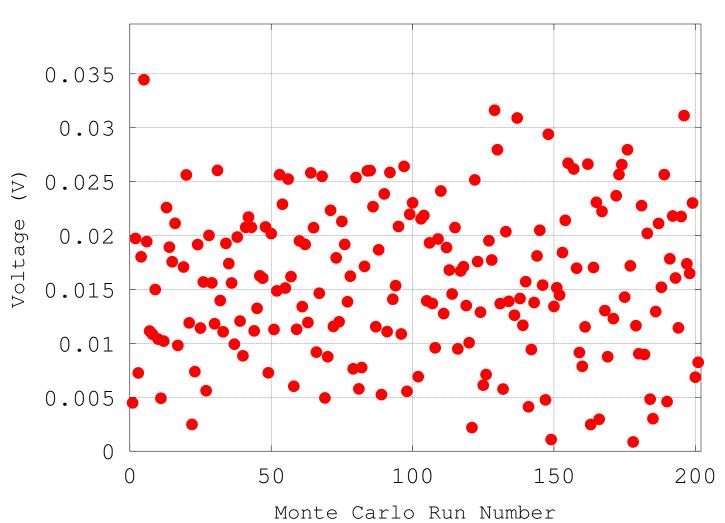
Comparator Differntial Input

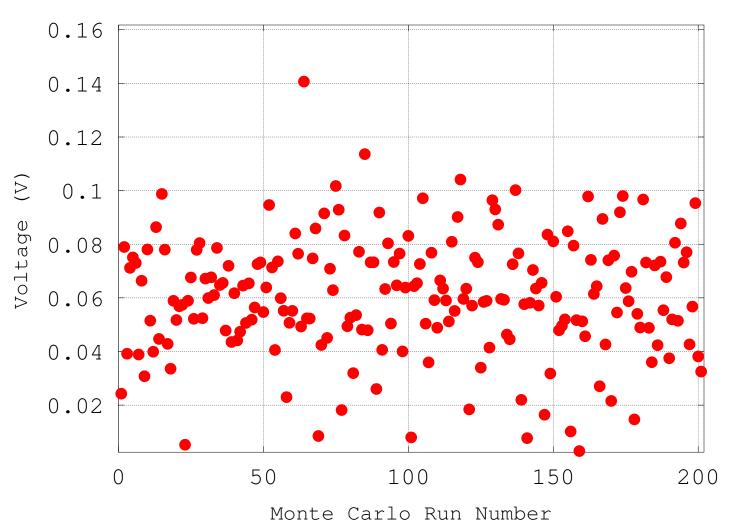


Monte Carlo Run Number

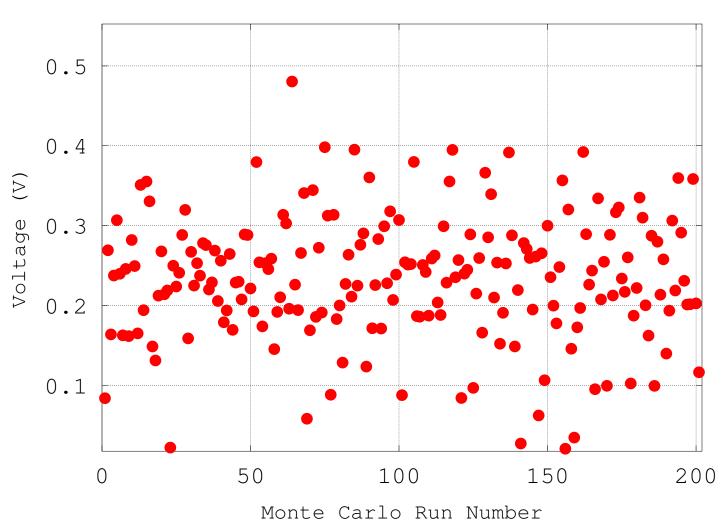
Diff Amp Stage 1 Differntial Output



Diff Amp Stage 2 Differntial Output



Diff Amp Stage 3 Differntial Output



Switcher Differntial Output

