

# Homework #2

## AES implementation

CNS Course Sapienza

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## 1 Homework Goal

This homework contains a basic python implementation of AES algorithm. Throughout the work, it has been chosen to make a compromise: the implementation is in midpoint between complete Galois Field and precomputed look-up tables for every step. Both encryption and decryption are studied and implemented, then the operation modes are implemented and finally it will illustrate a comparison with world known AES implementation.

## 2 Encryption

Before digging into the details of every operation performed, it is necessary to explain how the variables are stored in this implementation: the 4x4 state matrix is treated as single vector of 16 components, from which of course it is possible to build the matrix representation and vice versa, it was only a choice due to initial lack of knowledge of python language; every state block is a byte stored in hex values to simplify computations (as I discovered python stores hex values as int so in practice the bytes blocks are stored as int). Also the key is stored as an array of 16 bytes, and every bytes follows the same philosophy adopted for the state.

The encryption function is *encrypt*, and it is composed by a sequence of the following four big blocks:

1. Byte Substitution
2. Shift Row
3. Mix Columns
4. Key Addition

The following subsections contains implementation details and choices made to build the single blocks.

## 2.1 Byte Substitution

The byte substitution block is build by two functions, one perform the single byte substitution operation (*subByteSingle*), while the other one (*subByte*) performs the byte substitution to all the byte of the state calling the previous function for every single byte. The basic operation of single byte substitution is performing simply by looking to a prestored table which contains all the values for the 256 combinations of 8 bit contained in one byte. This method is more efficient and more simpler to implement with respect to performing all the operations needed in GF(256).

## 2.2 Shift Row

The shift row block is a simple function which perform a left row shift for every row of the state matrix; since the state is sorted as a plain array, it s necessary first to build the row of the matrix from the array, then perform the row shift and finally substitute back in the state array the shifted value. As written in AES standard, the shift is of one byte more very time one new row is processed (first row has no shift while last row has a circular shift of 3 bytes).

## 2.3 Mix Columns

The mix column block is based on an intermediate difficulty of implementation: the base idea is taken from [5], which is the central operation performed inside the *mixcolumns* function. In order to perform it, it is necessary to construct the columns from the array state and in the end rebuild the state vector form the columns. The central operation is based on another function called *xtime*, which together with the xor operations, is able to substitute all the GF operations. The *xtime* function implements the multiplication of the polynomial stored in the byte for x, its idea is taken from [4] and since the algorithm works with a byte implementation, it is necessary to use the final and in the return of the function to truncate the byte after the left shift.

To debug this section, I used the test vectors which can be found in [6].

## 2.4 Key Addition

The key addition layer basically uses 3 functions in this implementation: *keyAddition* perform the xor operation between the state and the subkey (the for is necessary to perform the operation bitwise, since the state is an array); *roundKey* generates the subkeys needed at every iteration of AES while *gFunction* is an auxiliary function needed by *roundKey*.

The *gFunction* corresponds to function *g* of classical AES implementation, the shift and the byte substitution are based on previous functions or parts of them, while the xor operation is performed with a precomputed vector instead of computing at every round the specific byte to xor. In this way, the implementation is more efficient and simpler.

The *roundKey* function first joins 4 bytes at a time of the key in order to work with words, then it needs the auxiliary *gFunction*, then it performs the four cascaded xor and finally it rebuilds the byte based key from the word. To debug this section, it has been used a step by step subkey generation that can be found in [2].

### 3 Decryption

The decryption part of AES is simpler than encryption since it is possible to reuse at least the key addition layer; based on implementation choices, as those made in this work, it is possible to reuse also the *mixcolumn* idea in its inverse. The decryption is less efficient than its counterpart since it generates the subkeys at every round instead of generating it once and storing it, this has been chosen to speed up the implementation. The decryption function is called *decrypt*.

#### 3.1 Mix Columns Inverse

The inverse of *mixcolumns* reuses part of its direct function together with a precomputation: the columns are preprocessed (fig. 1b) and then they are elaborated by the *mixcolumns* core (fig. 1a). All the mathematical details on this choice can be found in chapter 4 of [5].

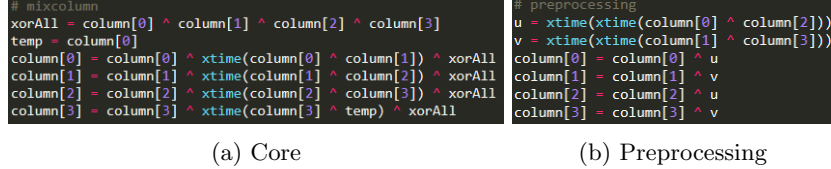


Figure 1: Mix columns base functions

#### 3.2 Shift Row Inverse

This blocks perform the row shift to the right, in order to reverse the *shiftRow* operation. As before, from state it builds the necessary rows and at the end the state is rebuilt from each rows. In order to reverse the direct operation, the first row is not shifted while the others receive successive shift till the third row which undergoes a right shift of three elements.

#### 3.3 Byte Substitution Inverse

This layer perform the inverse operation of Byte Substitution; it is based on two functions: *subByteInv* is just a wrapper to call the base function on every byte of the state; *subByteSingleInv* instead is the base function which performs the inverse Byte Substitution based on the inverse table seen before in the *subByteSingle*.

## 4 Operation Modes

Operation modes allows to use a block cipher as AES in the case the message has length different from one block size: in this case, it is possible to encrypt multiple blocks or even deal with blocks of length different from 128 bit of AES. In order to work with different blocks, we need to manipulate the input: in this work, it is assumed to receive a string of hex numbers as input, as in table 1, so it is necessary to have some input manipulation function.

The values in table 1 are taken from example of implementation of AES found on GitHub.

Input text	6bc1bee22e409f96e93d7e117393172a ae2d8a571e03ac9c9eb76fac45af8e51 30c81c46a35ce411e5fbc1191a0a52ef f69f2445df4f9b17ad2b417be66c37
Key	2b7e151628aed2a6abf7158809cf4f3c
IV (where needed)	5468617473206D79204B756E67204675

Table 1: Parameter used in testing of AES operation modes

### 4.1 Text to Blocks

First it is necessary the *text2blocks* function, which divides the hex string received as input into the blocks that can be encrypted by AES. In this step a problem arises: what to do with blocks whose length is different from block size? it has been chosen in this work to use the padding scheme PKCS#7, which in this case added a number of bytes to complete the block with value equal to the number of missing bytes; in the special case when the last block has length equal to block size, the padding scheme plans to add a new block composed by bytes whose value is equal to block size in bytes (16 in this case so a block composed by 10 in hex base). The padding is added during text to block conversion by a similar function as those explained before, the only difference is that it adds padding to the last block; this function is *text2blocksPadding*. To have a better understanding of why it was necessary to design two similar functions here it is their main motivation: *text2blocksPadding* is used with plaintext as input, so it must add padding since text is going to be transformed; *text2blocks* is used in decryption with ciphertext as input, so the padding has already been added in encryption and it is necessary to just split the text into blocks whose length is exactly equal to block size.

## 4.2 Blocks to Text

To obtain the text from an array of blocks, some extra functions are needed: *blocks2text* and *blocks2textPadding*, whose aim is the inverse of that of previous section. The first is used at the end of encryption phase to obtain the ciphertext from the array of encrypted blocks. Special mention goes to the second function, which works at the end of decryption to join plain blocks into the decrypted plaintext: in this case it is necessary to reverse and remove the padding scheme used, this operation is simpler when the last byte of last block is 16 in base ten since we simply remove the last block from the the array (and the corresponding list made to host the blocks undergoing manipulation); in the case the last byte read is different from 16, it is necessary to remove a number of couples (every byte is in hex, so two cipher must be removed) equal to the last byte read and this is done by a simple iterative *pop*.

## 4.3 Block manipulation

Since the state is a list of 16 int representing the 16 bytes, it is necessary to transform every block (which contains a string of hex values) into the int array needed by encryption function: this operation is performed by *block2int* function. The reverse function, *int2block*, allows to recover the block string from the list of 16 int which was used as state.

## 4.4 Implementation notes

All the operative modes have been implemented based on the slide of the course, CBC and CFB cause some problems since the following block to be encrypted needs to know the previous encrypted block, and due to the different implementation of *block2int* and *int2block*, it is necessary from the second block on to delete the spaces the function *int2block* introduces in order to have the right input for *block2int* in the following block computations.

Another problem is in the CTR, since for every block it is needed to increment the nonce, and a problem may arise when the increment arrives to the byte in position 0 in the block array and the current value is 255, I am not able to decide what to do in this situation, so I used a nonce which does not create this problem in the test (I know this is not the right way to do it). Furthermore, this implementation uses a 128 bit nonce; this is because initially I wasn't able to find a standard and I thought the nonce was all the 128 bit and it is incremented; then I found that *pycryptodome*, the library chosen as standard for the comparison, implemented a 64 bit nonce plus a 64 bit counter and they are put one at the side of the other to build a 128 bit IV. Both these standard are documented in [3], so I ha chosen to stay firm in y choice, so experimental result will be different. Perhaps, on further digging on the net, I found that it is possible to use an IV of 128 bit where the left 96 bits are nonce and the remaining 32 bits on the right are dedicated to counter, this is specified in [1] Instead, ECB and OFB were simpler to implement due to their simpler algo-

rithm.

A note on the implementation of CFB: it can be implemented choosing a variable number of bits of ciphertext to xor in the following iteration, in this case, to simplify the implementation, it has been chosen to xor the complete block.

## 5 Real World Standard Comparison

Mass of magnesium metal	= 8.59 g - 7.28 g
	= 1.31 g
Mass of magnesium oxide	= 9.46 g - 7.28 g
	= 2.18 g
Mass of oxygen	= 2.18 g - 1.31 g
	= 0.87 g

Because of this reaction, the required ratio is the atomic weight of magnesium: 16.00 g of oxygen as experimental mass of Mg: experimental mass of oxygen or  $\frac{x}{1.31} = \frac{16}{0.87}$  from which,  $M_{\text{Mg}} = 16.00 \times \frac{1.31}{0.87} = 24.1 = 24 \text{ g mol}^{-1}$  (to two significant figures).

## References

- [1] 64bit nonce in aes-ctr, why not 128? <https://crypto.stackexchange.com/a/27000>.
- [2] Aes example (8bit key and message). <https://kavaliro.com/wp-content/uploads/2014/03/AES.pdf>.
- [3] Recommendations for block cipher modes of operation. <https://nvlpubs.nist.gov/nistpubs/Legacy/SP/nistspecialpublication800-38a.pdf>.
- [4] Understanding aes mix-columns transformation calculation. [https://piazza.com/class\\_profile/get\\_resource/kf2apkmqhrq4qw/kg6jff10s14j1tu](https://piazza.com/class_profile/get_resource/kf2apkmqhrq4qw/kg6jff10s14j1tu).
- [5] J. Daemen and V. Rijmen. *The Design of Rijndael*. Springer-Verlag, Berlin, Heidelberg, 2002.
- [6] K. C. Xintong. Aes mixcolumn. [https://en.wikipedia.org/wiki/Rijndael\\_MixColumns](https://en.wikipedia.org/wiki/Rijndael_MixColumns).