

*** Filters need recalculated! ***

Sheet: /HF Filters/
File: hf_filters.kicad_sch

Title: HF Filters

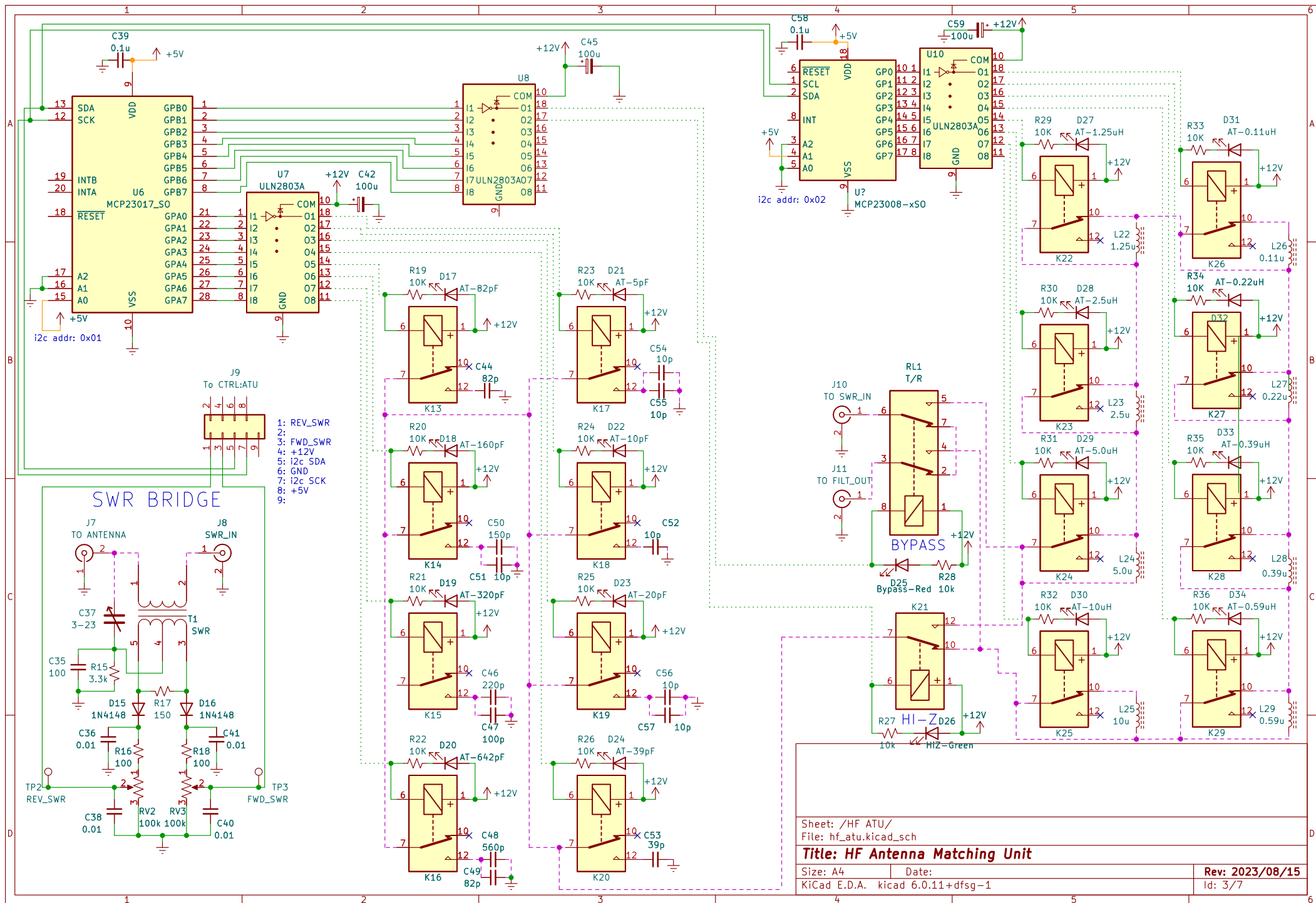
Size: A4

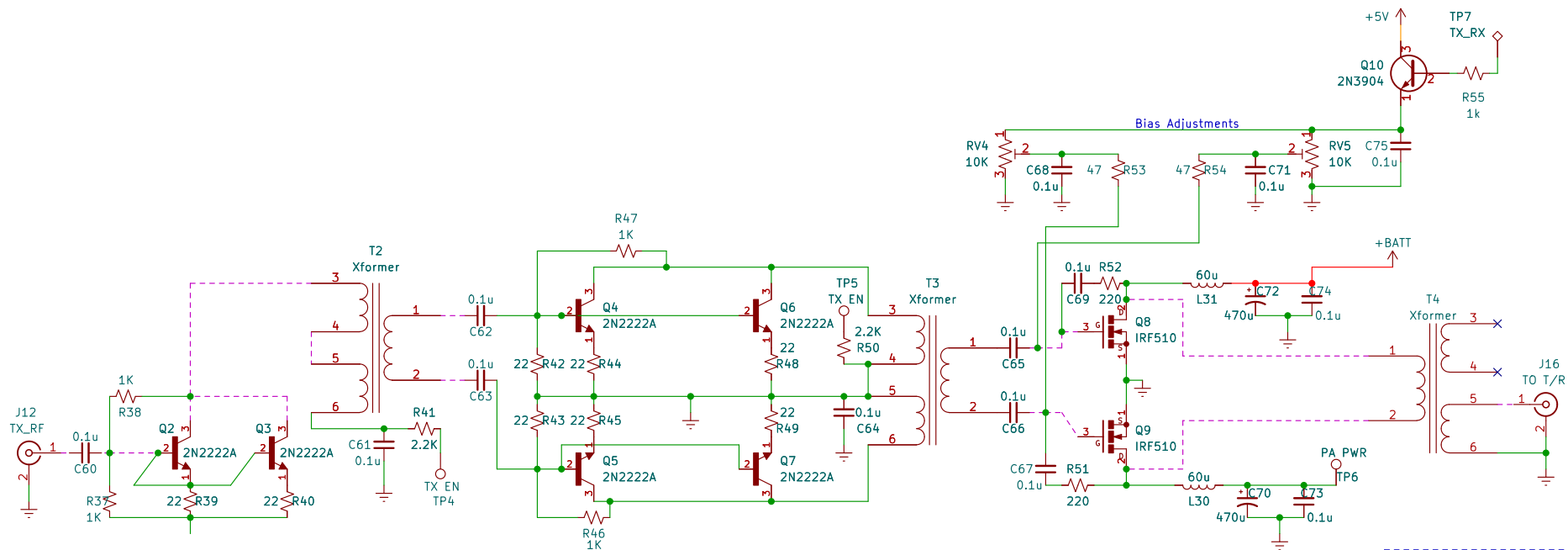
Date:

KiCad E.D.A. kicad 6.0.11+dfsg-1

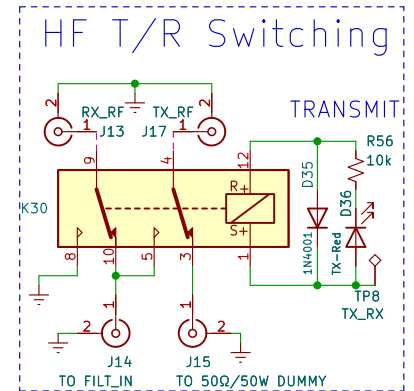
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Should we abandon the ubitx PA
in favor of something 100W?
With bias management by uc,
we can maintain 45W across HF.

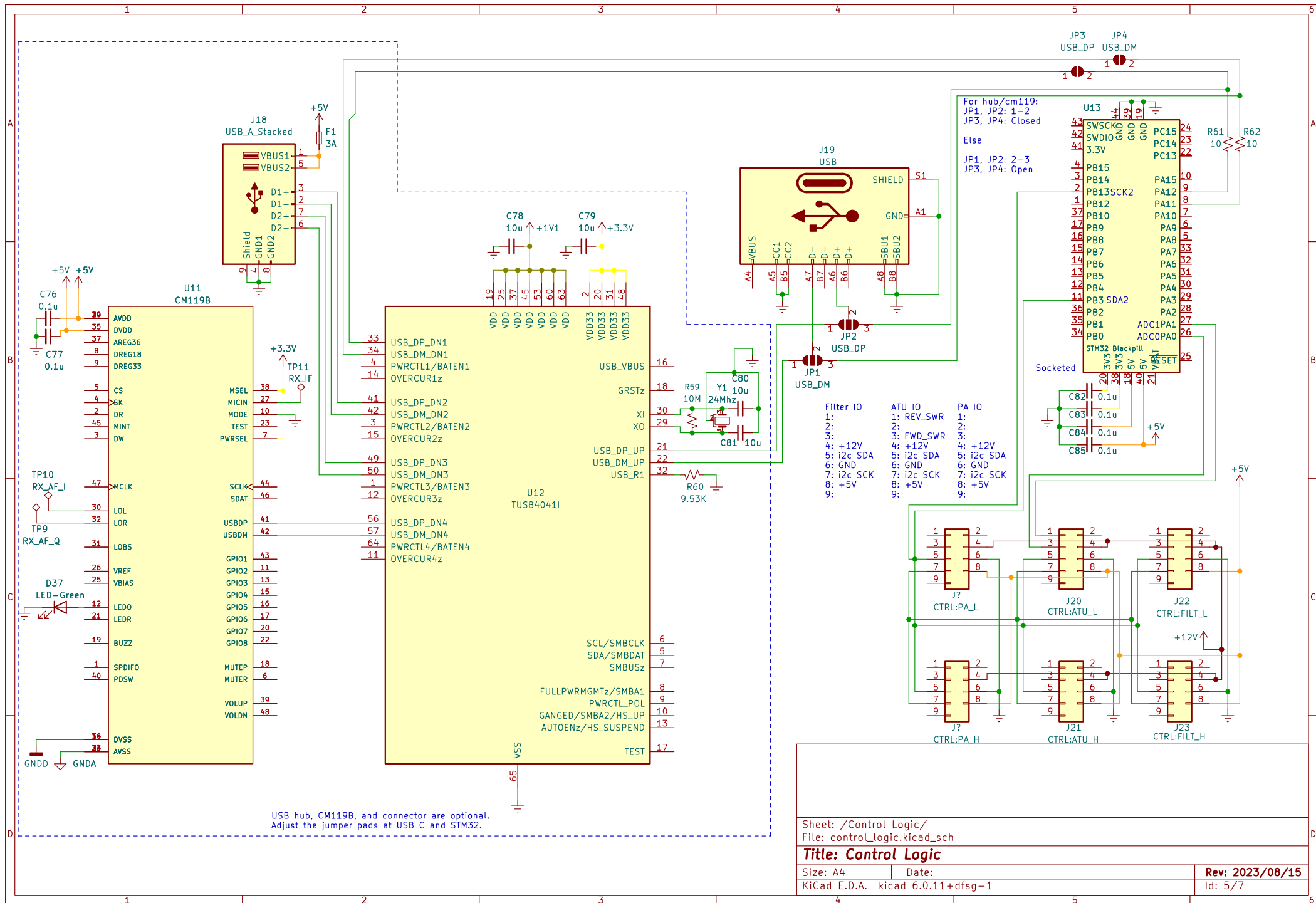


Sheet: /HF PA/
File: hf_pa.kicad_sch

Title: HF PA

Size: A4 Date:
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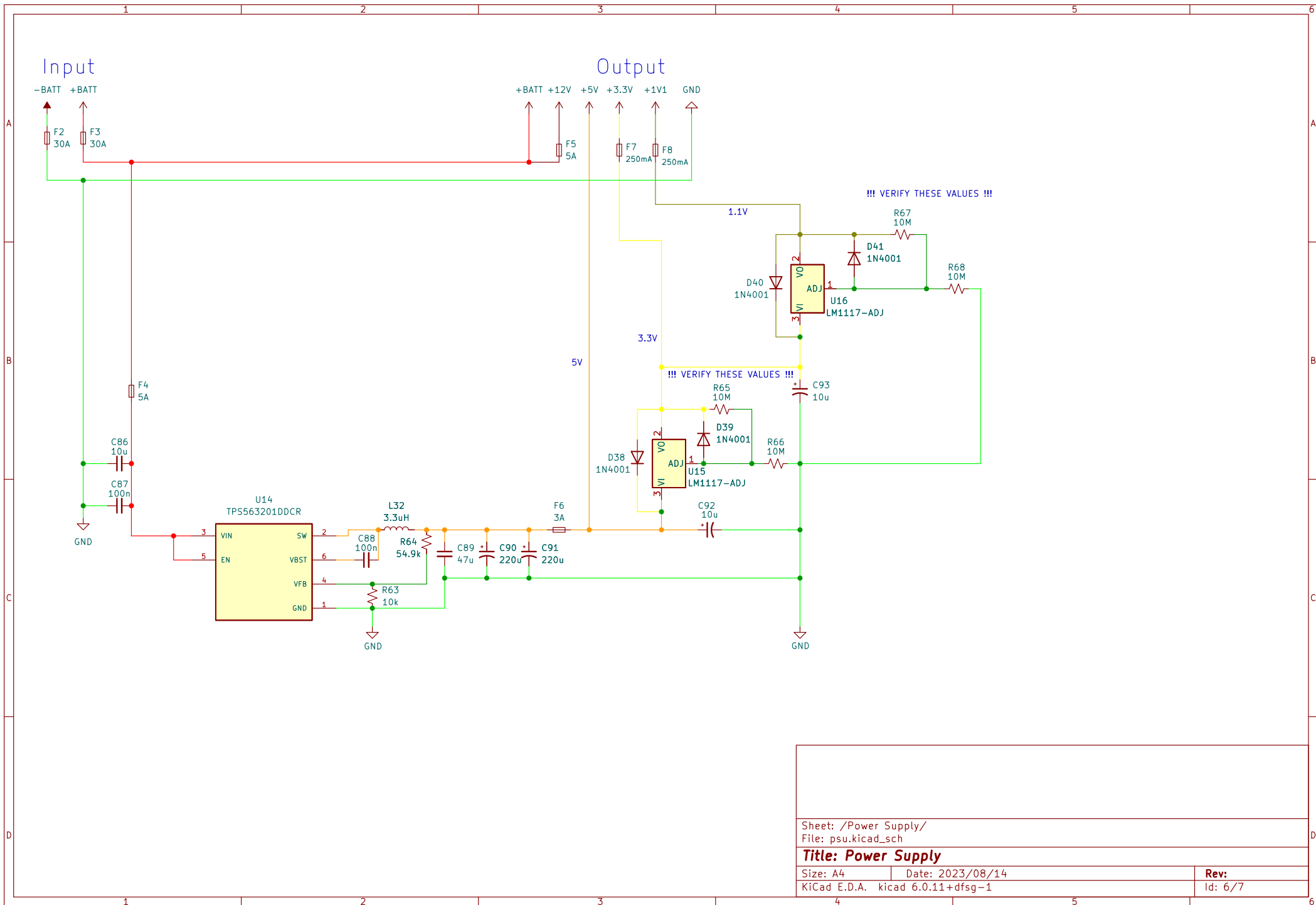
USB hub, CM119B, and connector are optional.
Adjust the jumper pads at USB C and STM32.

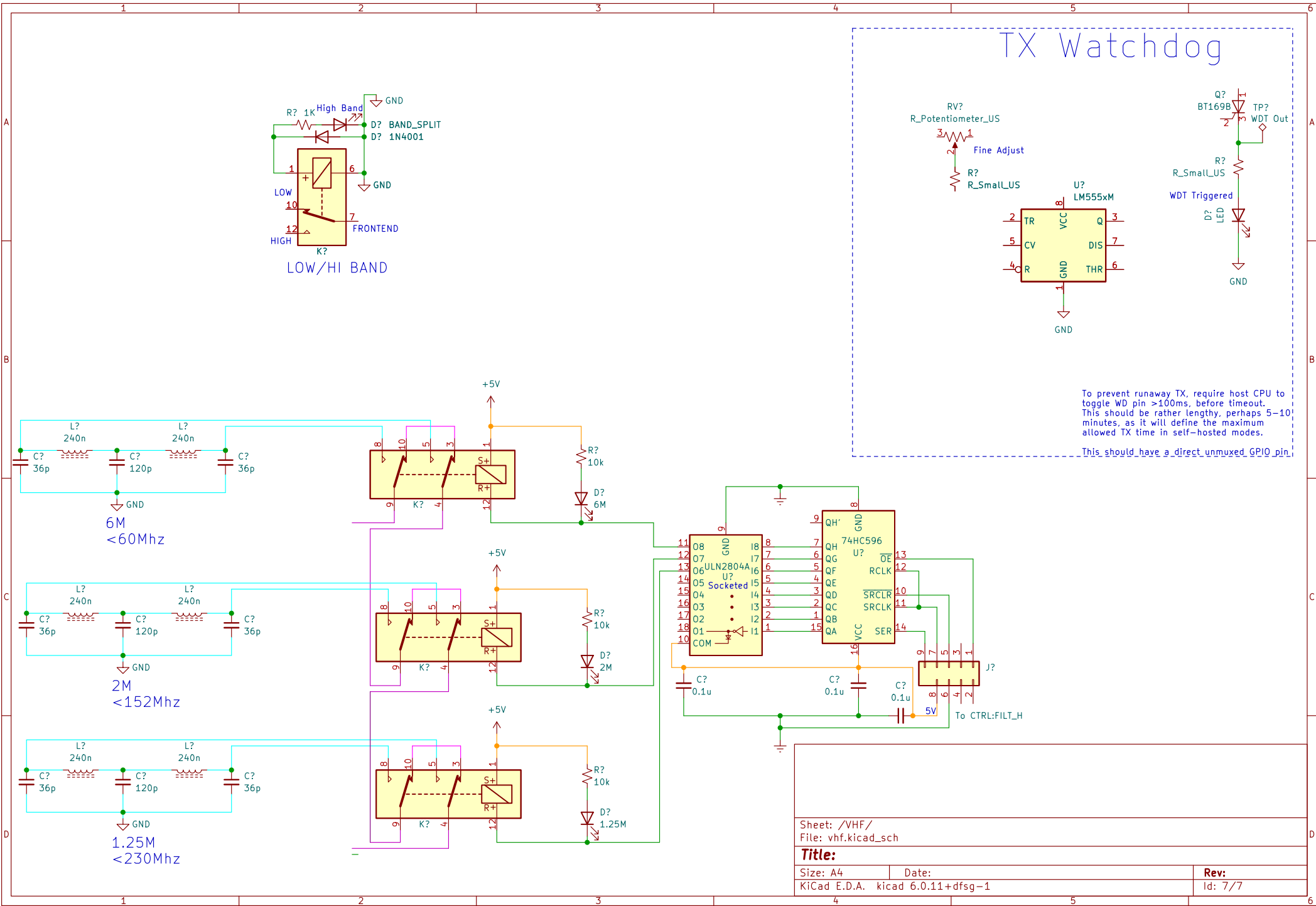
Sheet: /Control Logic/
File: control_logic.kicad_sch

Title: Control Logic

Size: A4 Date:
KiCad E.D.A. kicad 6.0.11+dfsg-1

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Sheet: /VHF/
File: vhf.kicad_sch

Title:

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Sheet: /VHF/
File: vhf.kicad_sch

Title:

Size: A4	Date:	Rev:
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TX Watchdog

To prevent runaway TX, require host CPU to toggle WD pin >100ms, before timeout. This should be rather lengthy, perhaps 5–10 minutes, as it will define the maximum allowed TX time in self-hosted modes.

This should have a direct unmuxed GPIO pin.

Sheet: /VHF/		
File: vhf.kicad_sch		
Title:		
Size: A4	Date:	Rev:
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[illegible]