

# Distributed Computing

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## 1 Perform a precise analysis of the time complexity of the Flooding algorithm.

### 1.1 The synchronous model

Let  $A$  be the synchronous Flooding algorithm. Let  $p_r$  be the root node in a tree  $G = (V, E)$ .  $p_r$  has message  $m$  at  $t = 0$ . Analysis is by induction.

*Base case:* each child of  $p_r$  receives  $m$  in the first round,  $t = 1$ .

*Step case:* a child processor  $p$  of a parent processor  $p'$  receives  $m$  at  $t$  if  $p'$  received  $m$  at  $t - 1$ . True by description of  $A$ .

Each processor at distance  $t$  from  $p_r$  therefore receives  $m$  in round  $t$ .  $A$  terminates when every processor has the message  $m$ . The last processor will receive  $m$  at  $t = D$ , where  $D$  is the *diameter* of  $G$ . This is also the depth of  $G$ . As  $D < |V|$ , the time complexity of  $A$  is  $O(n)$ .

### 1.2 The asynchronous model

In asynchronous systems the flooding algorithm terminates after  $R$  time units, where  $R$  is the radius of the source. However, the constructed spanning tree may not be a breadth-first search spanning tree.

In every execution of the broadcast algorithm in the asynchronous model, every process at distance  $t$  from  $p_r$  in the spanning tree receives  $M$  in

## 2 Consider an anonymous ring where processors start with binary inputs.

### 2.1 Give an argument that there is no uniform synchronous algorithm for computing the AND of the input bits.

Proof is by contradiction. Assume that there is a uniform synchronous algorithm  $A$  that computes AND. Assume a ring where all inputs are 1. In any round  $i$  the states

of all processors are identical, and these states do not depend on the size  $n$  of the ring, as the algorithm is uniform. As  $A$  is correct, there must exist a round  $t$  such that all processors terminate and output 1.

Now run  $A$  on a ring of size  $2(t + 1)$ , with 1 processor  $p$  with input 0 and  $2t + 1$  processors with input 1.  $p$  disseminates 0. However, the processor 'opposite'  $p$  will not receive 0 by round  $t$ ; it will instead terminate and output 1. This contradicts the assumption that  $A$  is correct (in which case 0 should be output at all processors).

## 2.2 Present an asynchronous (non-uniform) algorithm for computing the AND. The algorithm should send $O(n^2)$ messages in the worst-case.

Let  $n$  be the number of processors. Each processor sends a message to its right neighbour and waits for messages from its left neighbour. Each message  $m$  comprises a counter  $hop = 0$  and a state  $x$ .  $hop$  is incremented every time  $m$  is sent. If  $m.hop == n$ , the message has arrived where it started.

When a processor  $p$  with input state  $x$  receives a message  $m$ , it updates  $m.x = p.x \wedge m.x$ . It then checks  $m.hop$ . If  $m.hop < n$ , it forwards the message to the right. If  $m.hop == n$ , the processor terminates.

As each processor sends a message, and each message makes  $n$  hops, the message complexity is  $O(n^2)$ .

## 2.3 Present a synchronous algorithm for computing the AND. The algorithm should send $O(n)$ messages in the worst case.

Let  $n$  be the number of processors. A processor with an initial state of 0 sends a message in both directions and halts (in state 0). A processor with an initial state of 1 waits for  $\lfloor n/2 \rfloor$  cycles. If it receives a message during that period, then it forwards it and halts in state 0. If by cycle  $\lfloor n/2 \rfloor$  a processor has not received any message, it halts in state 1. The total number of messages sent is  $O(n)$ .