

Software Design Engineer seeking roles in Software Development, Application Programming, System Analysis, Technical Architecture, Requirement Gathering, Firmware Development, Configuration Management, Python, Linux, C, C++



Current Designation: Software Design Engineer Total Experience: 7 Year(s) 0 Month(s)

Current Company: Imagination Technologies (Alreay left)

Notice Period: 15 Days or less

Current Location: Pune Highest Degree: MS/M.Sc(Science) [Electronics]

Pref. Location: Bengaluru / Bangalore, Hyderabad, Pune Functional Area: IT Software - Application Programming /

Maintenance

Role: Software Developer

Industry: Semiconductors/Electronics

Marital Status: Single/unmarried

Key Skills: Video Codecs, LibVPX, FFMPEG, X264, H264, H265, SIMD, MIPS, RISC Architecture, Neural Networks,

Tensor Flow, Caffe, Shell Script, Python, Linux, C, Data Structures

Verified: Phone Number | Email - id

Last Active: 21-Jan-21 Last Modified: 21-Jan-21

Summary

7+ years of experience in Video Codec Optimization with efficient programming and debugging skills in C/C++/SIMD.

* Optimized code generation of deep learning layers for Imagination's neural network accelerator.

Work Experience

Imagination Technologies (Alreay left) as Software Design Engineer Aug 2013 to Till Date

Imagination Technologies Pvt. Ltd. as Software Design Engineer Aug 2013 to Feb 2020

Software Video Codec Optimization

Video compression techniques and Standards (H263, H264, HEVC, VP8, VP9)

* Optimized root level functions of H263/H264/HEVC/VP8/VP9: In Intra

Prediction/Motion Compensation/Motion Estimation/Loop Filter/SAD calculation for MIPS MSA SIMD coprocessor.

- * Ported various video codecs of codec libraries like ffmpeg, libyuv, x264, libvpx for MIPS MSA.
- * Improved HEVC Intra Prediction module C code for better performance.
- * Fine tuned and tested optimized code on MIPS MSA FPGA module and MIPS MSA simulator
- * Development and testing done on Linux based system.

Built and tested MIPS MSA optimized code on android emulator.

Al Software Algorithms Optimization

* Improved code generation of deep learning layers for Imagination's neural network accelerator. While working on the AI compiler, developed C++ development and

debugging skills.

* Developed a good bit of understanding of deep learning frameworks such as TensorFlow, Caffe.

Imagination Technologies as Software Design Engineer

Aug 2013 to Feb 2020

7+ years of experience in Video Codec Optimization with efficient programming and debugging skills in C/C++/SIMD. Optimized various codecs for better performance using C intrinsic for MIPS MSA SIMD engine. Also worked on optimization of code generation of deep learning layers.

Pragna Systems GmBH as Test Engineer

Dec 2012 to Jul 2013

Embedded Software Testing

- Tested and generated embedded C code for human less tractor operation project.
- * Used tools like VectorCAST to test embedded C code.
- * Developed Python scripts to generate Test Reports in expected format.

Education

UG: B.Sc (Physics) from Shri Pancham Khemraj Mahavidyalaya, Sindhudurg University of Mumbai in 2009

PG: MS/M.Sc(Science) (Electronics) from Department of Electonic Science University of Pune, Pune in 2011

Other Qualifications/Certifications/Programs:

Diploma in Embedded System Design

IT Skills

Skill Name	Version	Last Used	Experience
C, C++			
Shell, Python			
Linux, Windows			
TensorFlow, Caffe			
MS Visual Studio, Eclipse			
Vim, Notepad++			

Languages Known

Language	Proficiency	Read	Write	Speak
English	Proficient			
Hindi	Proficient			
Marathi	Expert			

Affirmative Action

Work Authorization

Category: General

Countries: India Job Type: Permanent

Physically Challenged: No

Employment Status: Full time