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DINESH KUMAR BANDIKE ⭐ Featured

Data Science Intern with over a year of experience as a Data Analyst
Providing data-driven, action-oriented solutions to challenging business problems

Current Location: Hyderabad / Secunderabad	**Total Experience:** 1 Year(s) 0 Month(s)
Pref. Location: Bengaluru / Bangalore,Chennai,Hyderabad	**Notice Period:** 15 Days or less
Functional Area: IT Software - Application Programming / Maintenance	**Highest Degree:** M.Tech [Electronics/Telecommunication]

Role: Fresher

Industry: Fresher/Trainee

Marital Status: Single/unmarried

Key Skills: Fresher,Software Development,Application Programming,Web Development,System Analysis,Technical Architecture,Requirement Gathering,Data Visualization,Machine Learning,Core Java,Python,MySQL,HTML,CSS,jQuery,Microsoft Azure

Verified : 📞 **Phone Number** | **Email - id**

Last Active: 22-Jan-21   Last Modified: 22-Jan-21

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Summary

Certified M.Tech (VLSI) Professional with excellence in managing Software Development,Application Programming,Project Management,Web Development,System Analysis,Technical Architecture,Requirement Gathering,Data Visualization,Machine Learning,Core Java,Python,MySQL,HTML,CSS,jQuery

Work Experience

◉ **Artificial penetration software solutions Pvt Ltd** as Data Scientist
Jun 2019 to Jun 2020

would be working on research and development projects involving statistical analysis, machine learning, natural language processing, graph analytics, and data visualization

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Education

UG: **B.Tech/B.E. (Electronics/Telecommunication)** from **Malla Reddy College of Engineering & Technology, Medak** in **2017**

PG: **M.Tech (Electronics/Telecommunication)** from **gokaraju rangaraju institute of engineering and technology, hyderabad** in **2020**

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IT Skills

Skill Name	Version	Last Used	Experience
C Language, Core JAVA, HTML5			
CSS, Bootstrap4, jQuery			
MySQL, IBM Watson Python			
Big Data, Data visualization			
Machine Learning, Verilog coding			
Android developer, web developer			
MS-OFFICE			

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Languages Known

Language	Proficiency	Read	Write	Speak
English	Expert	✔	✔	✔
Telugu	Expert	✔	✔	✔
Kannada	Expert	✔		✔
Hindi	Proficient			✔

Projects

◉ Project Title: Securing Designs of an Area Efficient BIST Technique in UART
Client: B.Dineshkumar
Nature of Employment: Full Time
Duration: Aug 2018 - Aug 2019
Onsite / Offsite: Offsite

Project Details: Proliferation drift of IC trade, many microchips are modeled in the foundry. The presence of behaving-inbuilt hardware Trojans (HTs) is of very tight security concern, without the awareness of end-users or original designers of the host, to identify Trojans-free circuit. For this designer seeks low switching probability nets to insert HTs to reduce power leakage. But the circuits net experiences distinct state and switching probabilities on test and function mode. The proposed method, fast heuristic, is actuated on the circuit under test (CUT). This is a minimal complex, high accurate, popular standard, and complex circuit tested with reasonable delay. In hardware self-testing, (built-in self-test) offer a worthy solution to reduces product failure, complexity occurs in proliferation. Design and actuation of universal asynchronous receiver transmitter (UART), to reduce power, area, to reach portable, stable, and reliable data transmission is employed.

Affirmative Action

Category: General

Physically Challenged: No

Work Authorization

Job Type: Permanent

Employment Status: Full time