

# PRIYADHARSHINI R

BE - ELECTRONICS AND COMMUNICATION ENGINEERING

## CONTACT

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## PROFESSIONAL SUMMARY

I am an Electronics and Communication Engineering student looking to start my career in the core domain. My internship experience has given me practical exposure to Embedded and IoT, Wireless Communication, and microcontroller basics. I am eager to learn, grow, and contribute to meaningful technical projects.

## ACADEMIC HISTORY

### St.Joseph'sGirlsHrSec School | 2021-2022

Computer Science

- Percentage : 85% Recognition for School-
- Level Academic & Event Leadership

### PSNA College of Engineering and Technology | 2022-2026

Bachelor of Electronic and Communication Engineering

- GPA: 8
- NCC Cadet & Fire-Shooting Leader – Leadership, discipline, and safety skills.

## SKILLS

- C , C++, Java
- Microcontroller/Microprocessor / Basic - [Electronics and Electrical - Embedded - Wireless Communication]
- Tools Handling - Arduino - PIC16F877A - ARM - ESP32 -GitHub- Maplab+Proteus - Vs code - Wokwi - Matlab - Simulink - Ansys

## WORK EXPERIENCE

### Intern-Embedded Systems & IoT | 2025-2025

PUMO TECHNOVATION

- Hands-on experience in embedded systems and microcontroller programming.
- Developed projects using MPLAB, Proteus, and IoT platforms like Blynk.
- Work done cloud-based monitoring, control, and circuit design

### Intern – Wireless Communication and RF | 2025-2025

THIAGARAJAR COLLEGE OF ENGINEERING

- Basic foundation in wireless communication and Antenna Designing.
- Antenna simulation and analysis using ANSYS HFSS.
- Designed high-gain microstrip patch antenna for 5G applications

## PROJECT

### Glow lot

- Developed an IoT-based LED control system using ESP32, Blynk mobile application, and Arduino IDE (C programming) over Wi-Fi.
- Integrated Blynk cloud for remote access, enabling LED ON/OFF control on the same IP network, simulating 5G-based smart control systems

## PROJECT

### High-Frequency Offset-Fed Microstrip Patch Antenna Design for 5G

- Designed and simulated a compact 3 mm single-band patch antenna using ANSYS HFSS for 5G communication with optimal gain.
- Achieved targeted resonant frequency and impedance matching, meeting design goals for next-generation wireless systems.

## CERTIFICATION

- VLSI Design Workshop | India's VEGA Processor
- Sixth Sense Robotics Workshop | Genesis EduTech
- (ADCP) | Cadd Cae Computers, Dindigul
- Innofesta'24 – National Level Project Competition (Participation) | Madurai
- Embedded Systems & IoT Internship | PUMO Technovation
- Edge AIoT Workflow Workshop | DigiToad Tech
- Wireless Communications Internship | TCE, Madurai
- NCC – B Certificate | National Cadet Corps

## PERSONAL INFORMATION

- **Name** : Priyadharshini R
- **Father Name** : Ramachandran M
- **Born Date** : 12-10-2004 (21 Years)
- **Gender** : Female
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