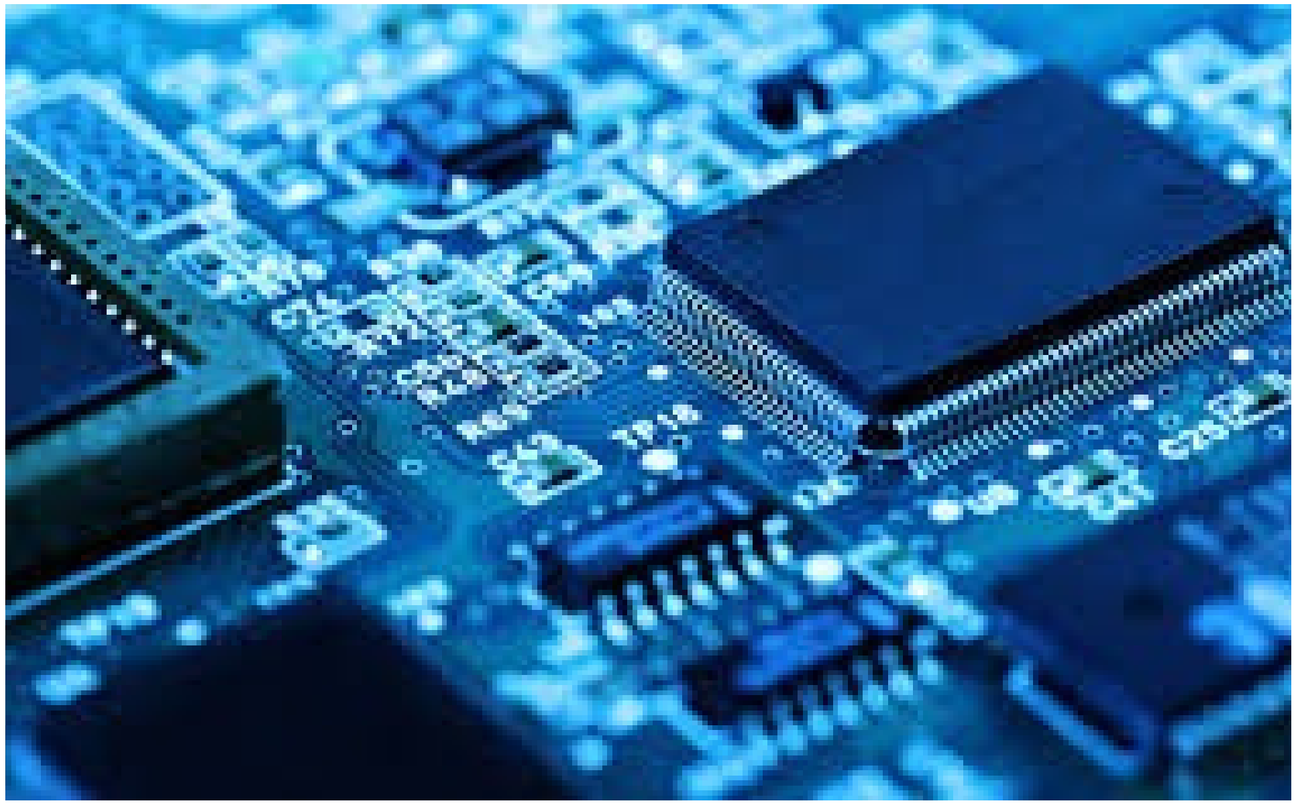
To Design 16-Bit ALU

Using 8086 Emulator

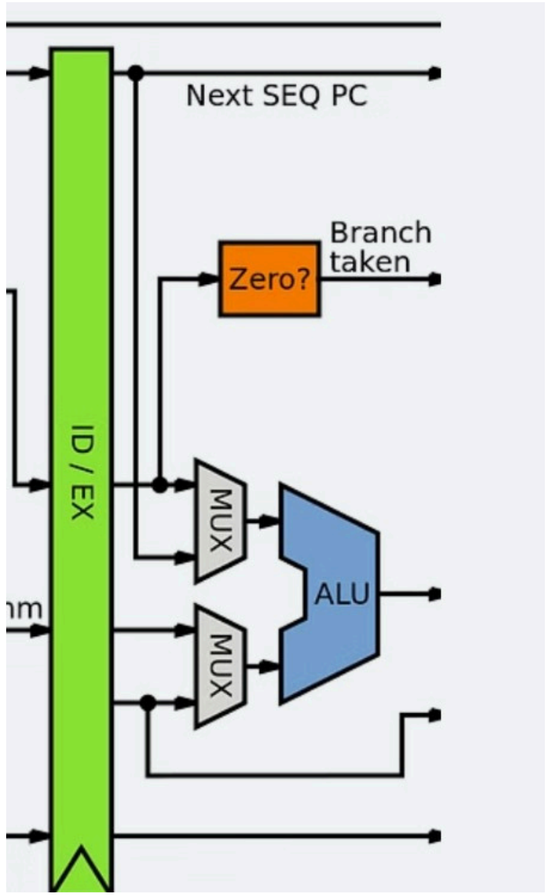
CONTENTS

INTRODUCTION

The 16-bit Arithmetic Logic Unit (ALU) is a fundamental building block of modern computer systems. It performs essential arithmetic and logical operations that power a wide range of applications, from embedded systems to high-performance computing.



Architectural Design of the 16-bit ALU



1.Register File

The register file stores the operands and results of ALU operations, providing fast access to data.

2.Arithmetic Unit

The arithmetic unit performs basic operations like addition, subtraction and multiplication on 16-bit data.

3.Logic Unit

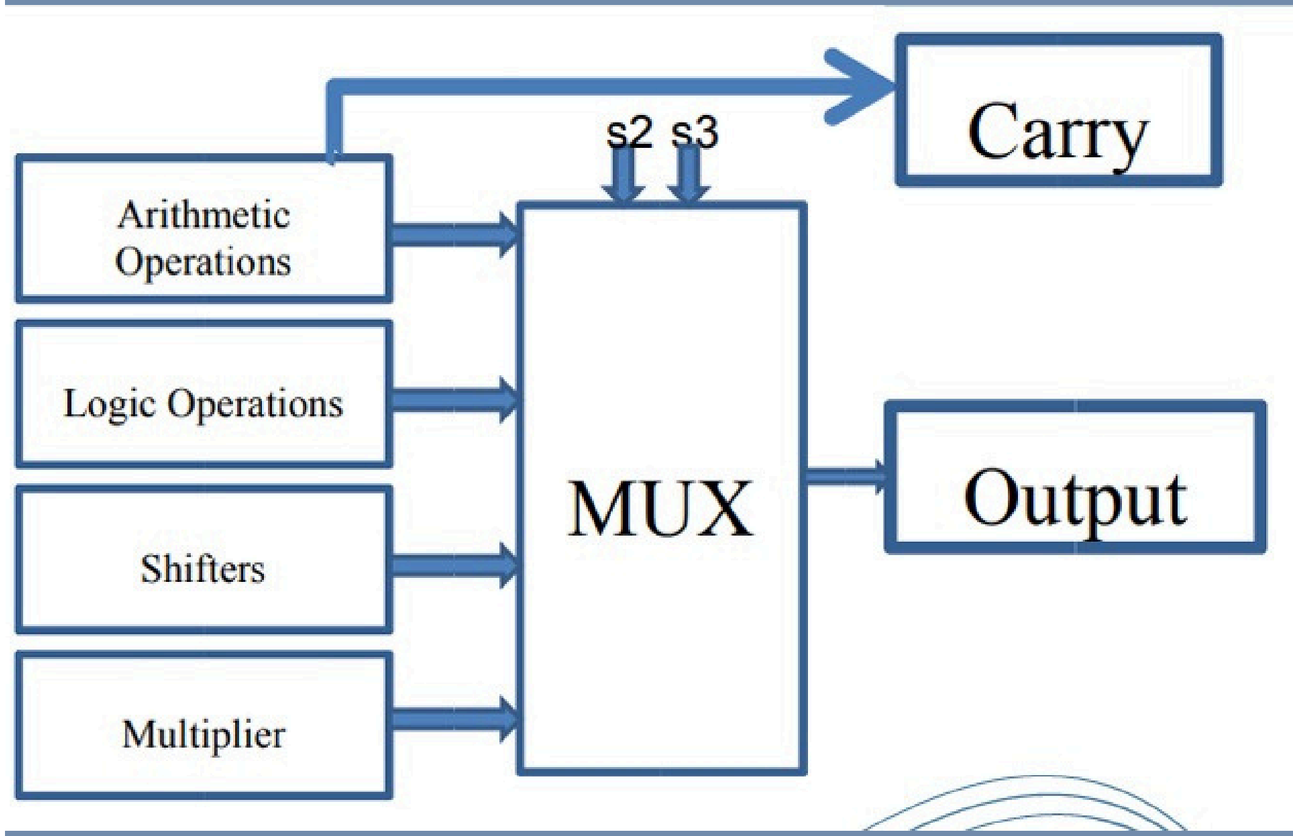
The logic unit handles bitwise operations such as AND, OR, and XOR enabling complex logical computations. bit of body text.

FUNCTIONAL UNITS AND OPERATIONS

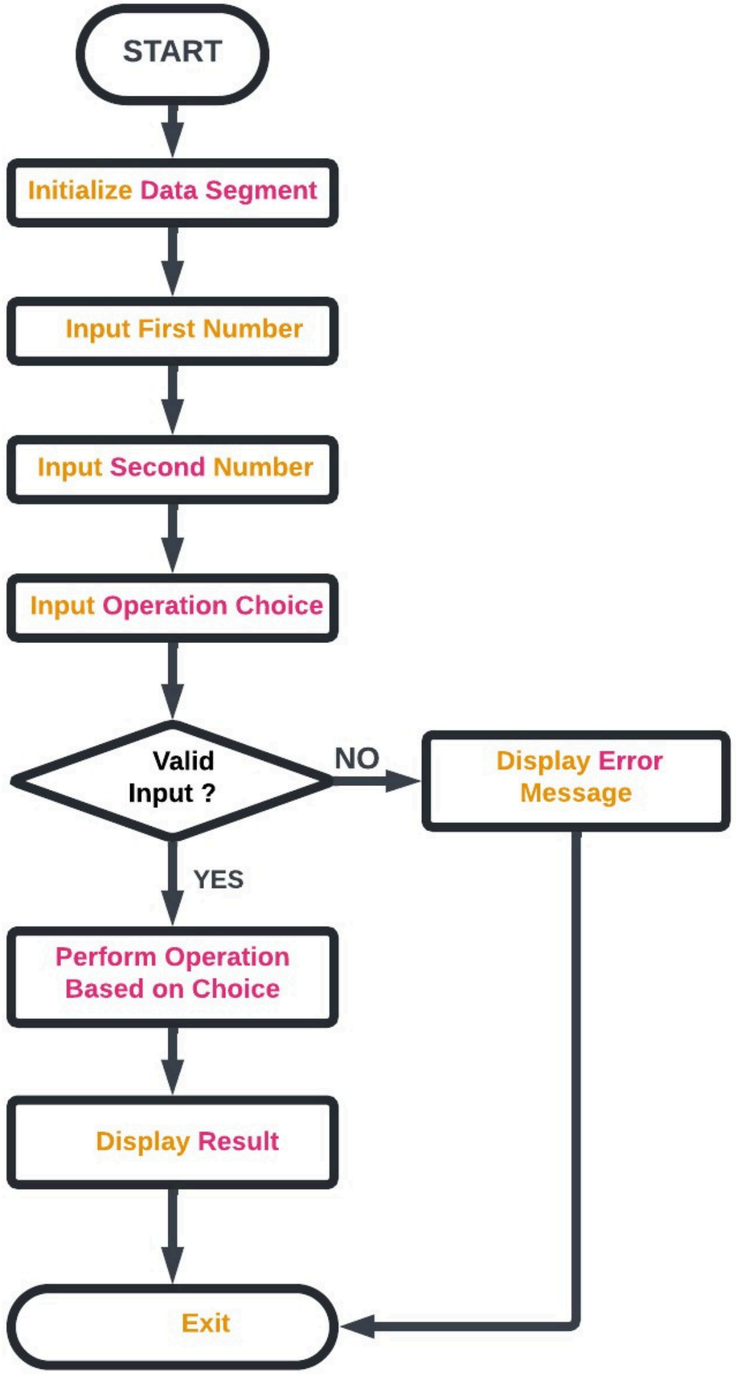
Arithmetic Operations:
The ALU supports standard arithmetic operations like addition, subtraction, multiplication, and division.

Shift Operations
Logical and arithmetic shift operations enable efficient bit manipulation and number scaling.
Comparison Operations. The ALU can perform various comparison operations,
including equality, greater than, and less than.

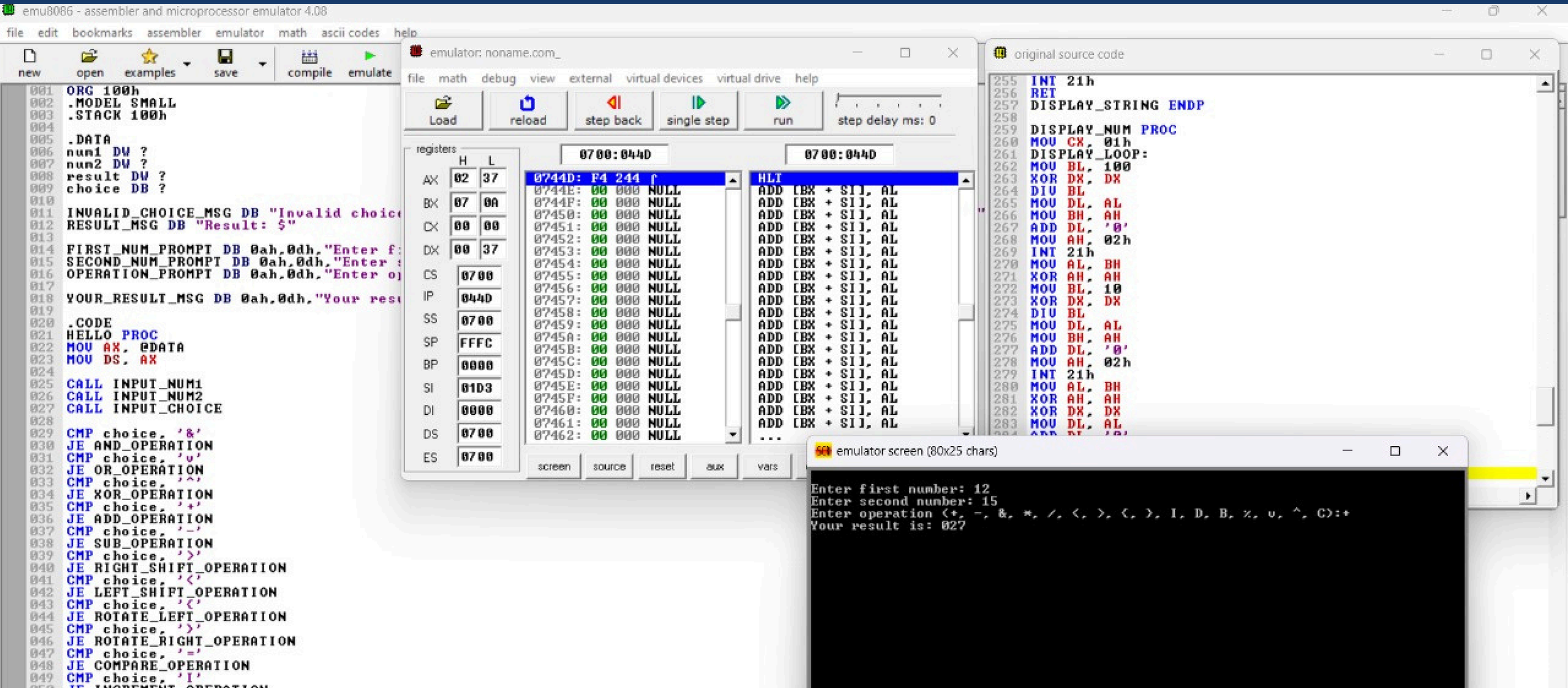
Logical Operations
Bitwise operations such as AND, OR, NOT, and XOR are provided for Boolean logic and bit manipulation.



FLOW CHART


START
Initialize Data Segment
Input First Number
Input Second Number
Input Operation Choice
Valid Input ?
NO
Display Error Message
YES
Perform Operation Based on Choice
Display Result
Exit

INTERFACE

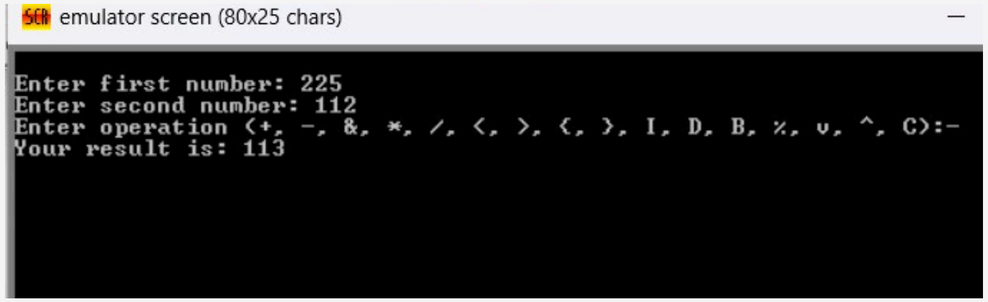



```
ORG 100h
.MODEL SMALL
.STACK 100h

.DATA
num1 DW ?
num2 DW ?
result DW ?
choice DB ?

INVALID_CHOICE_MSG DB "Invalid choic
RESULT_MSG DB "Result: $"

FIRST_NUM_PROMPT DB 0ah,0dh,"Enter f
SECOND_NUM_PROMPT DB 0ah,0dh,"Enter
OPERATION_PROMPT DB 0ah,0dh,"Enter o

YOUR_RESULT_MSG DB 0ah,0dh,"Your resu

.CODE
HELLO PROC
MOV AX, @DATA
MOV DS, AX

CALL INPUT_NUM1
CALL INPUT_NUM2
CALL INPUT_CHOICE

CMP choice, '&'
JE AND_OPERATION
CMP choice, 'v'
JE OR_OPERATION
CMP choice, '^'
JE XOR_OPERATION
CMP choice, '+'
JE ADD_OPERATION
CMP choice, '-'
JE SUB_OPERATION
CMP choice, '>'
JE RIGHT_SHIFT_OPERATION
CMP choice, '<'
JE LEFT_SHIFT_OPERATION
CMP choice, '('
JE ROTATE_LEFT_OPERATION
CMP choice, ')'
JE ROTATE_RIGHT_OPERATION
CMP choice, '='
JE COMPARE_OPERATION
CMP choice, 'I'
```

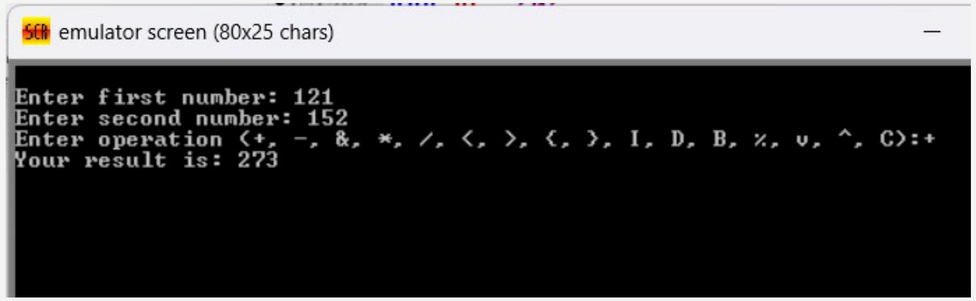
```
INT 21h
RET
DISPLAY_STRING ENDP

DISPLAY_NUM PROC
MOV CX, 01h
DISPLAY_LOOP:
MOV BL, 100
XOR DX, DX
DIV BL
MOV DL, AL
MOV BH, AH
ADD DL, '0'
MOV AH, 02h
INT 21h
MOV AL, BH
XOR AH, AH
MOV BL, 10
XOR DX, DX
DIV BL
MOV DL, AL
MOV BH, AH
ADD DL, '0'
MOV AH, 02h
INT 21h
MOV AL, BH
XOR AH, AH
XOR DX, DX
MOV DL, AL
```

ARITHMETIC INSTRUCTIONS

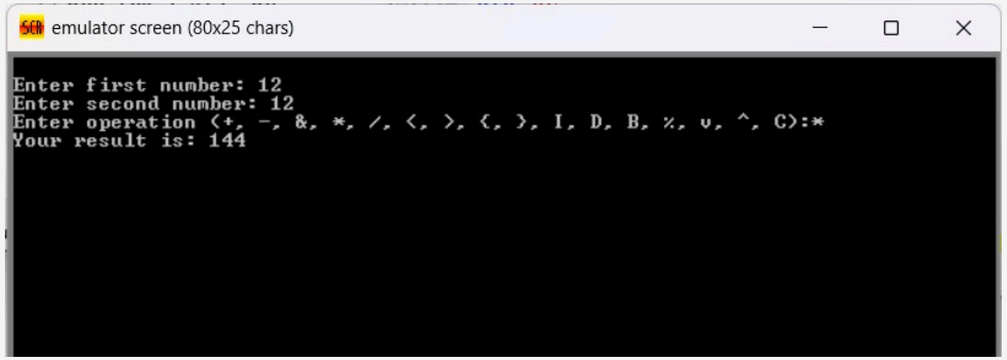
Subtraction



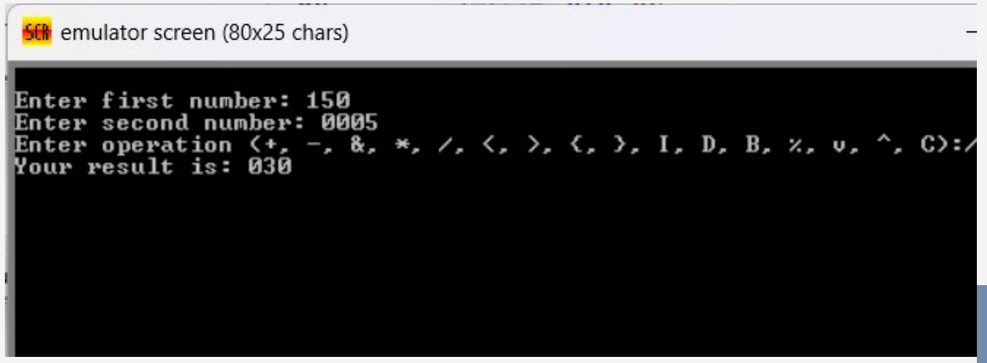
Addition



Multiply

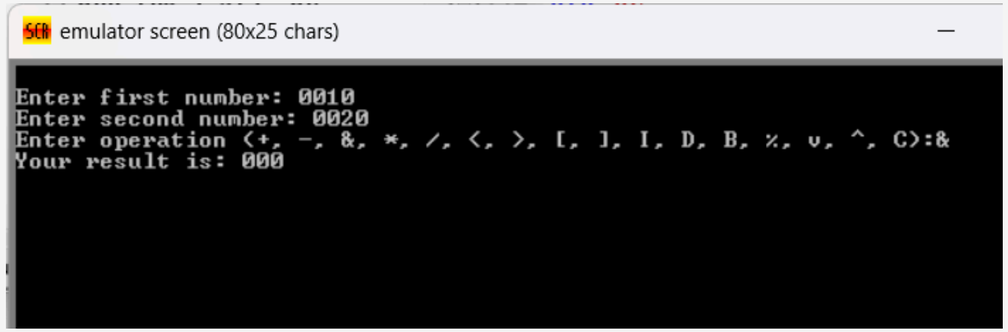


Division

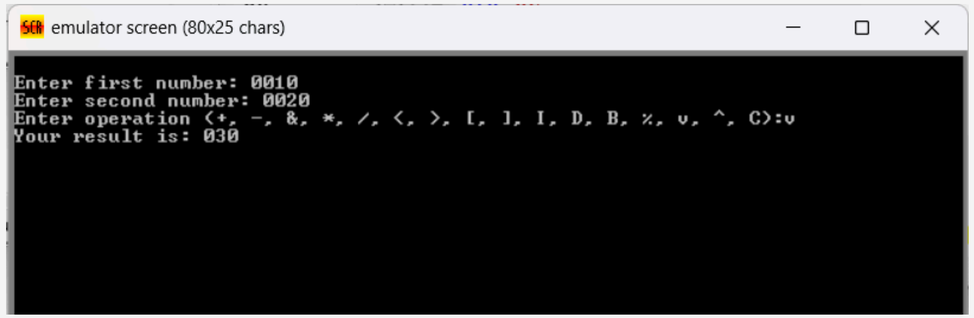


LOGICAL OPERATIONS

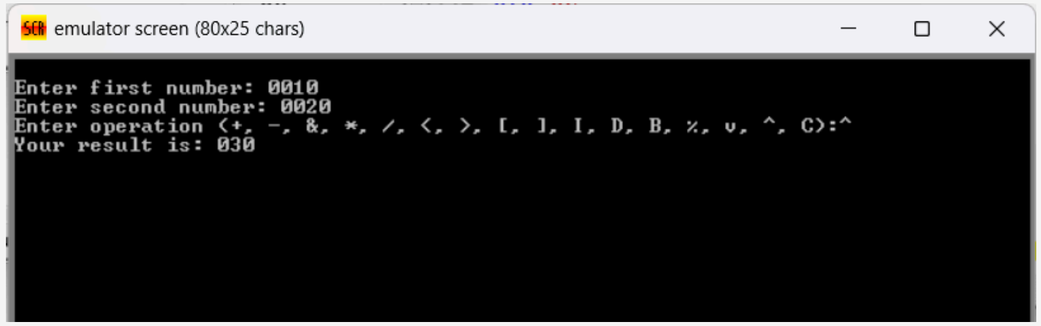
BITWISE AND



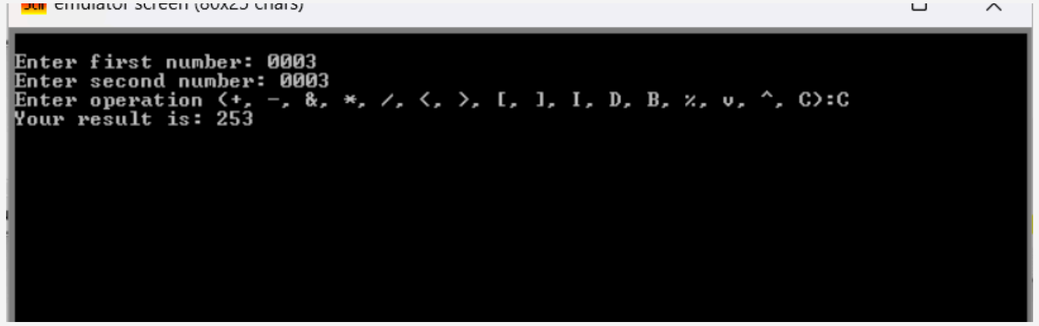
BITWISE OR



BITWISE XOR

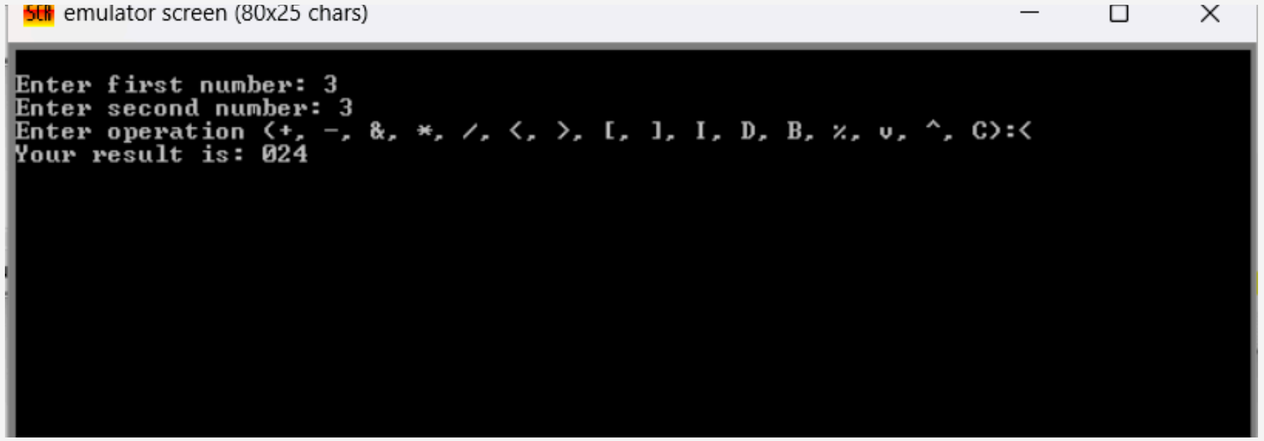


COMPLEMENT



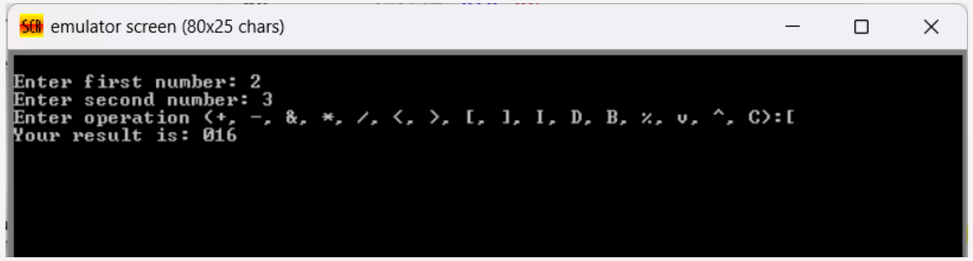
SHIFT/ROTATE OPERATIONS

SHIFT LEFT

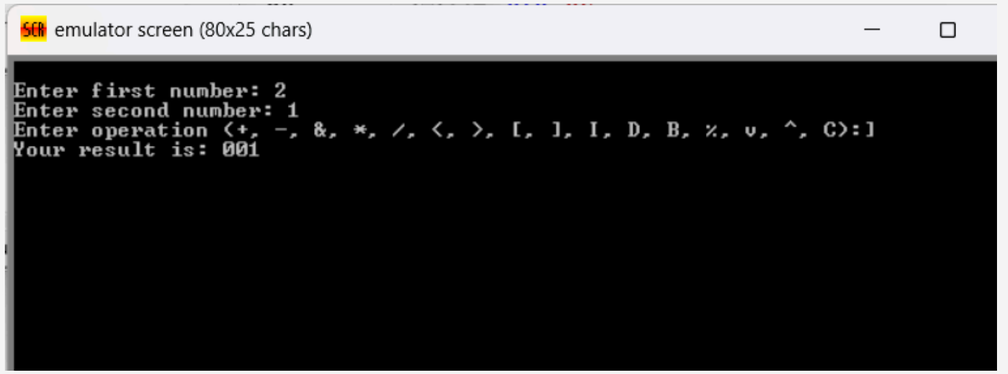


SHIFT RIGHT

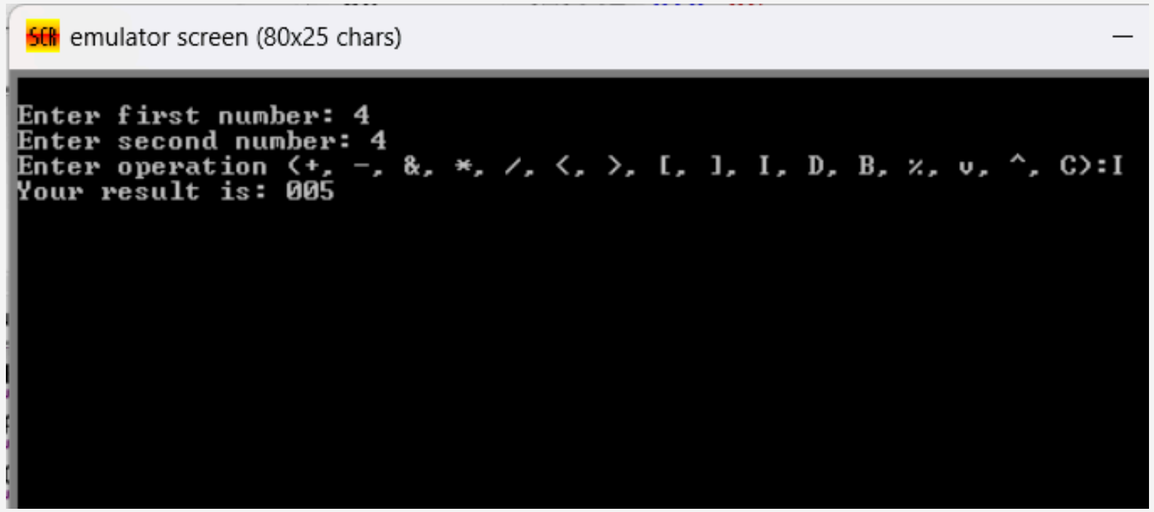
ROTATE LEFT



ROTATE RIGHT



INCREMENT



DECREMENT

LIMITATIONS

- **Limited Error Handling:**The code only provides a single error message for an invalid choice. It does not handle other potential errors such as division by zero or overflow errors. As a result, the program's robustness and reliability are compromised.

- **Limited Operand Size:** The code assumes that the operands (num1 and num2) are 16-bit integers (DW). This limits the range of values that can be processed by the program. For larger numbers, the program may produce incorrect results or overflow.

- **Limited User Interface:** The program's user interface is text-based and lacks interactivity

Thank you !