External Project Report on Digital Logic Design (EET1211)

[Topic Name-to be filled up by students]

Submitted by

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B. Tech. CSE 3rd Semester (Section - I)

INSTITUTE OF TECHNICAL EDUCATION AND RESEARCH
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Declaration

We, the undersigned students of B. Tech. of **(Write your Branch)** Department hereby declare that we own the full responsibility for the information, results etc. provided in this PROJECT titled "**(TOPIC NAME)**" submitted to **Siksha 'O' Anusandhan Deemed to be University, Bhubaneswar** for the partial fulfillment of the subject **Digital Logic Design (EET 1211)**. We have taken care in all respect to honor the intellectual property right and have acknowledged the contribution of others for using them in academic purpose and further declare that in case of any violation of intellectual property right or copyright we, as the candidate(s), will be fully responsible for the same.

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DATE:

PLACE:

Abstract

(to be written after the project is done. 100 – 200 words)

Contents

1. Introduction

Brief description of the project.

2. Problem Statement

I. Explanation of problem and identification of input and output variables.
II. Highlighting the constraints.

3. Methodology

I. Generating the solution to the problem by the use of Truth table/excitation table, K- map and (or) Boolean algebra.
II. Finding out the different digital ICs to be used in the optimized design.

4. Implementation

I. Drawing the logic diagram using different logic gates.
II. Program

5. Results & Interpretation

Verification of the output for different inputs that satisfies the problem statement by the use of truth table.

6. Conclusion

References

(as per the IEEE recommendations)

Appendices

Justification of the architecture / digital ICs used for implementation.

(Attach datasheets of the devices/ ICs that you have used)