

Lab 2c

Wednesday, November 23, 2022

6:01 PM

5.6.1 Lab #2c check-in requirements

The following items are due at the beginning of your Lab #2c session:

- ☒ The SPICE simulations for the stage 2 amplifier.
- ☒ The SPICE simulations for the coupled amplifier stage.
- ☒ The SPICE simulations for the entire circuit.
- ☐ Designed the impedance transformer and speaker loading of the circuit

To Measure:

- 1) Overall Voltage Gain (with RL, wo transformer and speaker)
- 2) Input Impedance
- 3) Max output voltage swing

1) $V_{out} = 2.11 \text{ V}$
Voltage gain = 10.55

- 2) Input impedance = 13kohm
- 3) Maximum Voltage swing = 10 V ,