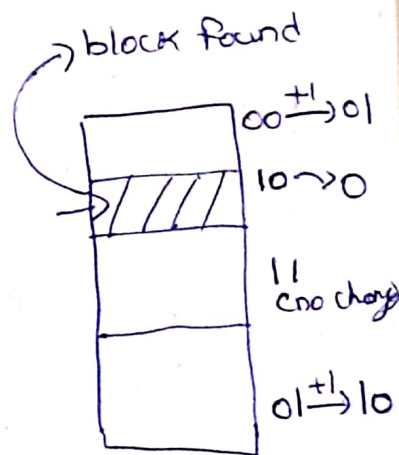


## ★ Replacement Algorithm (Hamacher)

### Least Recently Used (LRU)

- ) Replace a Cache line (block) which has not been <sup>referenced</sup> used for longest period of time.
- ) Assuming K-way cache

★) Hit occurs, Set the Counter value  $Zero(0)$  for the newly referenced block and increment the counter value of all blocks which have value lesser than the 'old' value of the referenced page (block)



Here  $K=4$

Above is a figure of set selected.

•) Miss occurs and Set is not full:-

Counter associated with ~~block is set~~ just referenced block is set to zero (0) and counters of all other blocks are incremented by 1.

•) Miss occurs and set is full:-

Identify the block associated ~~with~~ with highest value (above 11 in 4-way cache) and replace it, subsequently counter is set to zero (0) and counters of all other blocks are incremented by one.

(Another replacement policy: FIFO (first in-first out))

"Recent past is good indicator of near future" gives us idea that LRU is better than FIFO

Dt-11-03-2020

### Example of Mapping Techniques and LRU Replacement Policy (Homacher)

Problem: A computer has separate data and instruction caches. The data cache has 8 blocks. Each block can hold 1 word of 16 bits. Consider the program given in figure to identify the cache content in various mapping schemes.

Sol:)

```
int A[4][10];
```

```
int sum = 0;
```

```
for (j = 0; j < 10; j++)
```

```
    sum = sum + A[0][j];
```

```
avg = sum / 10;
```

```
for (i = 0; i < 10; i++)
```

```
    A[0][i] = A[0][i] / avg;
```

Consider the Array A is organized in column order and first element  $A[0][0]$  is at location 7A00

Variable sum, i, j are in processor's registers (Available, not shared to access mem)

It is a 4-way set Associative mapping.

39

Normalizing first row.

$10 \times 10^7$   
27th

\*) In Binary (1st location)

7A00  $\Rightarrow$  0111 1010 0000 0000

7A00	0111	1010	0000	0000
7A01	0111	1010	0000	0001
7A02	0111	1010	0000	0010
7A03	0111	1010	0000	0011
7A04	0111	1010	0000	0100
7A05	0111	1010	0000	0101
7A06	0111	1010	0000	0110
7A07	0111	1010	0000	0111

$A[0][0]$
$A[1][0]$
$A[2][0]$
$A[3][0]$
$A[0][1]$
$A[1][1]$
$A[2][1]$
$A[3][1]$
$A[0][2]$
$A[1][2]$
$\vdots$
$A[3][9]$

Total 40 elements

{ 7A27 0111 1010 0010 0111 }  
39th element



## Mapping scheme:-

7A22 0111 1010 0010 0111

Direct :-  $\leftarrow$  Tag (13 bits)  $\leftrightarrow$  cache line (3 bits)  $\rightarrow$  8 blocks in cache mapping

Associative :-  $\leftarrow$  Tag (16 bits)  $\rightarrow$

Set-Associative :-  $\leftarrow$  Tag (15 bits)  $\leftrightarrow$  #set = (1 bit) (4-way set-Associative)  
#set =  $\frac{8}{4} = 2$

Here:- As 1 block contains 1 word :  
 $\therefore$  No of bits for word field:  $\left( \frac{2}{2} \right) = 0$

Cache block	Content of cache after Pass $\rightarrow$ Content of cache after $j=1$ and so on					
	$j=1$	$j=3$	$j=5$	$j=7$	$j=9$	$i=9$
0	$A[0][0]$	$A[0][2]$	$A[0][4]$	$A[0][6]$	$A[0][8]$	
1						
2						
3						
4	$A[0][1]$	$A[0][3]$	$A[0][5]$	$A[0][7]$	$A[0][9]$	
5						
6						
7						

Eg:-  
 $A[0][0] = 0000 \rightarrow 0^{\text{th}} \text{ block}$   
 $+ 100 \text{ (Add 4)} \Rightarrow \text{As Column-order}$   
 $A[0][1] = 0100 \rightarrow 4^{\text{th}} \text{ block}$   
 $+ 100$   
 $A[0][2] = 0000 \rightarrow 0^{\text{th}} \text{ block}$   
 $+ 100$   
 $A[0][3] = 1000 \rightarrow 4^{\text{th}} \text{ block. and so on.}$

Now:-

next loop:  $i = 9$  (initially)

(we want location  $A[0][9] \rightarrow$  It is directly in the 4th block (so it's a hit))

→ This is the 1st hit as all were brought from mm so there was cache misses in last 10 times in bringing  $A[0][0] \dots A[0][9]$  to cache.

→ next  $i = 8$  :- we want  $A[0][8] \rightarrow$  we have  $A[0][8]$  in 0th block.

→  $i = 7$  ; we want  $A[0][7] \rightarrow$  we have  $A[0][7]$  in 4th block

→ Either we bring it back from mm

→ Or it is already there in 4th block, as it is direct mapping.

Total : 2 hits  
then miss

### # Associative mapping

→  $A[0][0]$  can be kept anywhere,  $A[0][1]$  can be kept anywhere and so on.

→ while replacing, replace the least recently used location.

Cache

Content of cache after pass.

block	$j=7$	$j=9$	$i=0$
0	$A[0][0]$	$A[0][8]$	$A[0][0]$
1	$A[0][1]$	$A[0][9]$	$A[0][1]$
2	$A[0][2]$	$A[0][2]$	$A[0][2]$
3	$A[0][3]$	$A[0][3]$	$A[0][3]$
4	$A[0][4]$	$A[0][4]$	$A[0][4]$
5	$A[0][5]$	$A[0][5]$	$A[0][5]$
6	$A[0][6]$	$A[0][6]$	$A[0][6]$
7	$A[0][7]$	$A[0][7]$	$A[0][7]$

Set  
0

For  $A[0][8]$  in  $j=9$  column we replace  $A[0][0]$  as it is least recently used. It is victim page.  
and for  $A[0][9]$  we use  $A[0][1]$  as victim page.

★ For  $i=9$ ;  $A[0][9]$  is directly available in 1<sup>st</sup> block  
So hit

Similarly: for  $i=8, 7, 6, 5, 4, 3, 2 \rightarrow$  hits

So total 8 hits.

$i=1$  we need  $A[0][1]$ ;  $\left\{ \begin{array}{l} \text{replace with } A[0][1] \\ \text{least recently used is } A[0][9] \text{ (victim page)} \end{array} \right.$

$i=0$  we need  $A[0][0]$ ;  $\left\{ \begin{array}{l} \text{least recently used is } A[0][8] \text{ (victim page)} \\ \text{is replaced with } A[0][0] \end{array} \right.$

# Set-Associative mapping

## Content of Cache after pass

Cache block	$j=3$	$j=7$	$j=8$	$i=5$
Set 0 { 0	$A[0][0]$	$A[0][4]$	$A[0][8]$	$A[0][4]$
1	$A[0][1]$	$A[0][5]$	$A[0][9]$	$A[0][5]$
2	$A[0][2]$	$A[0][6]$	$A[0][6]$	$A[0][6]$
3	$A[0][3]$	$A[0][7]$	$A[0][7]$	$A[0][7]$
4				
Set 1 { 5				
6				
7				

Set field is last bit.

So all of  $A[0][0], A[0][1], \dots, A[0][9]$  are in Set 0 as placed in column order.

↳ last bit of address is zero.

After  $j=3$ , we have the first column.

After  $j=7$ ; ~~for~~ for  $A[0][4]$  we have to replace  $A[0][0]$  as it was least recently used and so on.