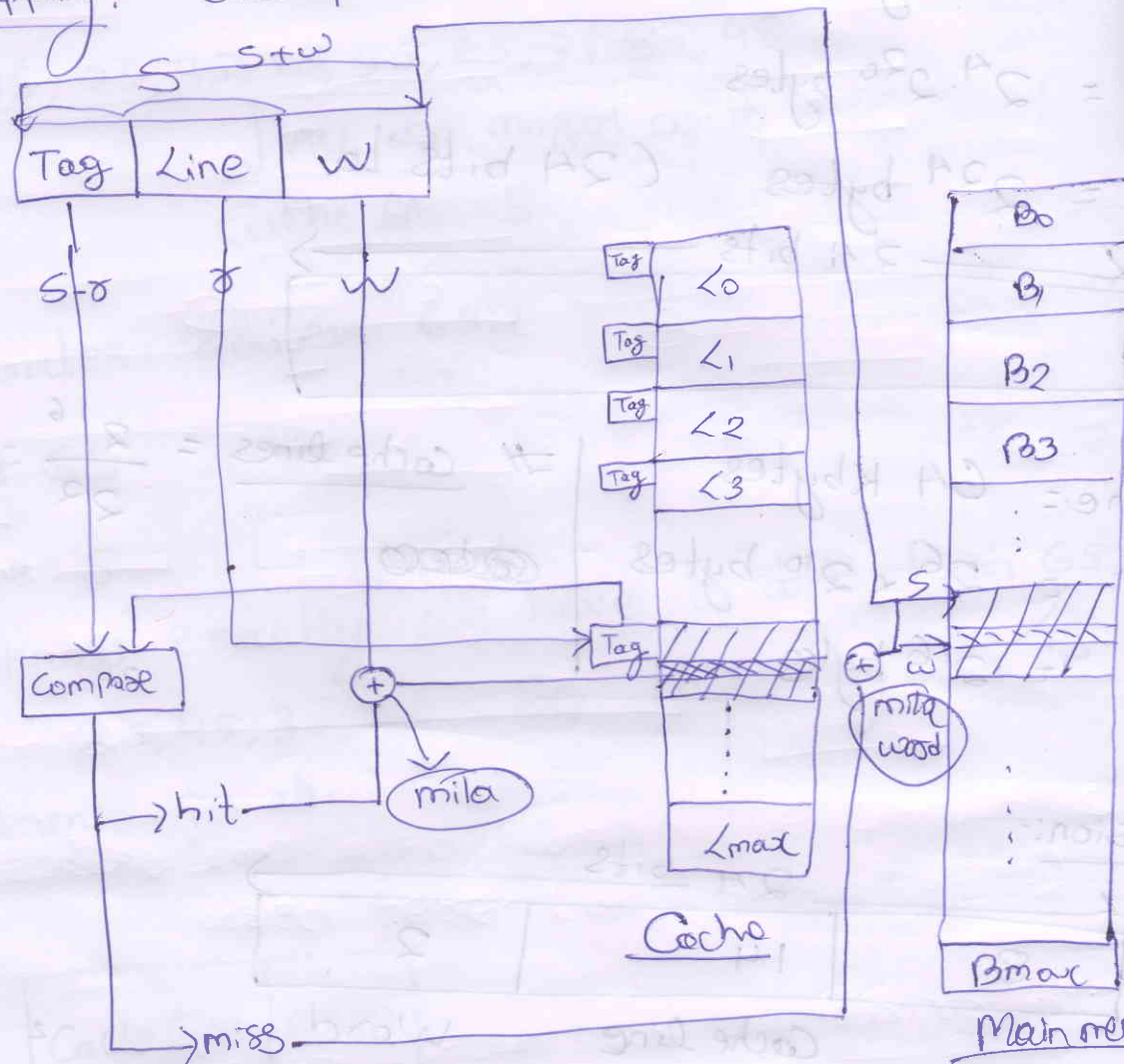


# Mapping Function (Hamacher/Stallings)

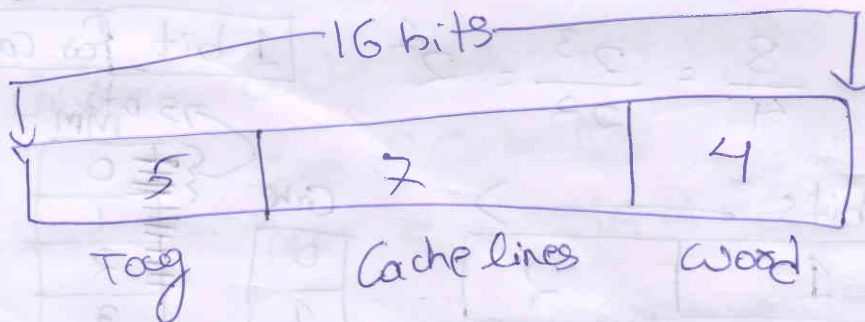
Direct mapping:- (Recap)



Size of Main memory = 64K words =  $2^6 \cdot 2^{10} = 2^{16}$

# blocks in cache = 128 =  $2^7$

# size of a block =  $2^4$  words



How do you divide mm address in Tag. and word for direct mapping?

## Disadvantages:-

Each block in main memory is mapped into a fixed cache line.

$S, SS \rightarrow$  Same cache line  $S$  (From previous example)

$$\begin{array}{c} \uparrow \quad \uparrow \\ C = A + B \end{array}$$

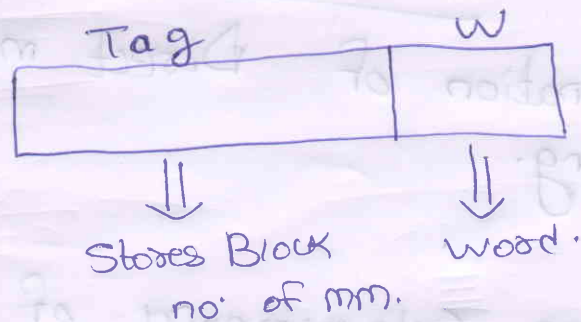
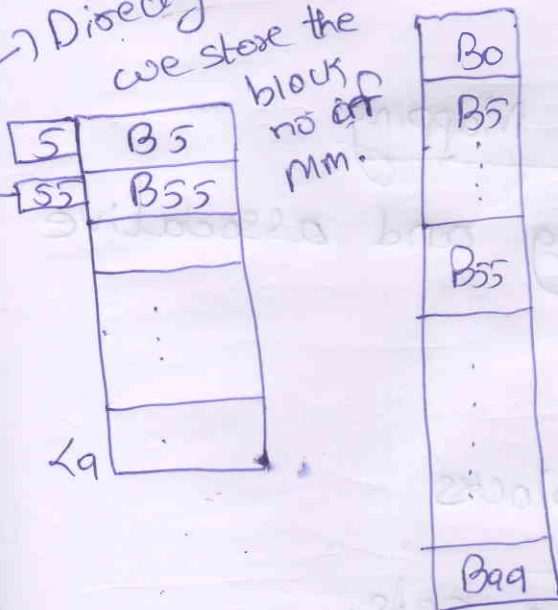
- First it fetches  $S$  with a hit in cache and  $S$  cache line is deleted
- Now for  $SS$ , there is a miss so that again  $SS$  is fetched into new  $S$ th cache line and the word is fetched.

So Repeated Cache miss occurs

## # Associative mapping:-

$\Rightarrow$  A block can be mapped in any cache line.

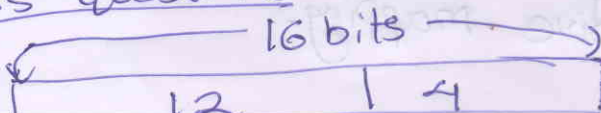
Directly in tag we store the block no. of mm.



Eg:- 6582

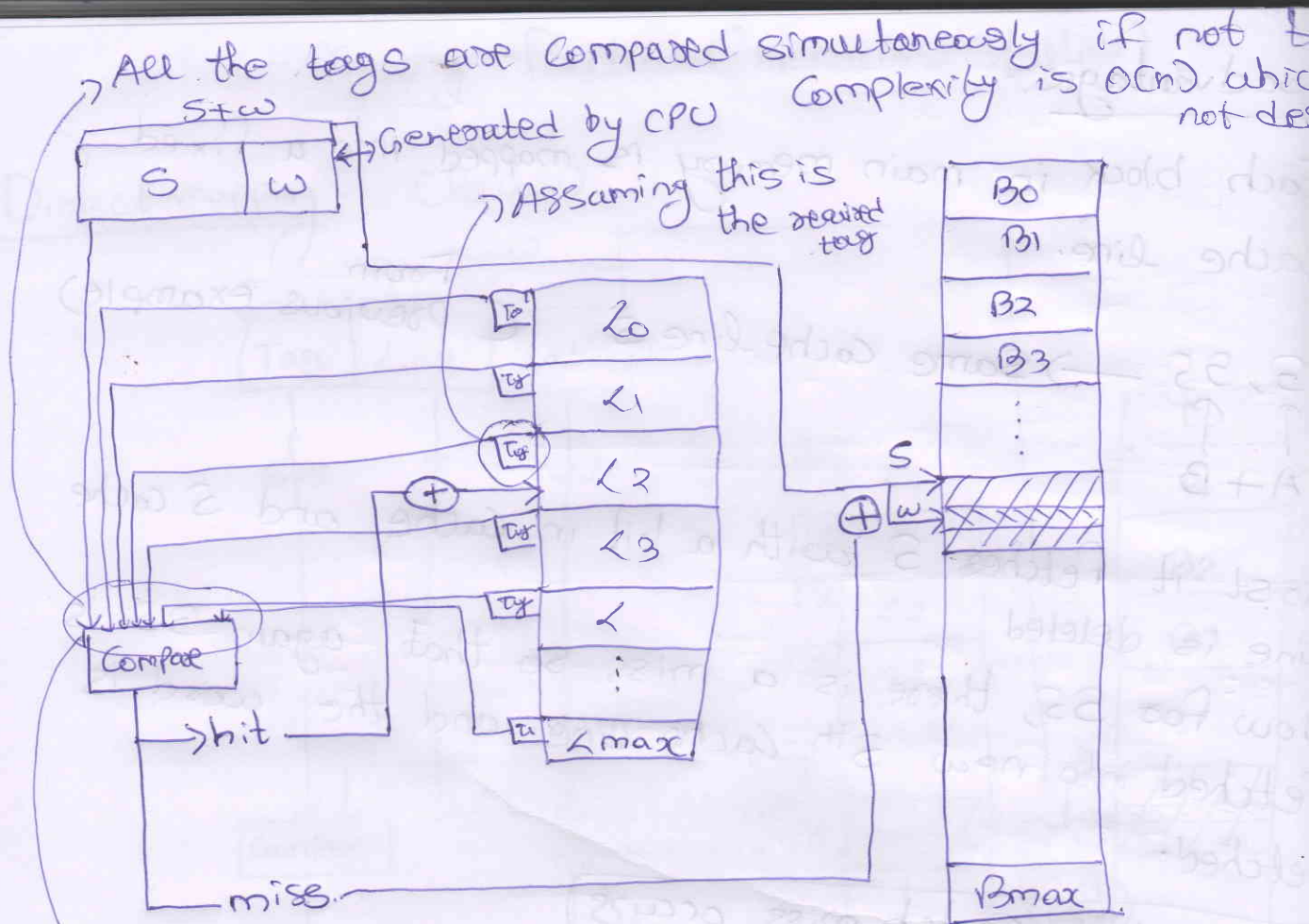


Eg:- This question



No. of Blocks in mm =  $\frac{2^{16}}{2^4} = 2^{12}$   
Tag stores Block no. so needs 12 bits





→ ~~Complicated~~ Complicated Comparative circuit is used here. (Out of scope of syllabus)

Drawbacks: Complex circuit for tag comparison.

## # Set-Associative Mapping

Combination of Direct mapping and associative mapping.

- Assume Cache memory of 10 blocks
- Cache lines are grouped into sets.
- Apply direct mapping with sets and then within a set apply associative mapping.

Note:- There is no sets in the mm.

$$S = j \text{ modulo } \# \text{set}$$

In the above example:-

If  $j = 56$ ,  $\# \text{sets} = 5$

$$S = 56 \text{ modulo } 5 \\ = 1.$$

~~S0~~ S1 is selected.

L0	}	S0
L1		
L2	}	S1
L3		
L4	}	S2
L5		
L6	}	S3
L7		
L8	}	S4
L9		