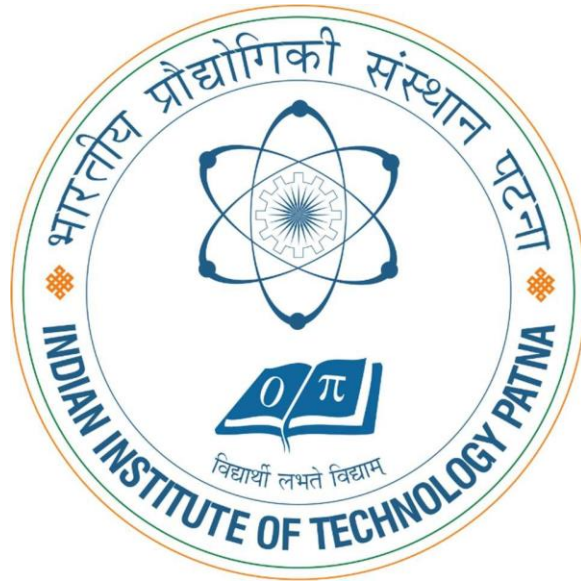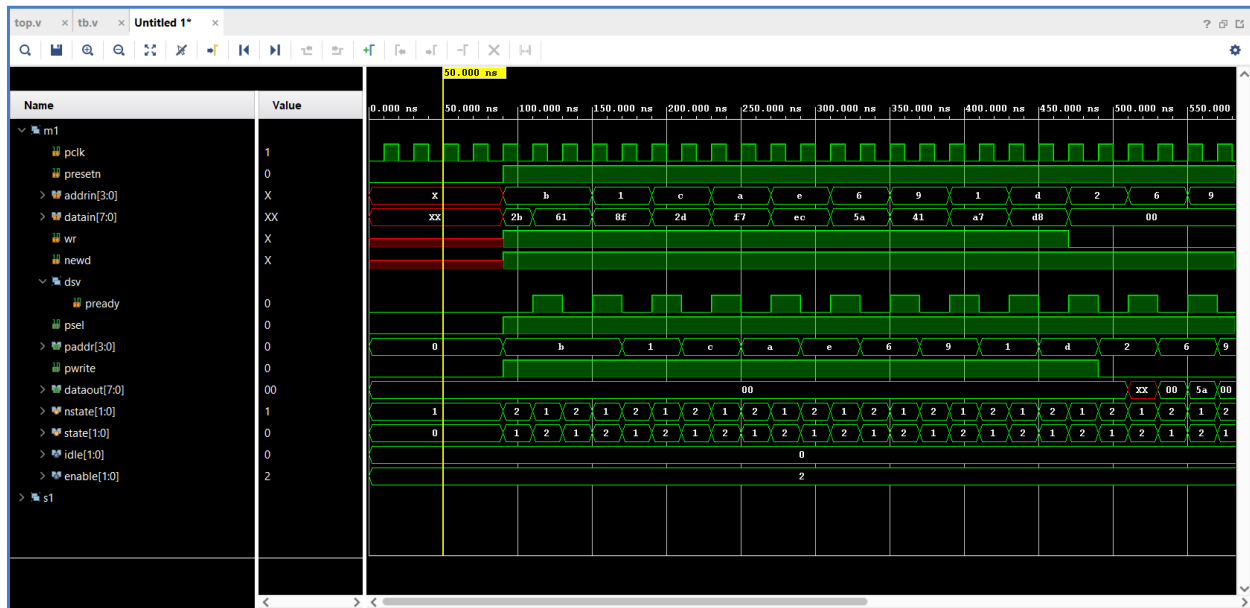# INDIAN INSTITUTE OF TECHNOLOGY PATNA
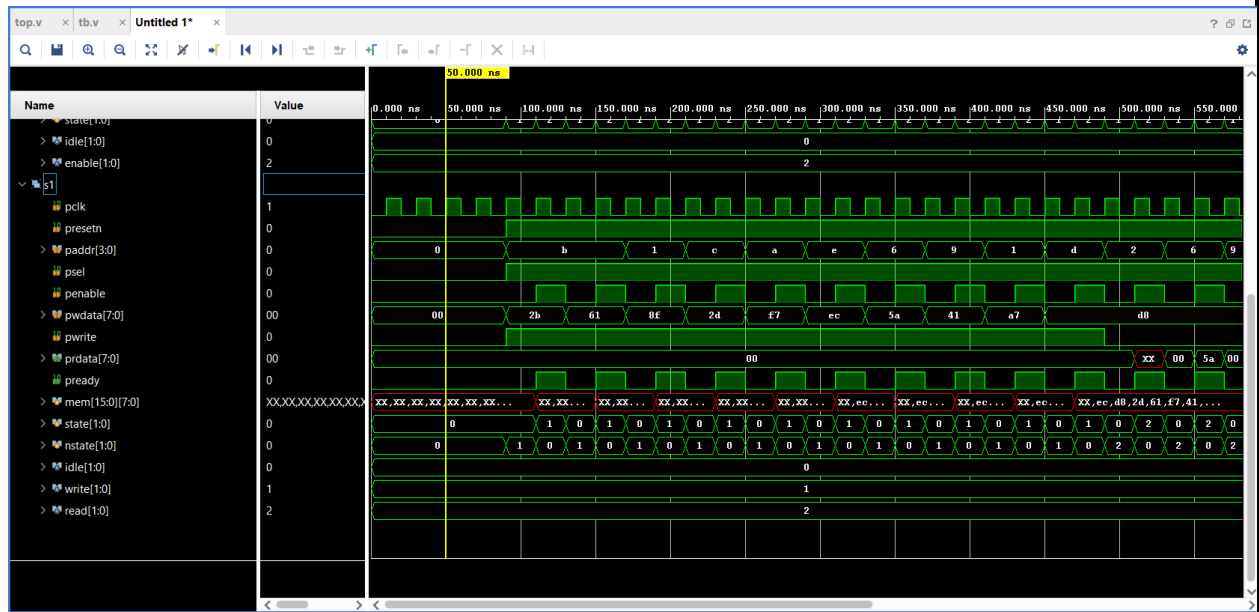


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# Results

:- 1) here m1 is the master and s1 is the slave

      2) in the testbench we are applying 10 random write and 10 random read operations to the dut

      3) we can see that when wr =1 we are generating 10 random datain values as 2b,61,8f,2d,f7, ec,5a,41,a7,d8

**Results:-** we can see we are writing 5a on address 6 in memory location and we are also able to read 5a from address 6 from salve memory

**Results:-** We can see in this waveform also that we write 41 on address 9 in the slave memory and we get the prdata also 41 from the memory address 9