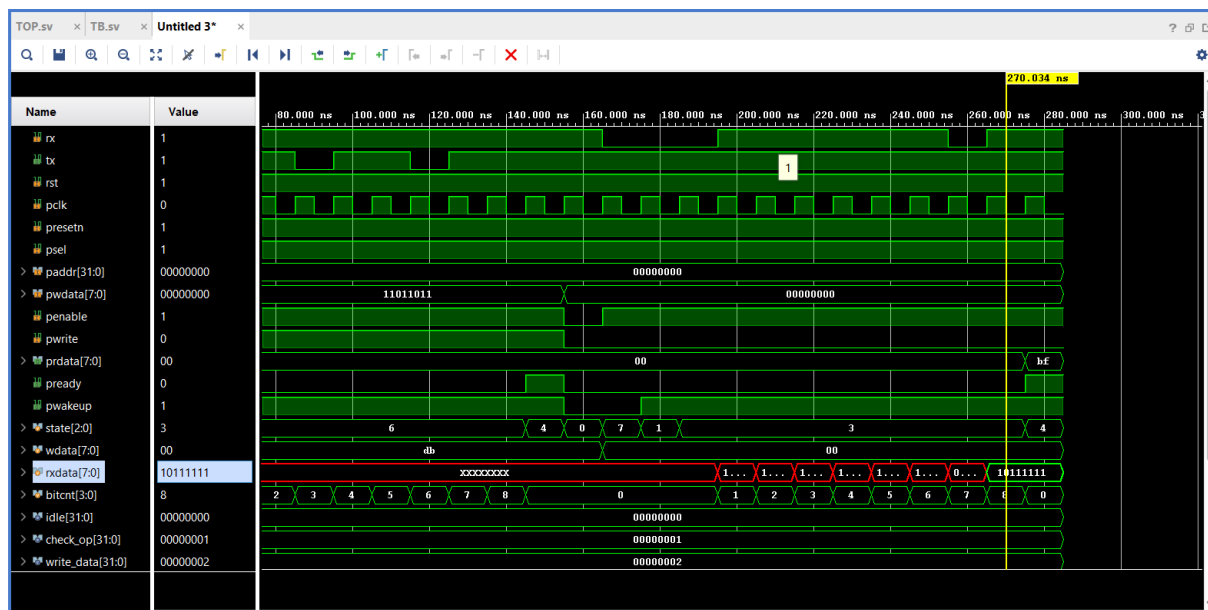Result:- we are transmitting 8 bits 11011011 to the receiver to start the transmission we can see tx initially becomes 0 then 11011011 is transmitted serially and then stop bit and we can also see that pready also becomes 1 after sending the stop bit.

We can see that we have received 10111111 (initially rx goes low to start the data transmission then we can see we receive 1 for the six consecutive clk then 0 then 1 after right shifting the data it becomes 10111111)that we can send on the rxdata as output.