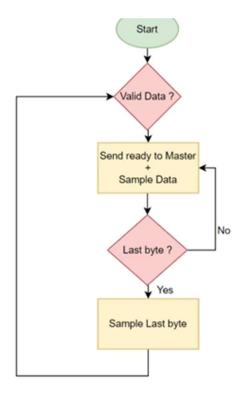
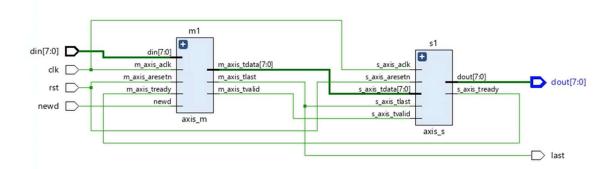


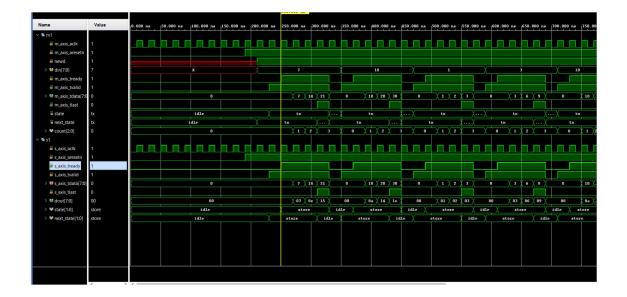
Master state diagram



Slave state diagram



Master and slave interconnection



Results:- at 250 ns tready and tvalid both are high, so data transmission to slave starts and din valve at this time is 7 so first data byte will be 7*0=0, after that we are transmitting 7*1=7 and after that 7*2=14 after that 7*3=21 and that is the last byte of our stream and we can verify this in the above waveforms similarly next din=10 so we transmit 0,10,20,30 to the slave.