



CERTIFICATE OF COMPLETION

# AMBA APB in Verilog

**Priyanshu Gupta**

Completion Date : 2025-07-30

Course Length : 4.0 Hour(s)

Certificate No. : 01985a87-16aa-7b5f-a196-f1722a211dc0

Certificate URL : <https://namaste-fpga.com/#/certificate/01985a87-16aa-7b5f-a196-f1722a211dc0>

A handwritten signature in black ink, appearing to read "Kumar Khandagle".

**Kumar Khandagle**

(Instructor, Namaste FPGA Learning Labs)

