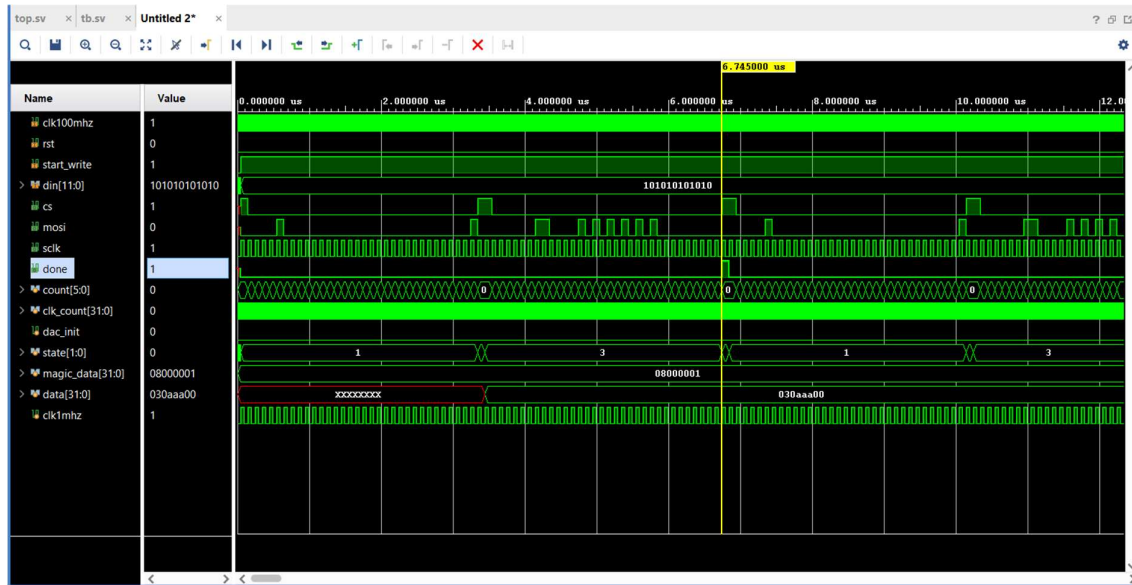


## RESULTS:-



We can see that after the send\_data state (3<sup>rd</sup> state) we have successfully sent the data to the output pin of the DAC converter (done has become 1).