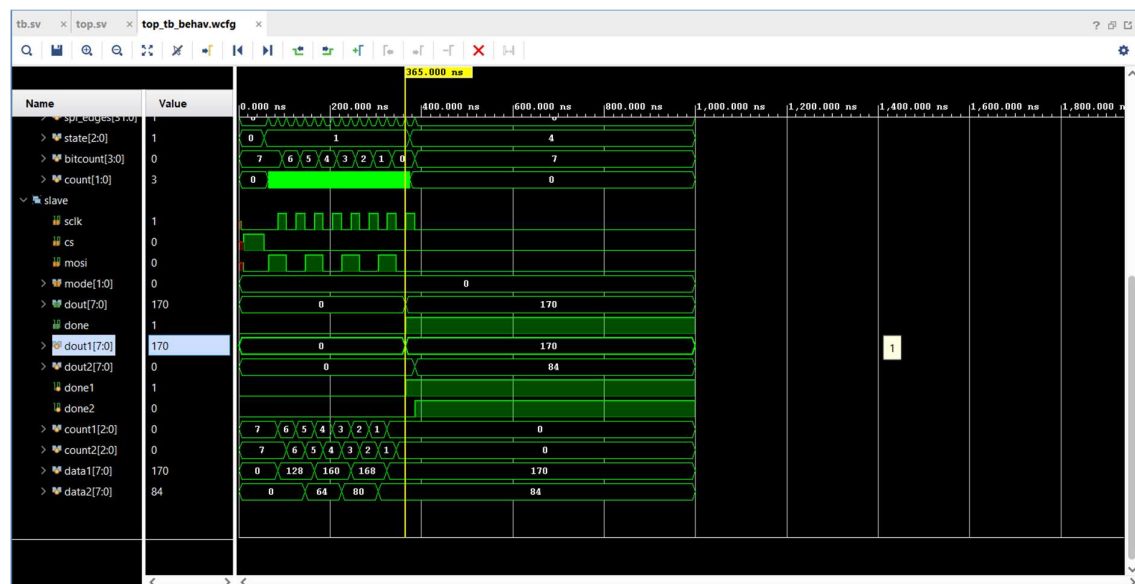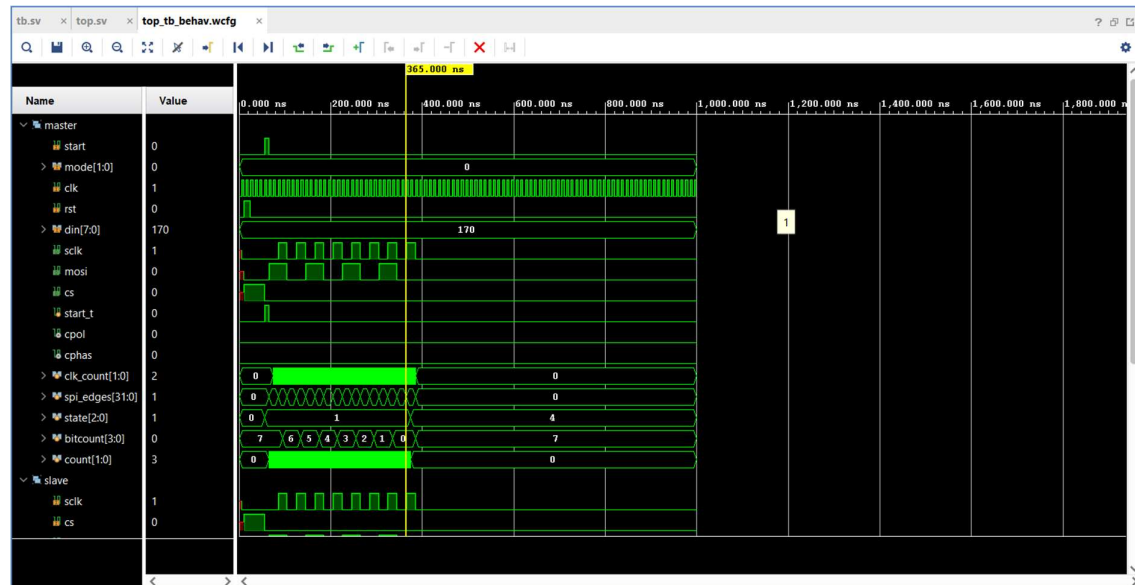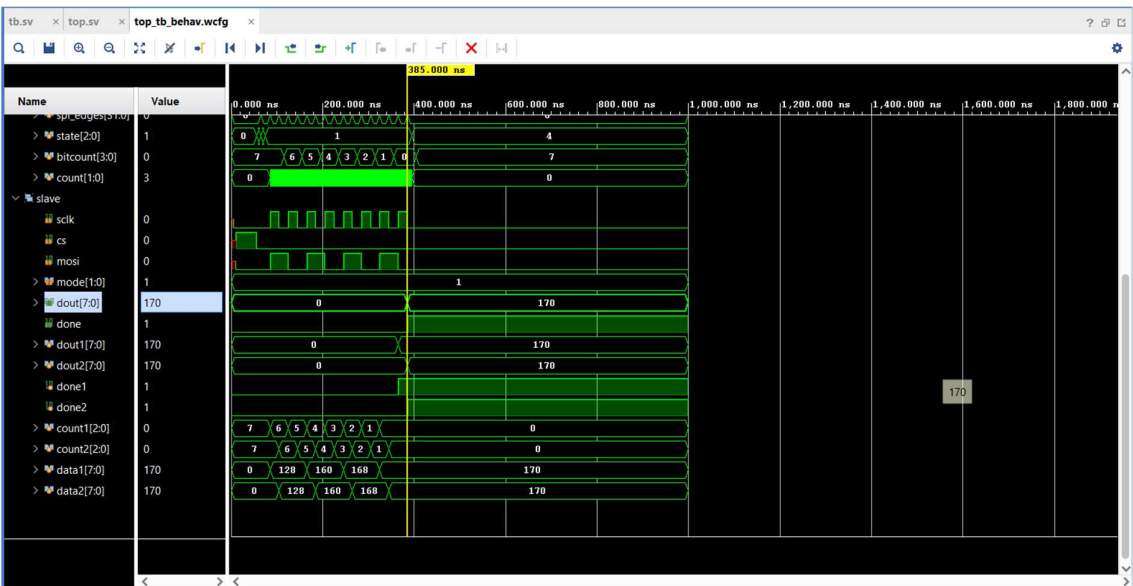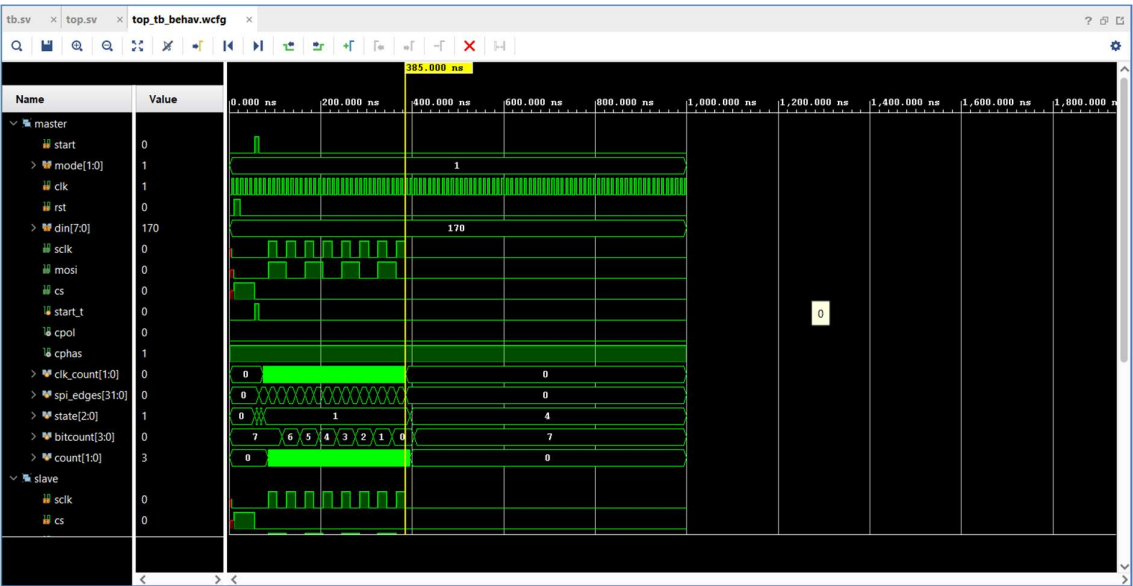RESULTS OF THE DIFFERENT MODES
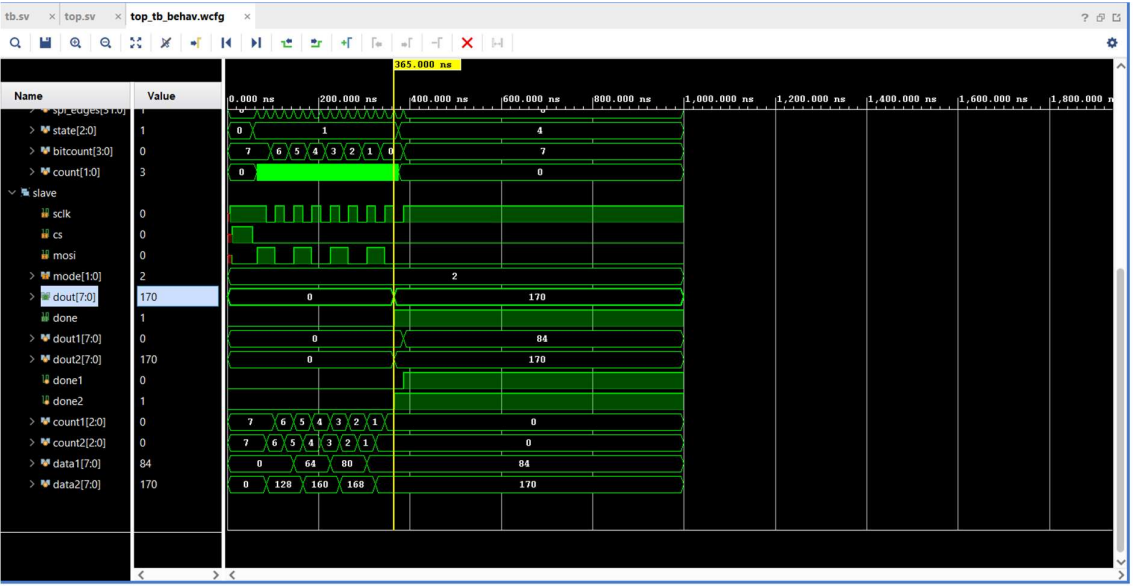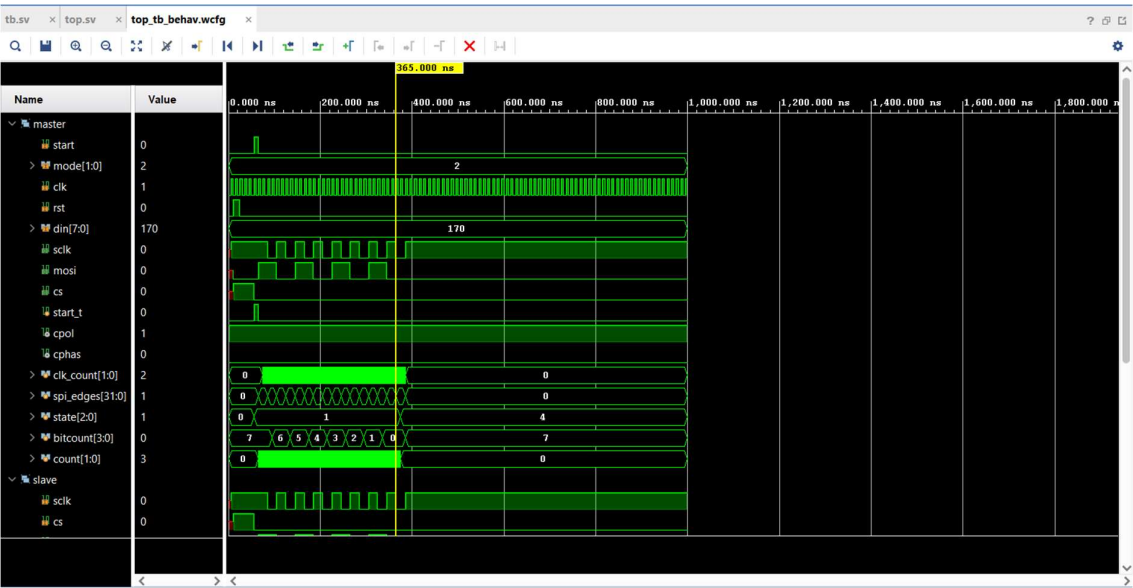
1)MODE 0(CPOL=0,CPHAS=0)

WE ARE TRANSMITTING 8 BIT DATA 10101010(WHICH ISEQUIVALENT TO 170 IN DECIMAL)FROM
MASTER TO SLAVE IN MODE 0 BOTH CPOL AND CPHAS ARE 0 SO INTIAL VALUE OF SCLK WHICH IS
DECIDED BY CPOL IS 0 AND SLAVE WILL BE START SAMPLING THE DATA FROM THE FIRST EDGE OF
SCLK (IN THIS CASE FIRST EDGE OF SCLK IS POSEDGE) IN SLAVE I AM SAMPLING MOSI ON THE BOTH
EDEGS OF THE SCLK THE DATA SAMPLED ON THE POSEDGE IS STORED IN THE DATA1 AND DONE1
BECOMES HIGH AND DATA SAMPLED ON THE NEDEGDE IS STORED IN THE DATA2 AND DONE2
BECOMES HIGH AFER SAMPLING THE DATA , NOW BASED ON THE VALUE OF MODE I CHOOSE DATA1
OR DATA2 AS OUTPUT DOUT SAME FOR THE DONE ALSO

MODE 1)(CPOL=0,CPHAS=1)

MODE 2) (CPOL=1,CPHAS=0)

MODE 3) (CPOL=1,CPHAS=1)