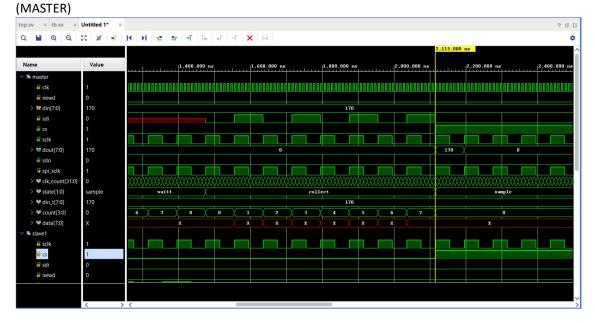
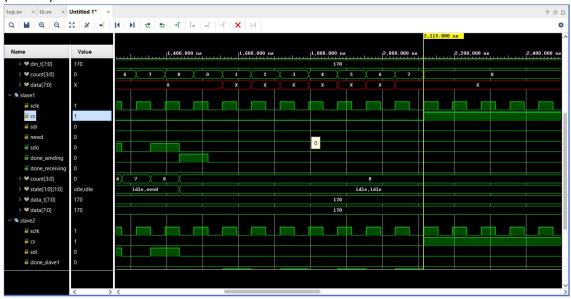
## RESULTS:-



## (SLAVE1)



IN THE TESTBENCH WE HAVE SENT THE DATA 10101010 FORM THE MASTER TO THE SLAVE1 IN THE(AS WE CAN SEE THAT DIN= 10101010) WE CAN ALSO SEE IN THE ABOVE IMAGE THAT SLAVE 1 DATA REGISTER IS ALSO GOT UPDATED WITH 170 SO MASTER HAS SUCESSFULLY SENT DATA TO THE SLAVE1 AND SLAVE1 HAS SUCCESSFULLY RECEIVED IT

NOW SLAVE1 IS SENDING THE SAME DATA TO THE SLAVE 2 AND WE CAN SEE IN THE SALVE2 IMAGE THAT SLAVE2 DATA REGISTER IS ALSO UPDATED WITH 170 AFTER COMPLETION OF THE RECEIVE STATE

AFTER THAT SLAVE 2 SENDS THIS DATA TO THE MASTER AND WE CAN SEE IN MASTER IMAGE AFTER

THE COMPLETION OF MASTER'S COLLECT STATE DOUT VALUE HAS BECOME 170 IT SHOWS THAT OUR DESIGN IS WORKING AS EXPECTED.

## 3) (SLAVE2)

