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S.B. Roll No……

COMPUTER ARCHITECTURE

4th Exam/Comp/IT/CSE/3621/Nov'18

Duration: 3Hrs. **M.Marks:75**

SECTION-A

Q1. Fill in the blanks. **15x1=15**

a. BCD stands for _____
b. A byte contains _____ Bits.
c. ASCII stands for _____
d. Stack is based on _____ Principle.
e. Control word is a collection of _____ bits.
f. CISC stands for _____
g. 2's complement of 000001 is _____?
h. 1's complement of 111101 is _____?
i. Register and memory are the types of _____
j. During DMA transfer, the _____ is idle and has no control of the memory buses.
k. DRAM stands for _____
l. MMU stands for Memory management units. (T/F)
m. DVD stands for _____
n. EPROM stands for_____
o. WORM stands for _____

SECTION-B

Q2. Attempt any five questions. **5x6=30**

i. What are the important characteristics of RISC architecture?
ii. Explain multiplication algorithm of 2 numbers with an example.
iii. Explain any 3 number systems with an example each.
iv. Explain any 6 addressing modes.
v. Differentiate between RISC and CISC.
vi. Explain various components of CPU.
vii. Explain control word with the help of an example.
viii. Explain the concept of pipeline processing.

SECTION-C

Q3. Attempt any three questions. **3x10=30**

a. Explain general register organization with the help of a diagram.
b. Explain DMA controller with the help of a diagram.
c. Explain memory hierarchy in detail with the help of a diagram.
d. What is an instruction format? Explain different types of instructions.
e. Convert: i) $(10101)_2$ to decimal. ii) Decimal $(25)_{10}$ to binary.

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COMPUTER ARCHITECTURE

4th Exam/Comp/IT/CSE/3621/May'18

Duration: 3Hrs. **M.Marks:75**

SECTION-A

Q1. Do as directed. **10x1.5=15**

a. Octal number system is _____ number system.
b. Two types of Parity systems are _____ and_____.
c. ASCII is _____ bit code.
d. RAM can by classified into _____ and _____.
e. WORM stands for _____.
f. Memory accessed by its contents is called _____ memory.
g. 2's Complement of $(12)_{10}$ is _____.
h. Floating Point Numbers has two parts _____ and _____.
i. _____ Algorithm multiply binary numbers in signed 2's complement representation.
j. The ratio of number of hits divided by total CPU references to memory is _____.

SECTION-B

Q2. Attempt any five questions. **5x6=30**

i. Explain RAM chip with diagram.
ii. Define Addressing Modes. Explain various types of Addressing Modes.
iii. What is parallel processing? Explain with diagram.
iv. Explain DMA with diagram.
v. Define Stack. Explain register stack.
vi. Explain Memory Map.
vii. What is Gray Code? Explain?

SECTION-C

Q3. Attempt any three questions. **3x10=30**

a. Explain various modes of data transfer.
b. Define Auxiliary Memory? Explain Magnetic disks.
c. What is instruction format? Explain in detail.
d. Explain algorithm for addition and subtraction.

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COMPUTER ARCHITECTURE

4th Exam/Comp/IT/CSE/3621/Sep'2020

Duration: 1.15 Hrs. **M.Marks:25**

SECTION-A

Q1. Attempt any three questions. **3x5=15**

a. Explain RAM chip with diagram.
b. Explain SHIFT MICRO OPERATIONS
c. Write down characteristics of RISC and CISC.
d. Explain Addition algorithm
e. What do you mean by addressing mode? Explain any two.
f. Write short note on Gray code.
g. What do you mean by FIFO buffer? Give neat and clean diagram of 4X4 FIFO buffer.
h. Write short note of cache memory.
i. Write short note on interrupt initiated I/O operation.

SECTION-B

Q2. Attempt any one questions. **1x10=10**

i. What is a DMA Controller? Describe along with its working using suitable diagrams.
ii. Explain memory hierarchy in detail with the help of a diagram
iii. Convert: i) $(10001110)_2$ to decimal. ii) Decimal $(25)_{10}$ to binary.

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COMPUTER ARCHITECTURE

4th Exam/COMP/IT/CSE/3621/Jun'2021

Duration: 1.15Hrs. **M.Marks:25**

SECTION-A

Q1. Attempt any three questions. **3x5=15**

i. What do you mean by addressing mode? Explain any five.
ii. Explain Daisy Chain priority interrupt.
iii. Explain the concept of pipeline processing.
iv. Explain the various instruction formats used in CPU with suitable examples?
v. Write in brief about Cache memory.
vi. Give a short note on Memory connection to CPU.
vii. Differentiate between RISC vs CISC characteristics.

SECTION-B

Q2. Attempt any one question. **1x10=10**

a. What is a DMA Controller? Describe along with its working using suitable diagrams.
b. Write a short note on : i) Input-Output Interface ii) Booth's multiplication algorithm
c. Explain general register organization with the help of a diagram.
d. What is Stack organization? Enlist its various operations with their microoperations.

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COMPUTER ARCHITECTURE

4th Exam/COMP/IT/CSE/3621/Jun'2022

Duration: 3Hrs. **M.Marks:75**

SECTION-A

Q1. Do as directed. **15x1=15**

a. Static and _____ memory are two main types of RAM.
b. What do you mean by DMA?
c. Briefly discuss Instruction format.
d. Define Cache memory.
e. Define Gray code.
f. CISC stands for _____.
g. LIFO stands for _____
h. What is stack?
i. What do you mean by binary number system?
j. 2's compliment of 1010 is _____
k. A byte contains _____ bits.
l. Convert decimal (25) into binary.
m. Define BCD
n. Define Parallel Processing.
o. What do you mean by ALU?

SECTION-B

Q2. Attempt any six questions. **6x5=30**

i. Differentiate between RISC & CISC.
ii. Discuss concept of memory hierarchy.
iii. Explain various components of CPU.
iv. Define control word. Discuss it with an example.
v. Explain data transfer techniques in brief.
vi. What is 2's compliment? Explain with an example
vii. What is an interrupt? Explain basic types of interrupt.
viii. Write a short note on stack organization.

SECTION-C

Q3. Attempt any three questions. **3x10=30**

a. Define Addressing modes. Describe various addressing modes with an example.
b. Explain DMA controller with diagram in detail.
c. Explain general register organization with suitable diagram.
d. Describe concept of virtual memory using paging and segmentation.
e. Explain cache memory organization in detail.

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COMPUTER ARCHITECTURE
4th Exam/CSE/IT/0495/Jun'2022
(For 2018 Batch Onwards)

Duration: 3Hrs. **M.Marks:75**

SECTION-A

Q1. Do as directed. **15x1=15**

a. BCD stands for_____.
b. ASCII stands for _____.
c. An _____ code field specifies the operation to be performed.
d. Fixed point number may not represent integers. (True/False)
e. The operation of insertion in a stack is known as PUSH. (True/False)
f. EBCDIC stands for_____.
g. ROM stands for_____.
h. RAM stands for_____.
i. Magnetic disks, magnetic tapes are examples of Auxiliary memory. (True/False)
j. A dedicated computer is assigned to one task.(True/False)
k. 2's complement = 1's complement + _____.
l. Serial interface is faster than parallel interface. (True/False)
m. MIMD stands for _____.
n. A nibble contains _____ bits.
o. WORM stands for _____.

SECTION-B

Q2. Attempt any six questions. **6x5=30**

i. Write a note on Gray code and BCD Code.
ii. Differentiate between CISC and RISC?
iii. Write a short note on interrupt cycle.
iv. Differentiate between primary memory and secondary memory.
v. What do you mean by addressing mode? Explain any three.
vi. Explain register and memory stack in detail.
vii. Convert the following:- a) $(0.1101)_2=(?)_{10}$ b) $(131)_8 = (?)_{10}$
viii. Explain 2's complement with the help of an example.

SECTION-C

Q3. Attempt any three questions. **3x10=30**

a. Explain the concept and working of DMA Controller in detail.
b. Explain memory hierarchy in detail with help of a diagram.
c. Describe the following:- a) Input-Output Interface b) Associative Memory
d. Explain the Booth multiplication algorithm with an example.
e. What do you mean by pipelining? Explain with the help of example.

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