VDD VDD 5VSYS 5VSYS

C1 C2 C3 C4
100n 10u 100n 10u

GND GND GND GND

J2
TFM-104-02-L-DH
GND

UART1_RX 01 01 02 02 UART1_TX
UART2_RX 03 03 04 04 UART2_TX
UART3_RX 05 05 06 06 UART3_TX
         07 07 08 08
GND

J3
TFM-102-02-L-DH
01 01 02 02
03 03 04 04
5VSYS GND

U1
CP2105-F01-GM

Pin	Signal		Pin	Signal
1	*SUSPEND/RI_SCI		25	EPAD → GND
2	GND → GND		24	GPIO.0_SCI/DCD_SCI
3	D+ ← USB_DM		23	GPIO.1_SCI/DTR_SCI
4	D- ← USB_DP		22	GPIO.2_SCI/DSR_SCI
5	VIO		21	TXD_SCI → UART3_TX
6	VDD ← VDD		20	RXD_SCI → UART3_RX
7	REGIN ← 5VSYS		19	RTS_SCI
8	VBUS		18	CTS_SCI
9	*RST		17	*SUSPEND/RI_ECI
10	CTS_ECI		16	NC/DCD_ECI/VPP
11	RTS_ECI		15	GPIO.0_ECI/DTR_ECI
12	RXD_ECI		14	GPIO.1_ECI/DSR_ECI
			13	TXD_ECI → UART2_TX

5VSYS R2 24k
VDD R1 4.3k
R3 47k
GND
C5 100n
GND
UART2_RX

C6 4.7u
GND

J1
Conn_02x10_Odd_Even

1	2
3	4	→ 5VSYS
5	6
7	8	→ UART2_TX
9	10	→ UART2_RX
11	12
13	14
15	16
17	18
19	20
21	22
23	24
25	26
27	28	→ USB_DM
29	30
31	32
33	34
35	36
37	38
39	40

USB_DP
GND GND

D1
PESD3V3L4UF,115

Pin	Signal		Pin	Signal
1	cathode_(diode_1) ← UART1_TX		6	cathode_(diode_4) → UART2_TX
2	common_anode_1 ← GND		5	common_anode_2 → GND
3	cathode_(diode_2) ← UART1_RX		4	cathode_(diode_3) → UART2_RX

D2
PESD3V3L4UF,115

Pin	Signal		Pin	Signal
1	cathode_(diode_1) ← UART3_TX		6	cathode_(diode_4)
2	common_anode_1 ← GND		5	common_anode_2
3	cathode_(diode_2) ← UART3_TX		4	cathode_(diode_3)