USB_Hub

File: USB_Hub.kicad_sch

UART_HUB

File: UART_HUB.kicad_sch

Radxa Shield Rev. 1.1

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File: Radxa Shield Rev. 1.1.kicad_sch

Title: Radxa Shield Rev. 1.1

Size: A3	Date: 2024-08-16	**Rev: V1.1**
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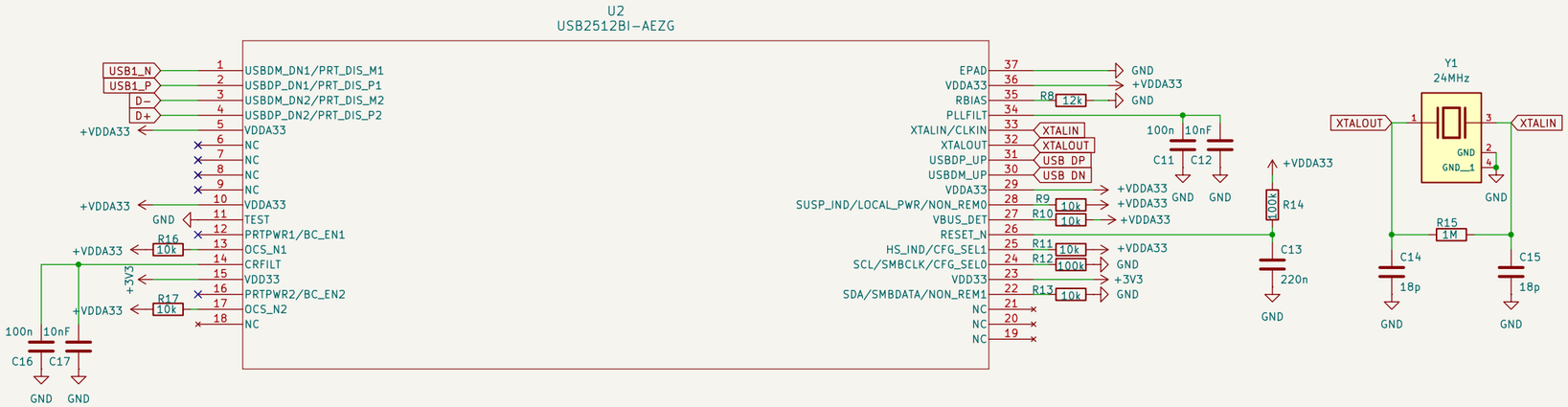

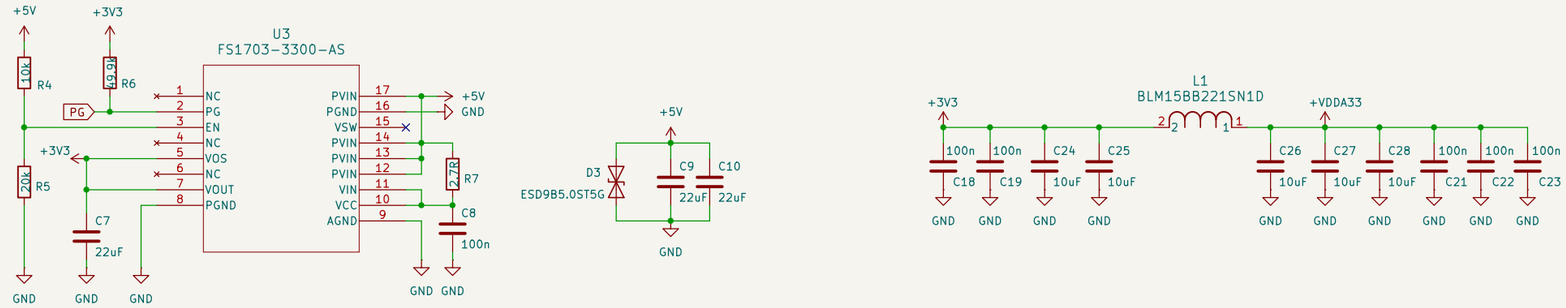

Input voltage(Vin).
Tie to PVIN through a 2.7Ω resistor.
R7=2.7Ω
– power good status
R6=49.9kΩ(Pull up to VCC – pin 10 or an external bias voltage – with a 49.9kΩ resistor)

2.USB2512BI-AEZG

– RBIAS:
USB Transceiver Bias: a 12.0 k (+/– 1%) resistor is attached from ground to this
pin to set the transceiver's internal bias settings.
R8=12k
–CFG_SEL1, CFG_SEL0 (pins 25 & 24)
Either pin can be driven low with an external 100.0K resistor to digital ground or a 10.0K resistor to VDD33
R11=10k, R12=100k
–NON_REM1, NON_REM0 (pins 22 & 28)
Either pin can be driven low with an external 100.0K resistor to digital ground or a 10.0K resistor to VDD33.
R9=R13=10k
–HS_IND (pin 25)
1 = Hub Is Connected At High Speed (480 Mbit/s)
R11=10k
–SUSP_IND (pin 28)
1 = Hub Is Configured And Is Active (Not In Suspend)

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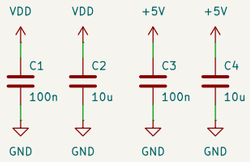

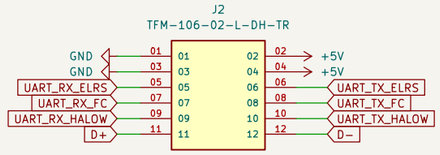

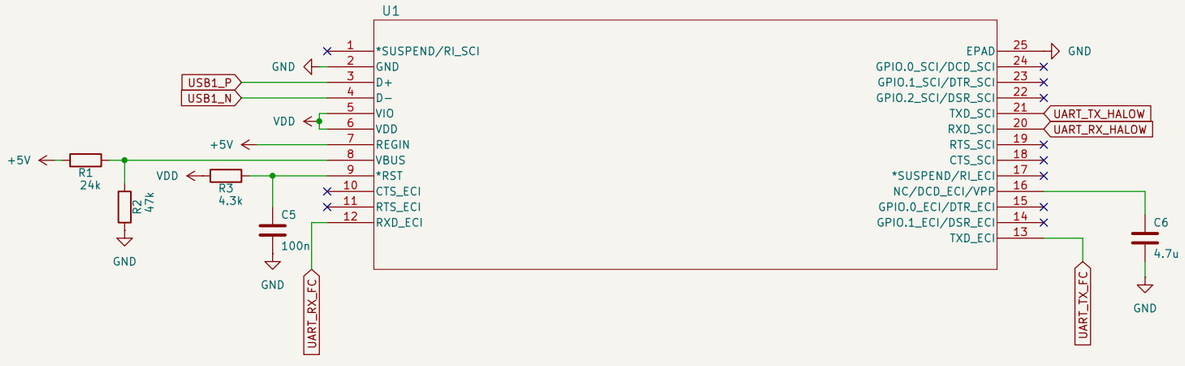

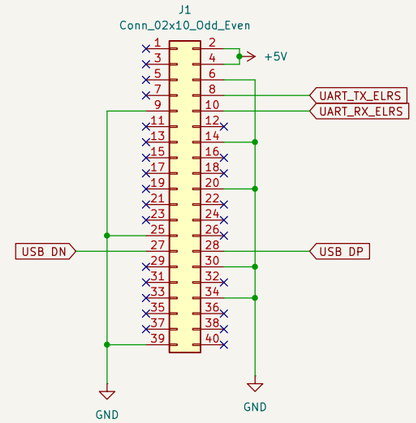

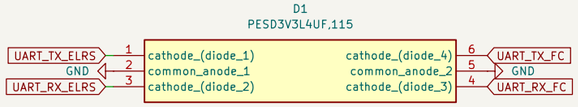

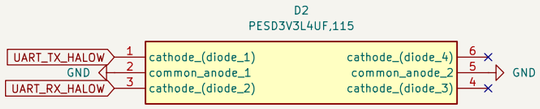

−VBUS
For self−powered systems where VDD and VIO may be unpowered when VBUS is connected
to 5 V, a resistor divider (or functionally−equivalent circuit) on VBUS is required to meet the
absolute maximum voltage on VBUS specification in the Electrical Characteristics section.
R1=24k, R2=47k

UART_HUB

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