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APPROVAL SHEET

承 认 书 记录编号:

版本: v0.0

Customer 客户名称	
Part NO. 产品型号	Z280IT010
Product type 产品内容	Mode: Transmissive type .Normally white. TFT LCD Module LCD Module: Graphic 240RGB*320Dot-matrix
Remarks 备注栏	□APPROVAL FOR SEPCIFICATIONS ONLY ■APPROVAL FOR SEPCIFICATIONS AND SAMPLE
Signature by Customer: 客户确认签章	•

展恒安确认

核准	审核	定制

客户确认

核准	审核	审核

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1.General Description

Z280IT010 is a 240RGB*320 dots matrix TFT LCD module. It has a TFT panel composed of 720 sources and 320gates. The LCM can be easily accessed by micro-controller.

2. Features

Display Mode	Transmissive
	a-TFT
Display Format	Graphic 320RGB*480 Dot-matrix
Input Data	4 线串口(SPI-4WIRE)
Viewing Direction	12 o'clock
Drive	ILI9341

3. Mechanical Specification

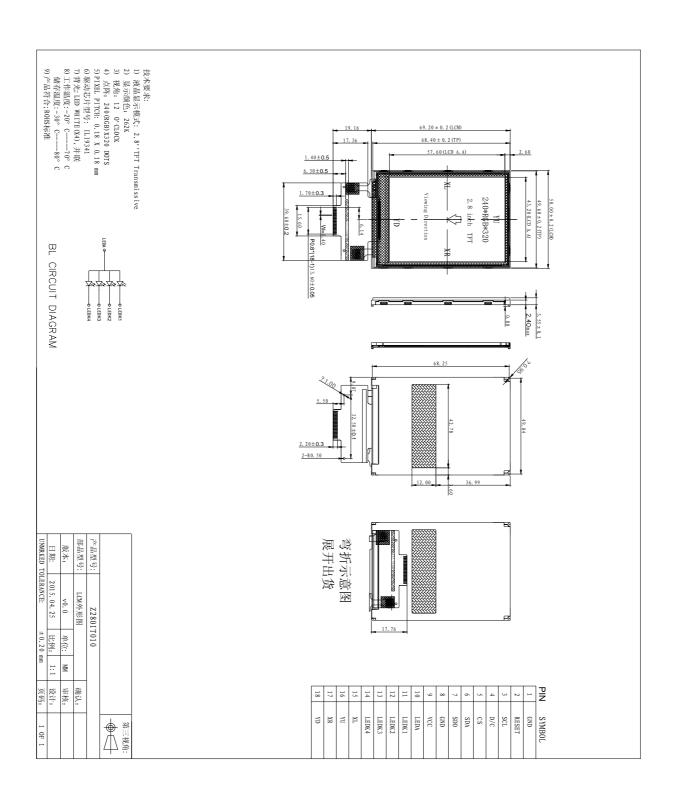
Item	Specifications	Unit
Dimensional outline	50.00(W)*69.20(H)*3.60MAX (T)	
Dimensional outline	(FPC not include)	mm
Resolution	240RGB*320	dots
LCD Active area	43.20(W)*57.60(H)	mm
Pixel size	0.180(W)*0.180(H)	mm

4. Mechanical Dimension

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5. Maximum Ratings

Item	Symbol	Min	Max	Unit	Note
Supply voltage	V	-0.3	4.6	V	
Operating temperature	$\mathbf{V_{T}}$	-0.3	Vcc+0.3	V	
Storage temperature	T _{OPR}	-20	70	${\mathbb C}$	
Storage temperature	T _{STR}	-30	80	${\mathbb C}$	

6. Electrical Characteristics

Item		Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage	Logic	V_{CC}		2.7	2.8	3.3	V
Input Voltage	H level	T _{IH}		0.8*IOVCC		IOVCC	V
input voitage	L level	T_{IL}		-0.3		0.2* IOVCC	V
Storage temperature		${ m I_{DD}}$	With internal voltage generation $V_{\rm CC}$ =2.8V; $T_{\rm emp}$ =25°C			TBD	mA

7. Backlight Characteristic

Item	Symbol	Min	Typical	Max	Unit
LED module Forward voltage	$\mathbf{V}_{\mathbf{LED}}$	3.0	3.2	3.4	V
LED module current	V_{LED}		90		mA
L/G Surface Luminance ★1	L_{S}	3200			Cd/m³
LCM Surface brightness uniform ★2	L_{D}	80			%

★ 1Test condition is:

- (a) Center point on active area.
- (b)Best Contrast.

★2Uniform measure condition:

- (1) Measure 9 point. Measure location show below;
- (2)Uniform=(Min. brightness/Max. brightness)*100%

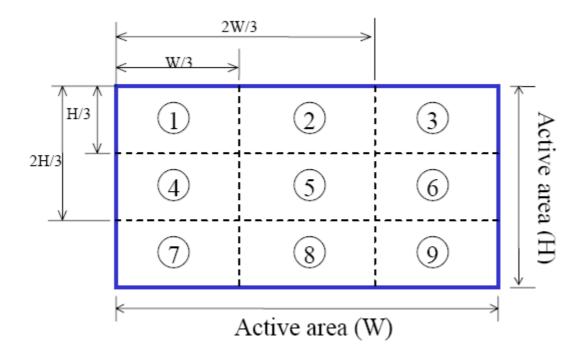
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(3)Best Contrast.



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8. Module Function Description

8.1Pin Descriptions

PIN No.	Symbol	Description
1	GND	Ground (接地脚)
2	RESET	LCM Reset pin Signal is active low. (屏复位脚,低电平复位)
3	SCL	This pin is used serial interface clock in 4-wire 8-bit serial data interface. (4线串口的时钟信号) If not used, this pin should be connected to IOVCC or GND. (不用时接IOVCC或者接地)
4	D/C	- 4-line system (D/CX): Serves as command or parameter select. Fix to IOVCC level when not in use. (4 线串口的寄存器/数据选择,不用时接 IOVCC)
5	CS	Chip select pin ("Low" enable) (屏驱动芯片片选脚,低电平有效)
6	SDA	Serial input signal. The data is applied on the rising edge of the SCL signal. If not used, fix this pin at IOVCC or GND (串口数据输入信号, 不用时接 I0VCC 或者接地)
7	SD0	Serial output signal. The data is outputted on the falling edge of the SCL signal. If not used, open this pin (串口数据输出信号,不用时悬空)
8	GND	Ground (接地脚)
9	VCC	Power supply for LCM (2.8V-3.3V) (屏供电脚))
10	LEDA	Anode of Backlight (3.0V-3.4V Typical:3.2V) (背光正极供电脚,电压范围:3.0-3.4V,典型值:3.2V)
11	LEDK1	Cathode of Backlight (背光负极供电脚)
12	LEDK2	Cathode of Backlight

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		(背光负极供电脚)
13	LEDK3	Cathode of Backlight (背光负极供电脚)
14	LEDK4	Cathode of Backlight (背光负极供电脚)
15	XL	Touch panel control pin (触摸屏控制脚)
16	YU	Touch panel control pin (触摸屏控制脚)
17	XR	Touch panel control pin (触摸屏控制脚)
18	YD	Touch panel control pin (触摸屏控制脚)

关于供电说明:

IOVCC 和 VCC 连一起,用 2.8V-3.3V 供电,背光 LED 可以单独供电 (3.0-3.4 V),也可以和 VCC 共用一组电压 (A 为正接 VCC, K 连一起作 为负接地).

8.2 Timing characteristics.

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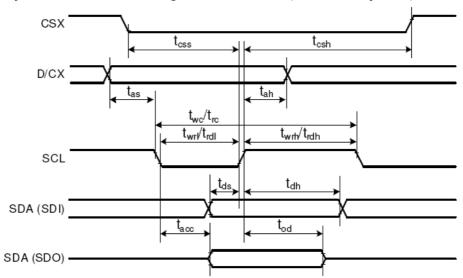
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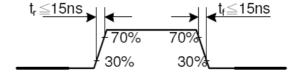
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18.3.4 Display Serial Interface Timing Characteristics (4-line SPI system)



Signal	Symbol	Parameter	min	max	Unit	Description
CSX	tcss	Chip select time (Write)	40	•	ns	
CSA	tcsh	Chip select hold time (Read)	40	-	ns	
	twc	Serial clock cycle (Write)	100	-	ns	
	twrh	SCL "H" pulse width (Write)	40	-	ns	
SCL	twrl	SCL "L" pulse width (Write)	40	-	ns	
SOL	trc	Serial clock cycle (Read)	150	-	ns	
	trdh	SCL "H" pulse width (Read)	60	•	ns	
	trdl	SCL "L" pulse width (Read)	60	-	ns	
D/CX	tas	D/CX setup time	10	-		
D/CX	tah	D/CX hold time (Write / Read)	10	-		
SDA / SDI	tds	Data setup time (Write)	30	-	ns	
(Input)	tdh	Data hold time (Write)	30	-	ns	
SDA / SDO	tacc	Access time (Read)	10	-	ns	For maximum CL=30pF
(Output)	tod	Output disable time (Read)	10	50	ns	For minimum CL=8pF

Note: Ta = 25 °C, VDDI=1.65V to 3.3V, VCI=2.5V to 3.3V, AGND=VSS=0V



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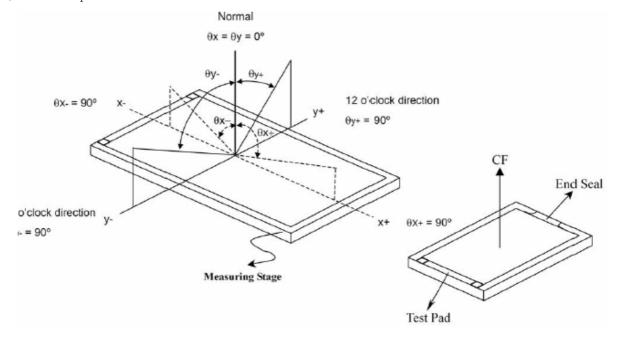
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9. Electro-optical Characteristics

Item	Symbol	Conditions	Tem	ıp	Min.	Typ.	Max.	Unit	Note
Dagnanga Tima	T_R	$\theta = \Phi = 0$	25℃	7.7		TBD	TBD	msec	NOTE2
Response Time	T_{F}					TBD	TBD		NOTEZ
Viewing Angle Range	$\Phi = 0^{0} (6")$	$\Phi = 90^{\circ} (3)$	")	ф	=180°(12")	$\Phi = 270^{\circ}$	(9")	NOTE3
θ (25°C) CR≥10	TBD	TBD		TI	3D		TBD		NOTE3

The above "viewing angle" is the measuring position with the largest contrast ratio. Not for good image quality. Viewing direction for good image quality is 12 O'clock.

- For panel only
- •Electro-Optical Characteristics Test Method



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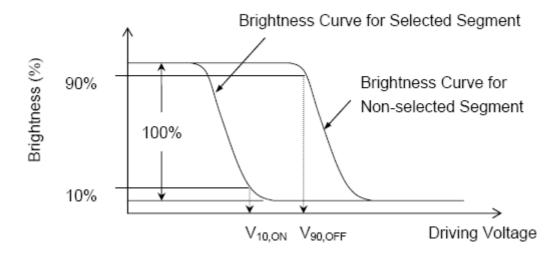
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$$Vop = (V_{10, ON} + V_{90, OFF})/2$$

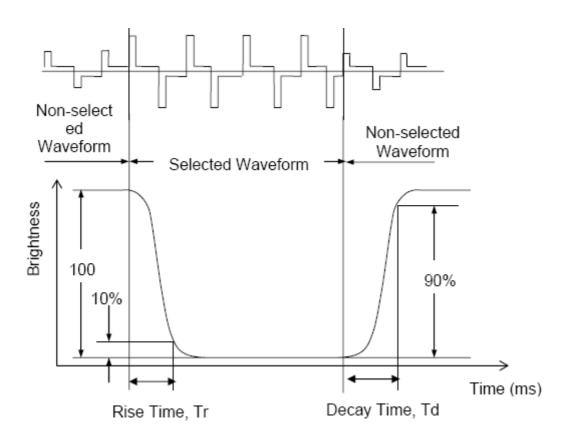


.Note2.Definition of Optical Response Time:

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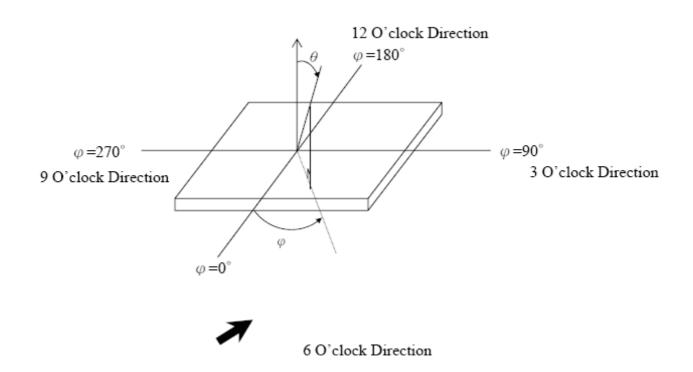
. Note3.Definition of Viewing Angle θ and Φ :

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Note4.Definition of Contrast ratio (CR):

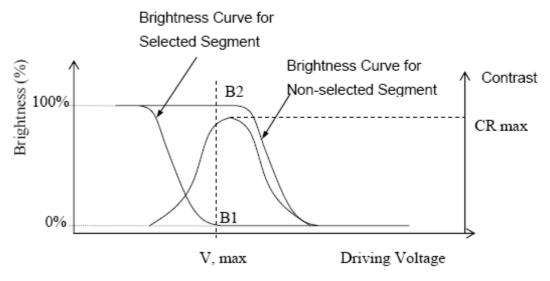
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CR = Brightness of Non-selected Segment (B2) Brightness of Selected Segment (B1)



10. Reliability10.1Mtbf

The LCD module shall be designed to meet a minimum MTBF value of 50000 hours with normal

10.2Test condition

NO.	ITEM	CONDITION	CRITERION
1	High Temperature Non-Operating Test	80°C*240Hrs	。 No Defect Of Operational
2	Low Temperature Non-Operating Test	-30°C*240Hrs	Function In Room Temperature
3	High Temperature/Humidity Non Operating Test	60°C*90%RH*240Hrs	Are Allowable
4	High Temperature Operating Test	70°C*240Hrs	。 IDD of LCM in Pre-and
5	Low Temperature Operating Test	-20°C*240Hrs	Post-Test Should Follow
6	Thermal Shock Test	-20°C (30Min) ↔70°C (30Min)	Specification
6	Thermal Shock Test	*10CYCLES	

Notes:

- 1. Judgments should be made after exposure in room temperature for two hours.
- 2. The distill water is used for the high temperature/humidity test.
- 3. The sample above is individually for every reliability tests condition.

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11.Inspection standards

1.AQL(Acceptable Quality Level

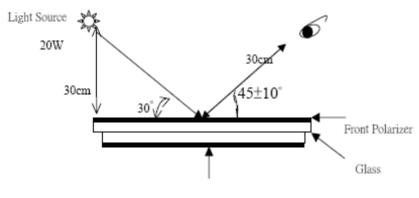
AQL of major and minor defect.

	MAJOR DEFECT	MINOR DEFECT
AQL	0.65	1.5

2. Basic conditions for inspection

The LCM face to us, in normal environment, the lux is 1000 ± 200 .(Darkroom's lux: 100 ± 50), About an angle of incidence 30, a distance of 30 cm with an angle of 45 degree to check the products without uncovering the film!

(As shown below)



Rear Polarizer

3.Inspection item and criteria

3.1 Visual inspection criterion in immobility

3.1.1Glass defect

NO	Defect item	Criteria	Remark
1	Dimension Unconformity	By Engineering Drawing	
	(Major defect)		
2	Cracks (Major defect)	 Linear cracks panel Reject Nonlinear crack contrast by limited sample 	
3	Glass extrude the conductive area (minor defect)	a: disregards and no influenceassemblage.1) b≤1/3Pin width(non bonding	A: Length, b: Width

|--|

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	I		1
4	Pin-side ,conductive area damaged (minor defect)	area) [Accept] 2)bonding area≤0.5mm [Accept] (a c: disregards) b≤1/3of effective length for bonding electrode [Accept]	a: length, b: Width, c: Thickness
5	Pin-side,non-conductive area damaged (minor defect)	1)Damage area don't touch the ITO (Inclueling contraposition mark, except scribing mark) [Accept] 2)C <t 3)c="T" 3of="" 4)a="" [accept]="" b="" b≤bm1="" disregards<="" glue="" not="" seal="" td="" the="" touch="" width=""><td>a: Length, b: Width c: Thickness</td></t>	a: Length, b: Width c: Thickness
6	Non-pin-side damage (minor defect)	c <t 1="" 1)b="" 3bm="" [reject]="" [reject]<="" b="" c="T" exceeds="" glue="" not="" seal="" td="" the="" touch=""><td>c: Thickness b: width of BM 內緣 damage</td></t>	c: Thickness b: width of BM 內緣 damage

3.1.2LCD appearance defect(View area)

NO	Defect item	Criteria		Remark
		Specification	Allowable	note1:L: Length, W: Width
	Fiber class	W ≤ 0.03mm	disregard	note2: disregard if out of AA
1	Fiber glass cratch polarizer	0.03mm <w≤0.05mm; L≤3.0mm</w≤0.05mm; 	2	r r
	scratch/folded (minor defect)	0.05mm <w≤0.1mm; L≤3.0mm</w≤0.1mm; 	1	
		W>0.1mm;L>3.0mm	0	W

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Dolorizor hubblo		φ ≤ 0.2mm	disregard	note1: $\Phi = (L+W)/2$, L:Length,
2	Polarizer bubble concave and convex	0.2 mm $< \Phi \le 0.3$ mm	2	W :Width
2	(minor defect)	0.3 mm $< \phi \le 0.5$ mm	1	note2:disregard if out of AA
	(illillor defect)	0.5mm< ф	0	
		$\phi \leq 0.15$ mm	disregard	note2:disregard if out of AA
	Black dots, dirty dots,	0.15 mm $< \phi \le 0.25$ mm	2	
3 impurities, eye winker (minor defect)	0.25 mm $< \phi \le 0.3$ mm	1	± φ	
	(minor defect)	0.3mm< φ	0	ϕ
		φ ≤ 0.1mm	disregard	note1: $\phi = (L+W)/2$, L=Length,
4	Polarizer prick	0.1 mm $< \Phi \le 0.25$ mm	3	W=Width
4	(minor defect)	φ>0.25mm	0	note2:the distance between two
		Ψ / 0. Δ J IIIII	U	dots>5mm

3.1.3FPC

NO	Defect item	Criteria		Remark
	Copper screen peel	Copper screen pe	el	
1	(minor defect)		【Reject】	
1				
2	No release tape or peel	No release tape of	r peel	
			【Reject】	
	Dirty dot and impurity of FPC	Specification	Allowable	Note1: Cannot have stride
3	for customer using side	Φ ≦ 0.25mm	2	ITO impurities
	(minor defect)	Ф>0. 25	0	

3.1.4Black tape &Mara tape

NO	Defect item	Criteria F	Remark
	FPC or H/S black tape	1. shift spec:	LCD
		1) glue to the polarize	
		【Reject】	× X
1	(minor defect)	2) IC bare 【Reject】 y1	
1		2. left-and-right spec:	
		1)exceed of FPC edge or Mara tape	y2
		H-S edge [Reject]	%1 →
		2) IC bare 【Reject】 Heart Sanl	

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2	No black tape	No black tape	
	(major defect)	【Reject】	
2	Tape position mistake	Not by engineering drawing	
3	(minor defect)		
	Mara tape defect	Peel before pulling the	
4	(minor defect)	protecting film	
		【Reject】	

3.1.5Silicon and Taffy glue

NO	Defect item	Criteria	Remark
1	Quantity of silicon	Uncover the ITO and circuit area	note: compared by engineering
	(major defect)	【Reject】	
2	Taffy glue	1.Uncover the reveal copper area Reject	note: if customer has special
	(major defect)	2.Cover layer 0.3mm(Min)~3.0mm(Max)	requirement, refer to the technical
		【Reject】	document
			3.0mm(Max)
3	Depth of glue covering	Depth of glue covering overtop front	Except of the special requirement
	(major defect)	Polarizer 【Reject】	

3.2Electrical criteria

NO	Defect item	Criteria	Remark
1	No display	No display	
	(major defect)	【Reject】	
2	Missing line	Missing line	
	(major defect)	【Reject】	
3	Seg-com light and dark	Seg-com light and dark	ND filter 2% test
	(major defect)	【Reject】	
4	No display in immobility	No display in immobility	
	(major defect)	【Reject】	
5	Flicker of Pattern	Flicker of Pattern	
	(major defect)	【Reject】	
6	Mura	ND filter 2%test	
	(major defect)		
7	Over current	Over current	

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	(major defect)	【Reject】		
8	Voltage out of specification	Voltage out of		
	(major defect)	specification		
		【Reject】		
9	Pattern blur, error code	Pattern blur, error code		
	(major defect)	【Reject】		
10	Dark light, Flicker	Dark light, Flicker		
	(major defect)	【Reject】		
11	Black/white dots Dirty	Specification	Allowable	Note1:disregard if out of AA
	dots, eye winker	φ ≦0.15mm	disregard	¥
	(major defect)	0.15 mm $< \Phi \le 0.25$ mm	2	\bigcirc $\downarrow \phi$
		0.25 mm $< \phi \le 0.3$ mm	1	+
		0.3mm< ф	0	ϕ
12	Fiber glass crutch Polarizer	₩ ≤ 0.03mm	disregard	Note1:L: Length, W: Width
	scratch/folded	$0.03 \text{mm} < W \le 0.0.05 \text{mm}$	2	Note2: disregard if out of AA
	(major defect)	L≤3.0mm	2	← т →
		0.05 mm $<$ W \leq 0.1mm	1	
		L≤3.0mm	1	V X
		W>0.1mm;L>3.0mm	0	w

12.Precautions for using LCD modules.

12.1 Safety

- (1)Do mot swallow any liquid crystal ,even if there is no proof that liquid crystal is poisonous.
- (2)If the LCD panel breaks, be careful not to get liquid crystal to touch your skin.
- (3)If skin is exposed to liquid crystal, wash the area thoroughly with alcohol or soap.

12.2Srorang Conditions

- (4)Store the panel or module in a dark place where the temperature is 23 ± 5 °C and the humidity is below 45 ±20 %RH.
- (5) Store in anti-static electricity container.
- (6) Store in clean environment, free from dust, active gas, and solvent.

5

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- (7) Do not place the module near organics solvents or corrosive gases.
- (8))Do not crush, shake, or jolt the module.

12.3Handling Precautions

- (9) Avoid static electricity, which can damage the CMOS LSI.
- (10) The polarizing plate of the display is very fragile, please handle if very carefully.
- (11) Do not give external shock.
- (12)DO mot apply excessive force on the surface.
- (13)Bo not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- (14) Do not use ketonics solvent & Aromatic solvent, use with a soft cloth soaked with a cleaning naphtha solvent.
- (15) Do not operate it above the absolute maximum rating.
- (16) Do not remove the panel or frame from the module.

12.4Warranty

The period is within twelve months since the date of shipping out under normal using and storage conditions.

13. Revision history

Version	Revise record	Date
v0.0	Original version	2015-04-25