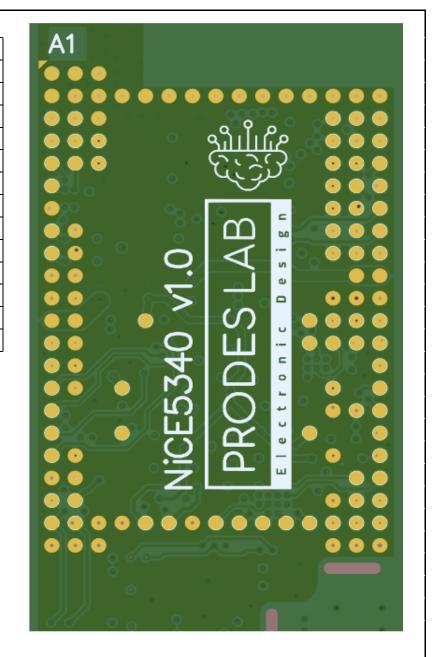


Туре	Voltage	Owner
GPIO	1V8	nRF5340
Input	1V8	Touch
USB	3V3	nRF5340
1/0	VSYS	nPM1100
1/0	1V8	MEMS MIC
OUT	VSYS	LRA Haptic
1/0	1V8	BCU
GPIO	1V8	FPGA
GPIO	3V3	FPGA
ANALOG IN	1	nPM1100
ANALOG I/O	1	nRF5340
QSPI	1V8	nRF5340
SWD	1V8	nRF5340

	Rail Name	Voltage	
	VBUS	5V	
	VSYS	VBATT / VBUS	
	VLIPO	VBATT	
	VCC3V3	3V3	
	VDD1V8	1V8	
	1V8	1V8	
	GND	0V	





NiCE5340 LGA Pin Map

Bottom View 21.06.2024