

# Knight Rider

## Design Description:

- This design should output 'Knight Rider' pattern.
- The design will use testbench file provided for Rotating Dot circuit for simulation purposes.
- Design I/O pins will be identical with Rotating Dot Circuit.
- The .ucf file provided with Rotating Dot circuit can be reused to synthesize the circuit on Digilent BASYS2 FPGA boards.