

«new» connector

New Design Description

Sending data from PC to FPGA

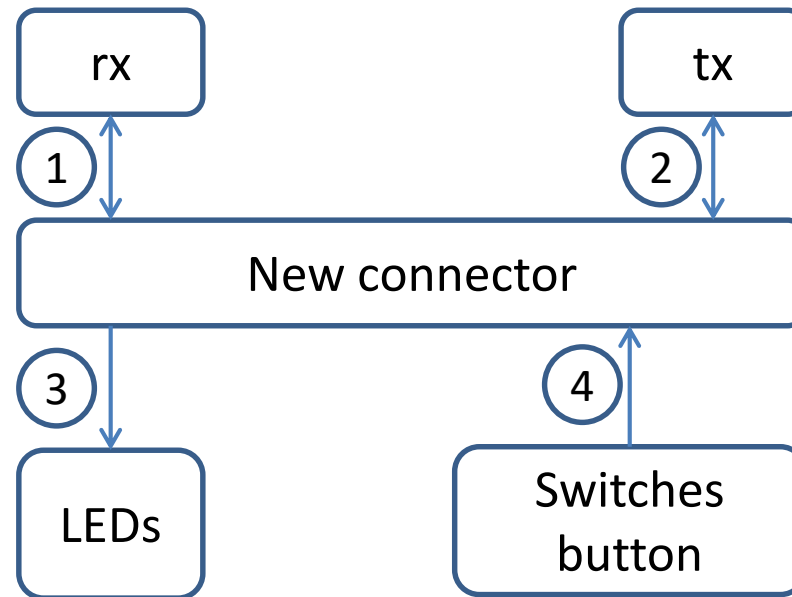
- If R is sent from PC, FPGA will wait the next term to display it at the right most 4 LEDs.
- Similarly if L is sent, FPGA will wait the next term and display that term at the left most 4 LEDs.
- Here term can be one digit hexadecimal number (1,2,..e,f) and it should be sent after R and L letters from PC via Hyper Terminal.

Sending data from FPGA to PC

- If push button is pressed, the data at the left most 4 switches and the right most 4 switches will send to PC in order. For example, let's say there is data on switches like the following 8'b11010011. We should see d3 on screen.

You may find following link useful for converting binary to ASCII character.
<http://www.asciitable.com/>

Inputs and Outputs



ports ① : receive_done
received_data [7:0]

ports ③ : led_data [7:0]

ports ② : transmit_done
transmit_start
transmit_data [7:0]

ports ④ : switch_enable
switch_input [7:0]