

application

```
graph TD; application[application] <-->|FIFO| cache[cache]; cache <-->|AXI| dram[DRAM];
```

The diagram illustrates a memory hierarchy. At the top is a rounded rectangle labeled 'application'. Below it is a larger rounded rectangle labeled 'cache'. At the bottom is a rounded rectangle labeled 'DRAM'. A double-headed vertical arrow connects the 'application' box to the 'cache' box, with the label 'FIFO' positioned to the right of the arrow. Another double-headed vertical arrow connects the 'cache' box to the 'DRAM' box, with the label 'AXI' positioned to the right of the arrow.

FIFO

cache

AXI

DRAM