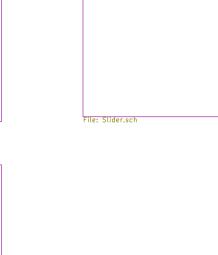


>CTRL_3_3V	YOB<
	Y1B<
CTRL_SDA	Y2B<
CTRL_SCL	Y3B<
	YOA<
⊃ <u>\$\$</u>	Y1A<
MOSI	Y2A<
>MISO	Y3A<
⊳sck	XO<
	X1<
	X2<
	X3<
	X4<
	X5<
	X6<
	X7<
File: Controller.sch	

Sheet:	Sensors

Sheet: LED_Display
DISP_5V
DISP_SCK
DISP_MOSI
DISP_MISO
DISP_SS
File: LED_Display.sch

heet: RF			

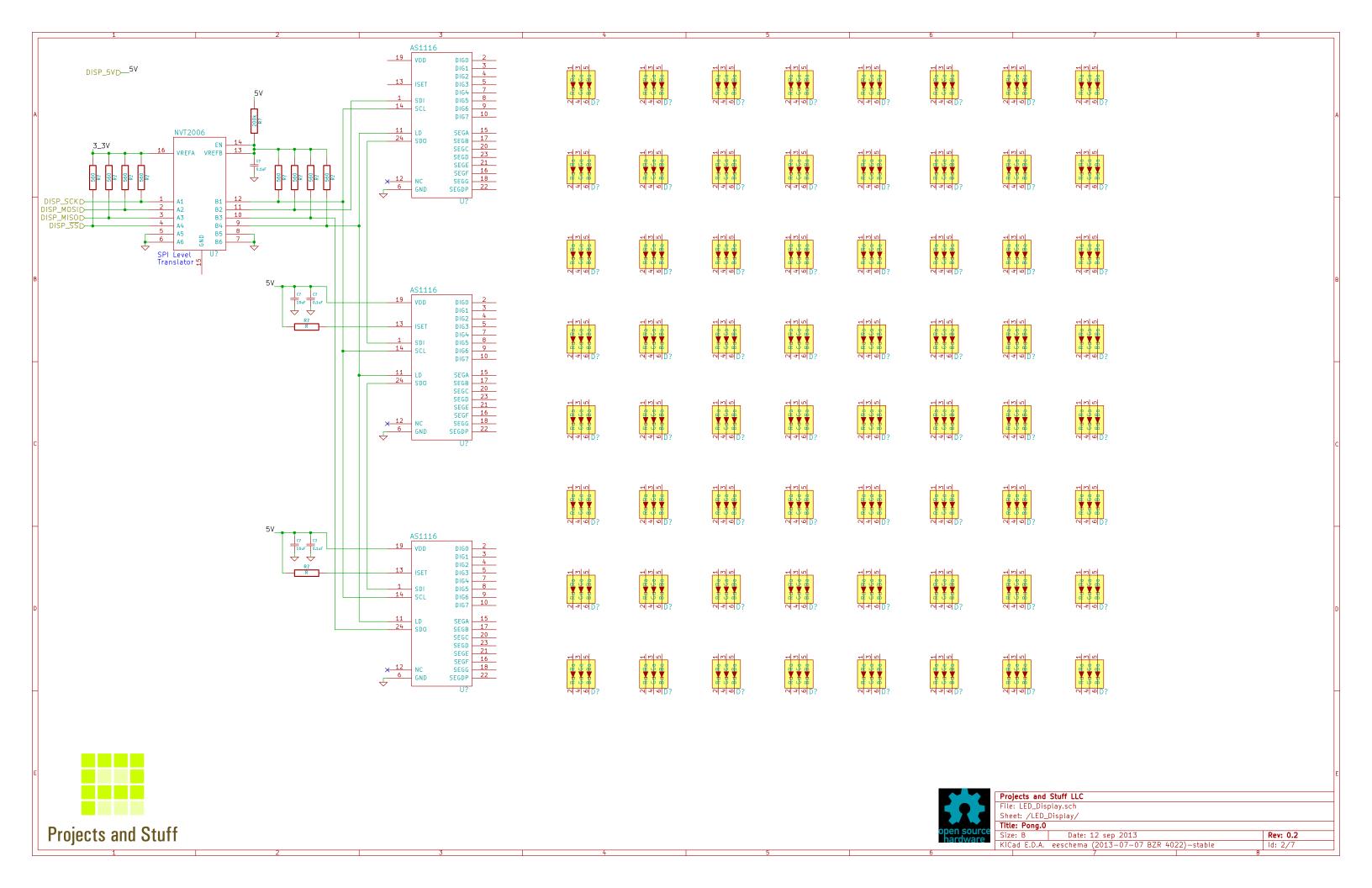


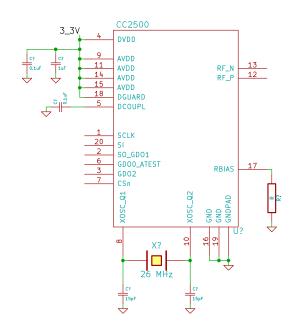
Sheet: Slider





Projects and	Stuff LLC	
File: Pong.0.	sch	
Sheet: /		
Title: Pong.0		
Size: B	Date: 12 sep 2013	Rev: 0.2
KiCad E.D.A.	eeschema (2013-07-07 BZR 4022)-stable	ld: 1/7



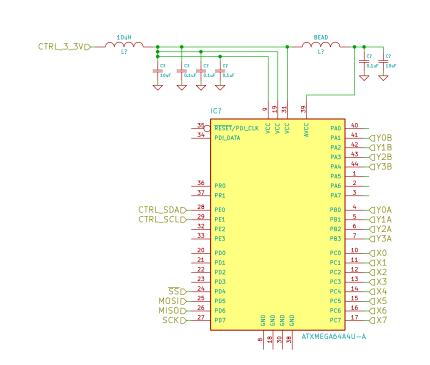




**
open source hardware

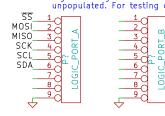
Projects and	Stuff LLC	
File: RF.sch		
Sheet: /RF/		
Title: Pong.0		
Size: B	Date: 12 sep 2013	Rev: 0.2
KiCad F D A	eeschema (2013-07-07 BZR 4022)-stable	Id+ 3/7







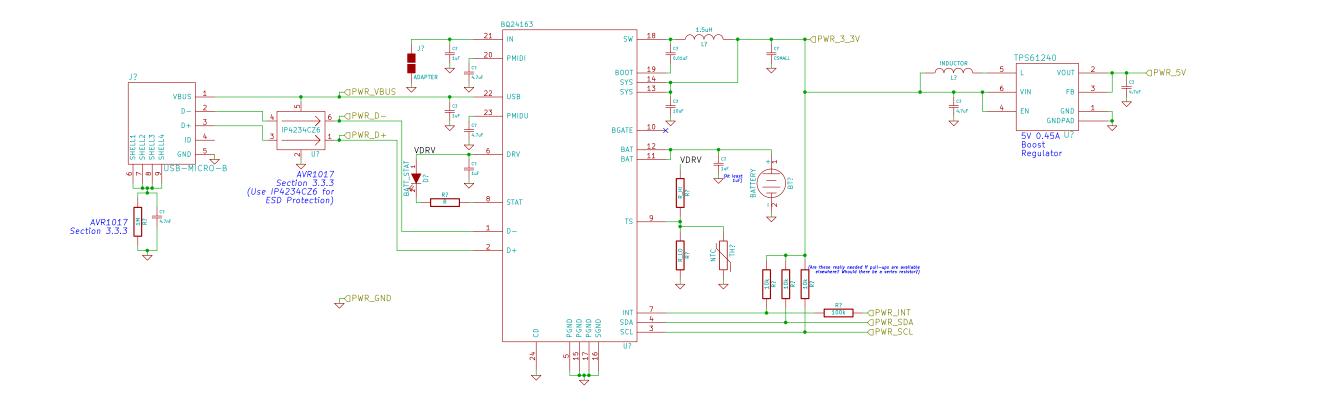
0.100" Headers for viewing important logic and signal functions. (Normally unpopulated. For testing only)







Projects and	Stuff LLC		
File: Controll	er.sch		
Sheet: /Cont	roller/		
Title: Pong.0			
Size: B	Date: 12 sep 2013	Rev: 0.2	
KiCad E.D.A.	eeschema (2013-07-07 BZR 4022)-stable	ld: 5/7	





open source

Projects and	Stuff LLC	
File: PowerM	anagement.sch	
Sheet: /Pow	erManagement/	
Title: Pong.0		
Size: B	Date: 12 sep 2013	Rev: 0.2
KiCad F D A	eeschema (2013-07-07 BZR 4022)-stable	ld: 6/7

