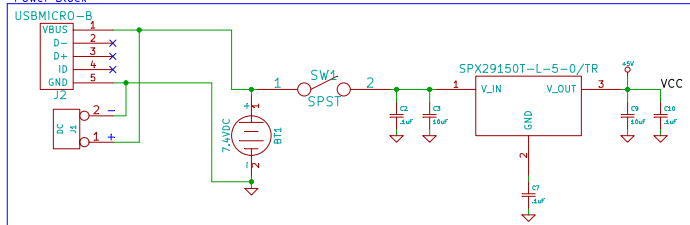
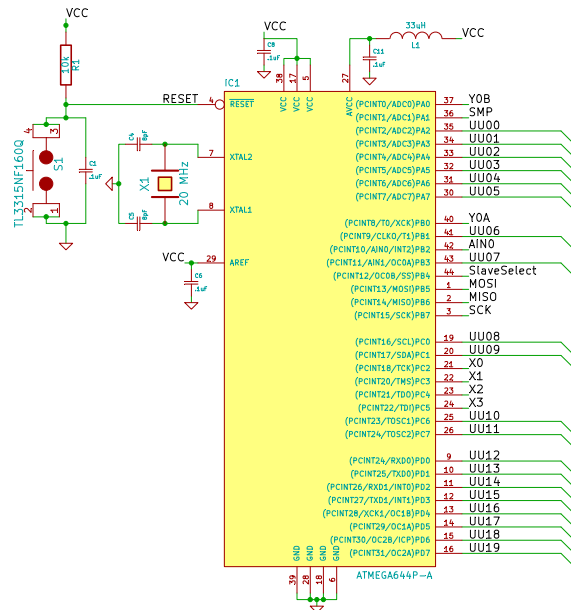
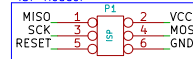


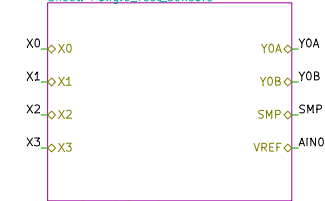
Power Block



ISP Header

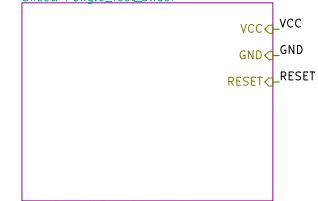


Sheet: Pong_0_Test_Sensors



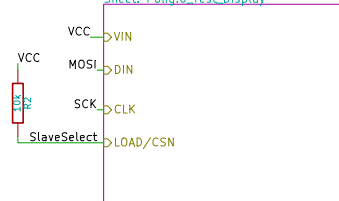
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Sheet: Pong_0_Test_Slider

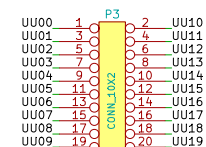
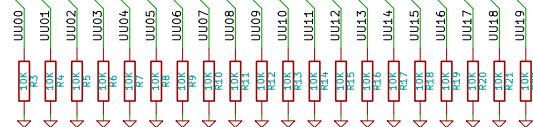


File: Pong_0_Test_Slider.sch

Sheet: Pong_0_Test_Display



File: Pong_0_Test_Display.sch



Projects and Stuff

Layout Notes:

- 1) Place the .1uF caps as close to the input power pins as possible
- 2) U000 through U019 are unused in this test device, but can be accessed at header P3



Projects and Stuff LLC

File: Pong_0_Test.sch

Sheet: /

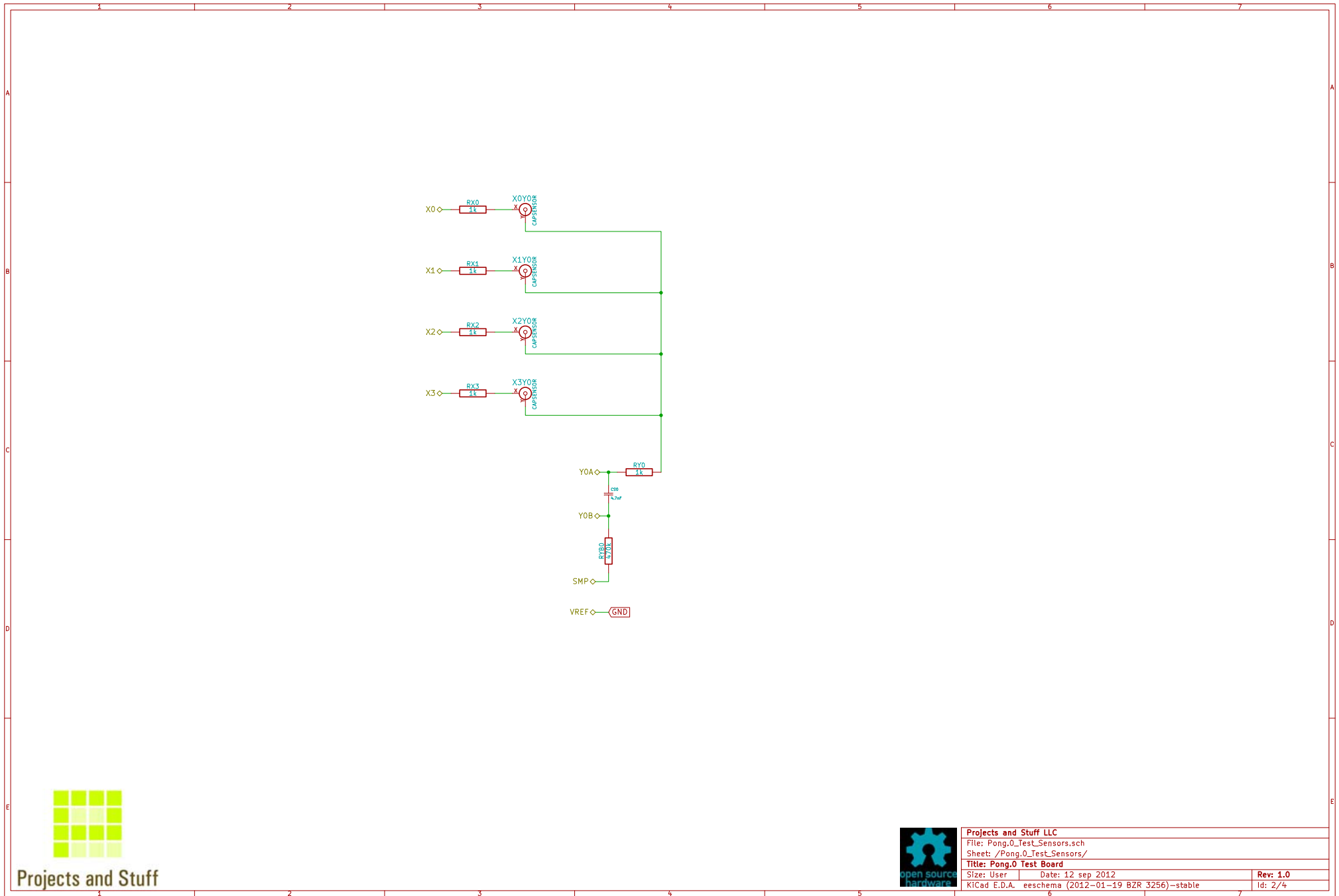
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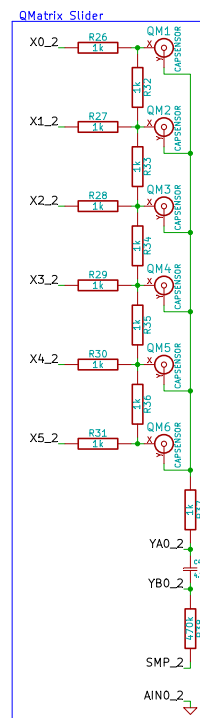
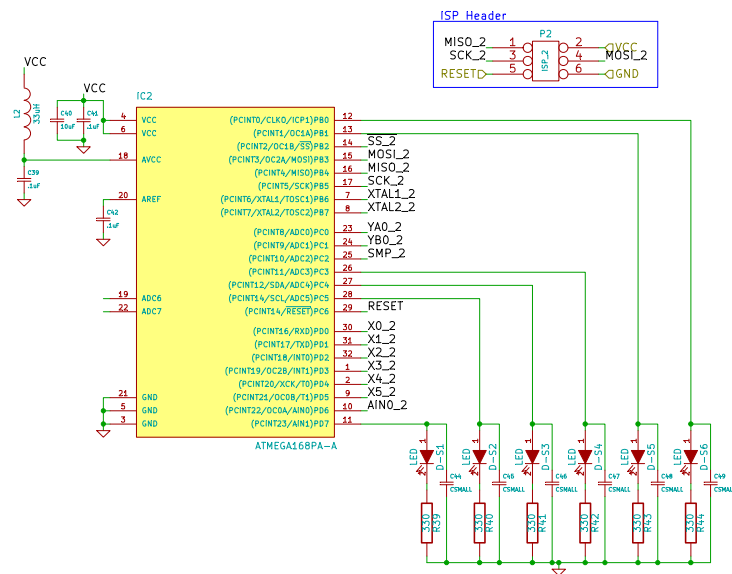
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Rev: 1.0

Id: 1/4





Projects and Stuff



QMatrix Slider		Rev: 1.0	
Projects and Stuff LLC		Id: 3/4	
File: Pong_0_Test_Slider.sch			
Sheet: /Pong_0_Test_Slider/			
Title: Pong_0 Test Board			
Size: User	Date: 12 sep 2012		
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