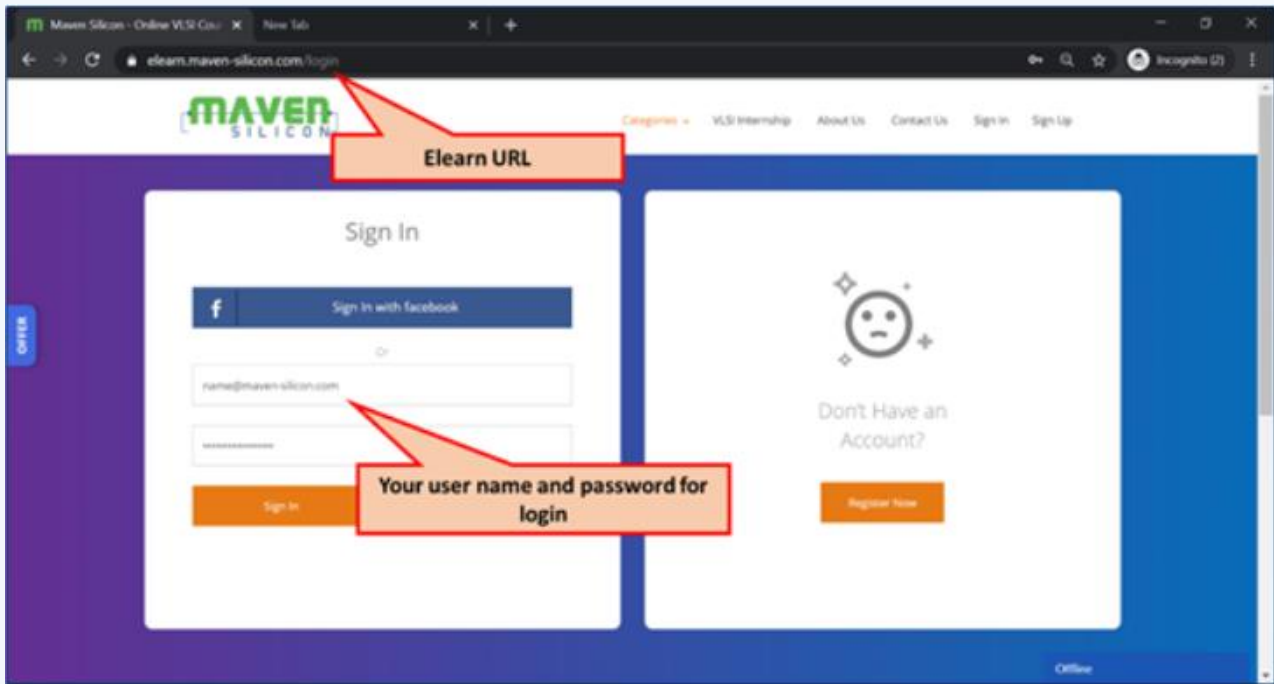


**Elearn Portal – User Guide**

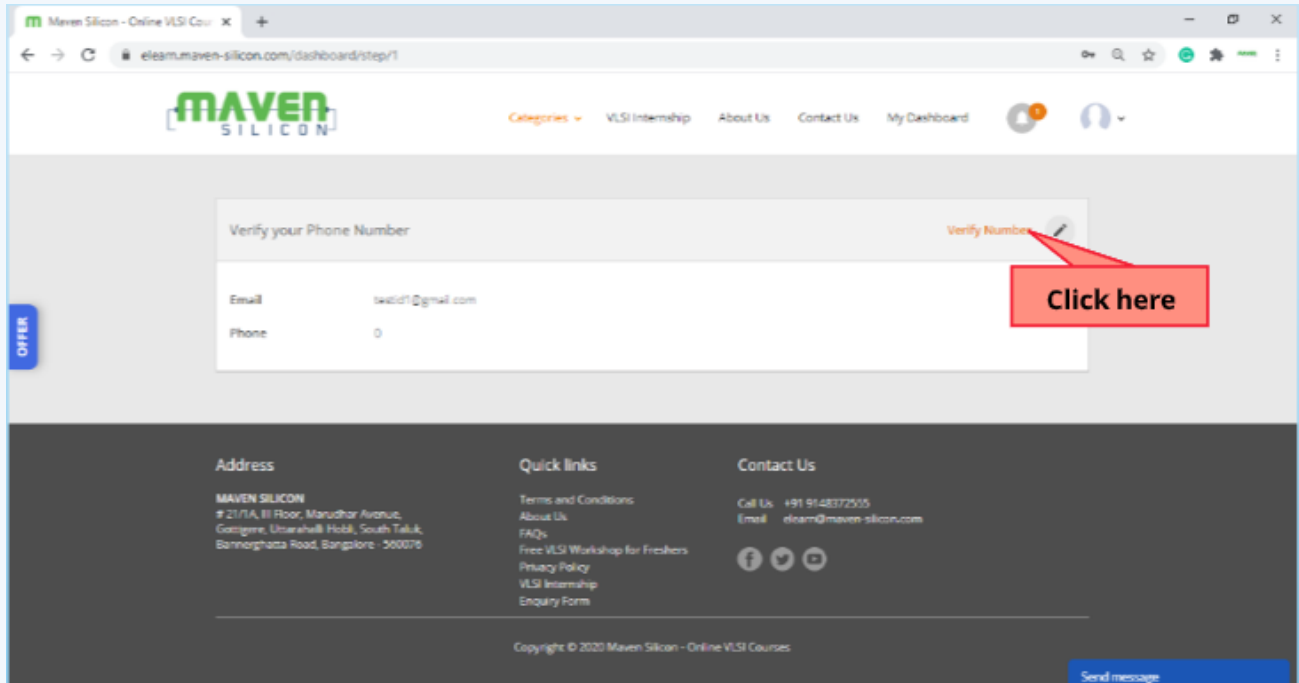
1. You can log in to the Elearn portal as shown in the figure below with your login credentials. <https://elearn.maven-silicon.com/login>

To get the credentials search "**Online@maven-silicon.com**" in your email inbox



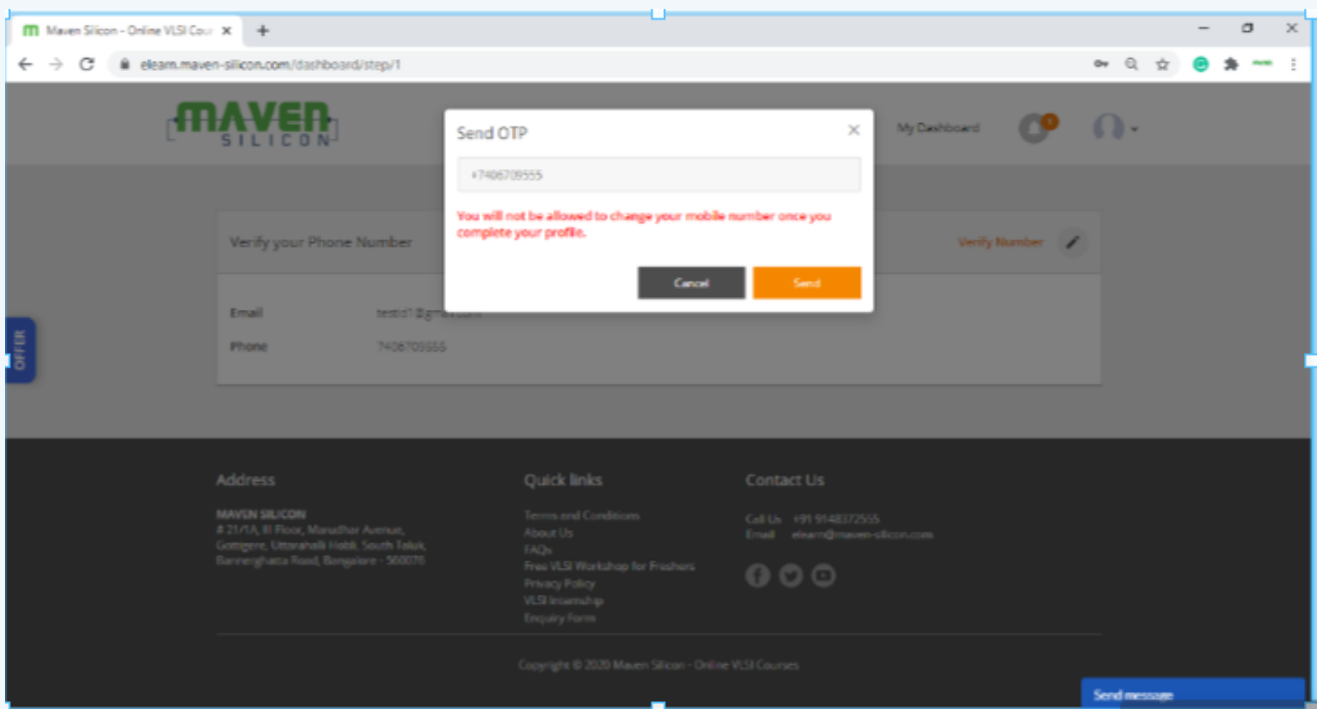
2. Once you login you will be directed to verify your contact details

Click on Edit option and update your contact number



3. Once you update your number click on verify number.

You will get a pop up saying sent OTP click on send.



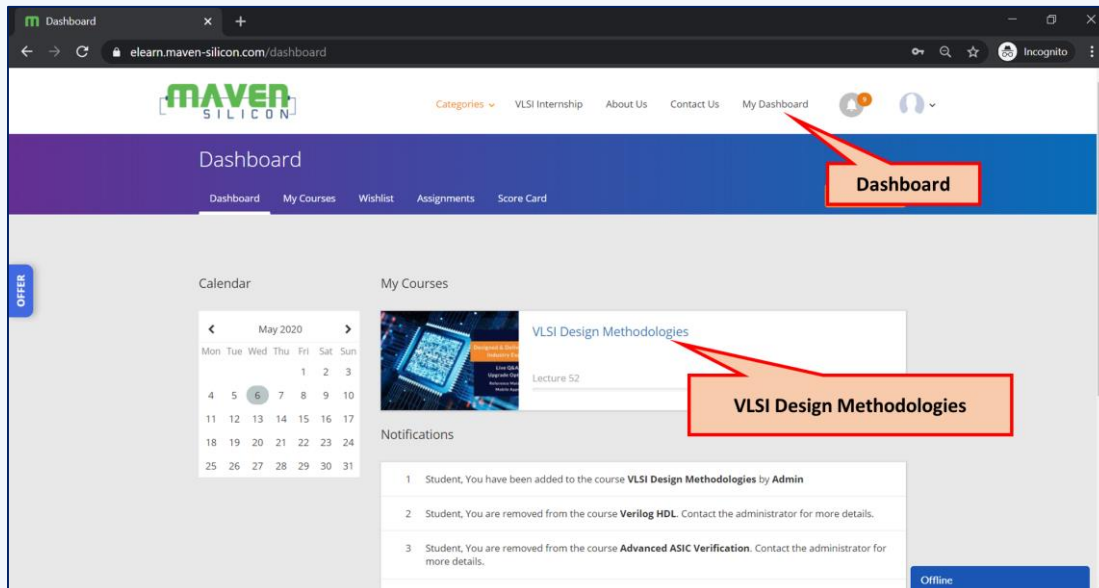
4. You will us receiving the OTP from JM-MAVENS ID

Kindly update the OTP and verify your contact number.

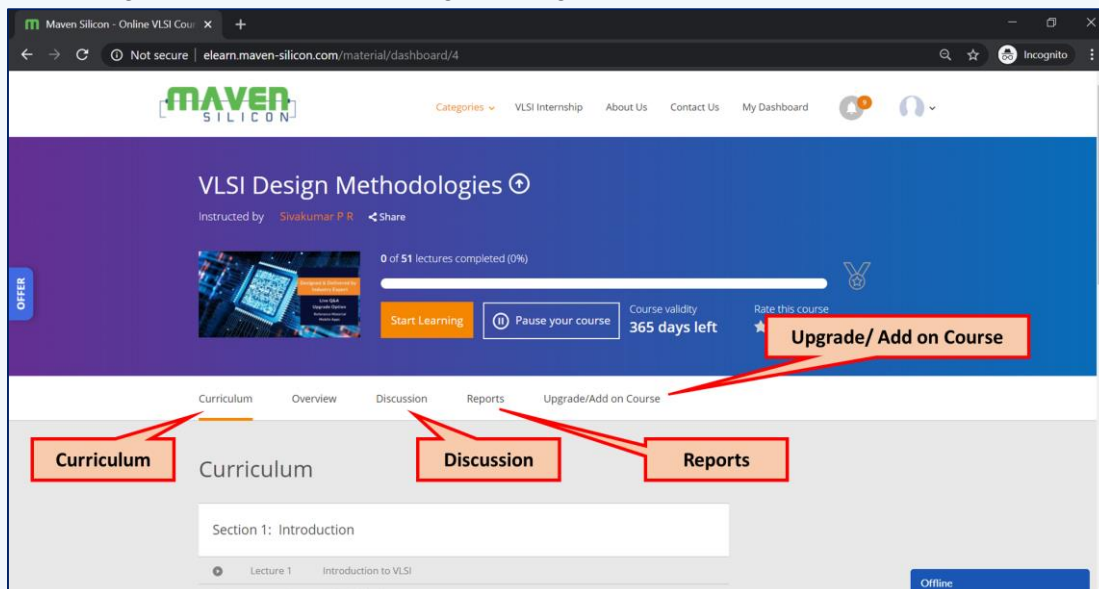
5. Your mobile number is verified now

Please fill up the sheet with your data.

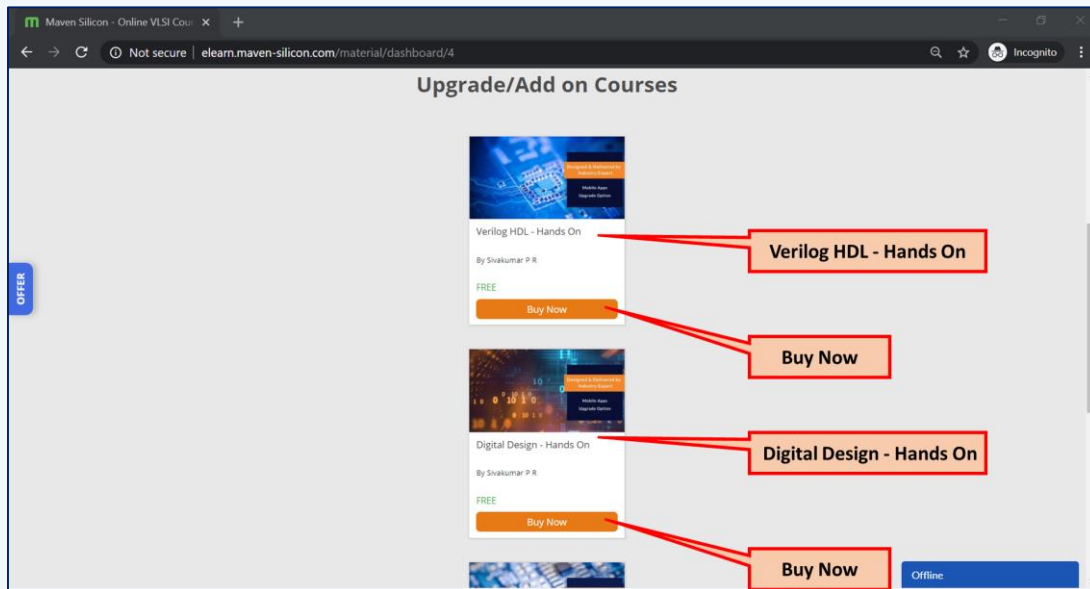
1. After changing the password, Go to “My Dashboard”, you will be able to see the courses that you have subscribed. Click on the “VLSI Design Methodologies” course to get the Course page.



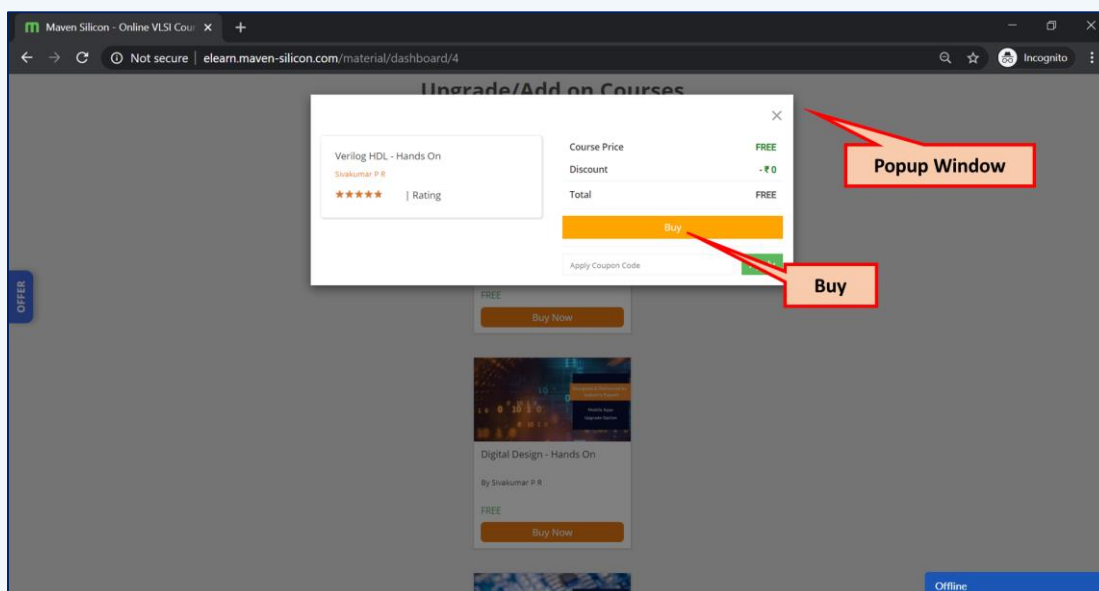
2. There are multiple tabs in the Course Page. Reports tab to analyze the quiz that you have attempted, and Discussion tab to post/view the queries. Curriculum tab to get the complete course agenda. Click on Upgrade/Add on Course to add Add-on Courses to your id. You will be able to see three add-on course. They are Verilog HDL - Hands On, and Digital Design - Hands On.



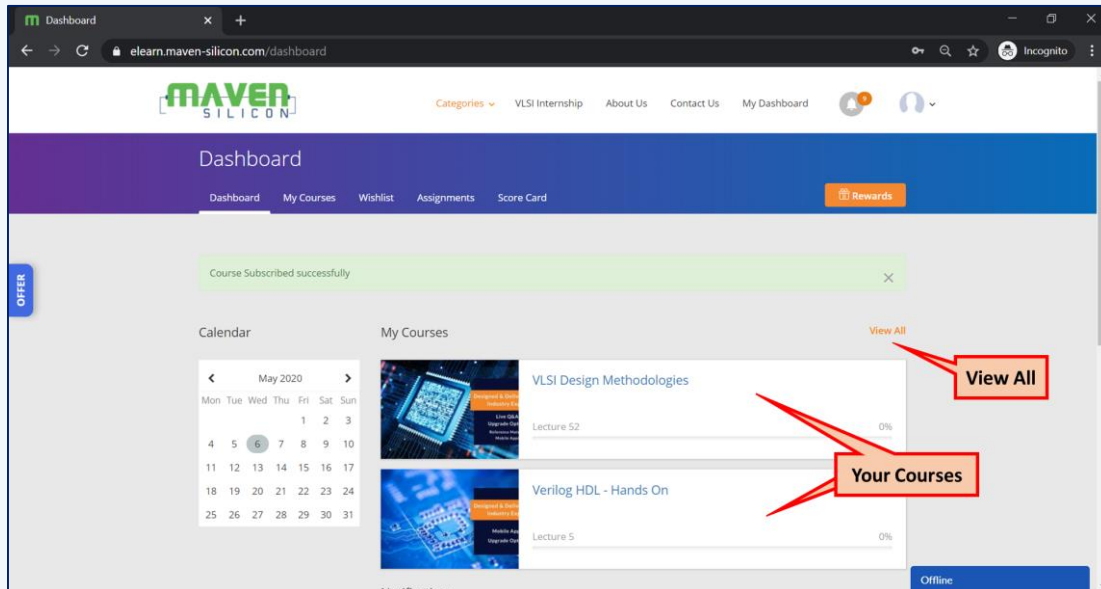
- After clicking on Upgrade/Add on Course to add Add-on Courses to your id, if you scroll down, you will be able to see the add-on course. They are Verilog HDL - Hands On, Digital Design - Hands On. Verilog HDL - Hands on Course contains Verilog Project Related Videos and Digital Design - Hands on Course contains Assignments for various topics in Digital Electronics. You need to subscribe to both the courses. Click on Buy Now for both the courses to add them to your login id.



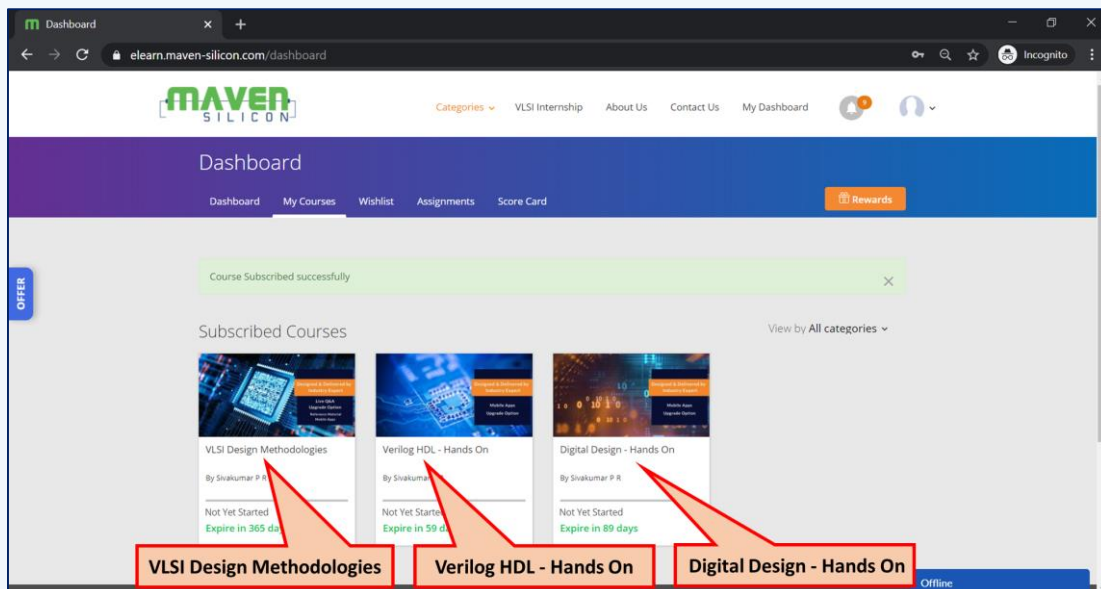
- After clicking on Buy now, a pop up window will appear and click on Buy button to confirm the purchase of the courses.



5. After adding both the courses, you will be automatically redirected to your Dashboard Page as below. Click on View All to view all the courses added to your login id.

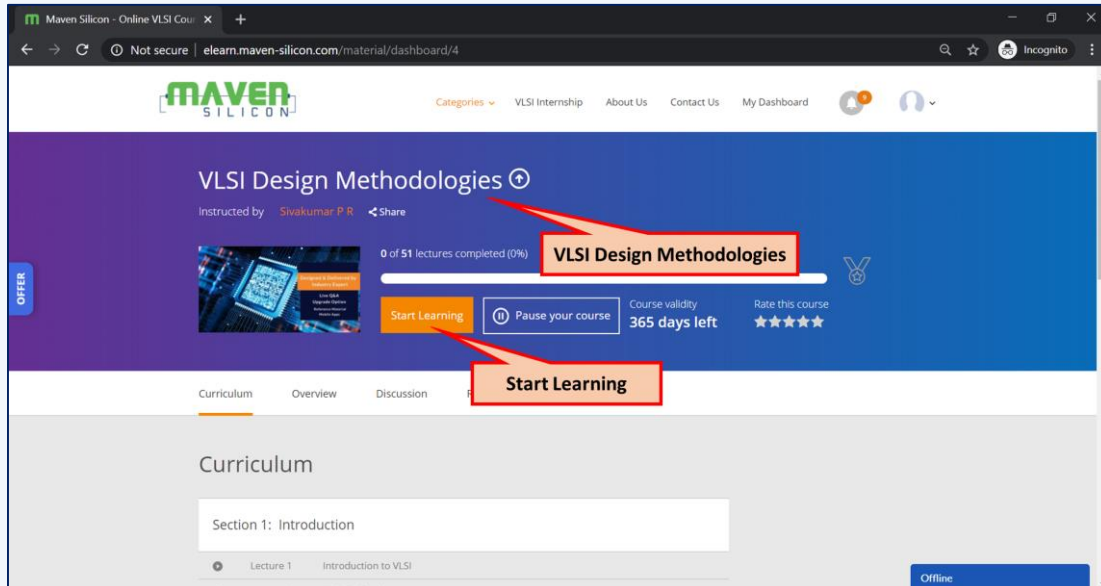


6. After this, you will be able to find all the three courses all the courses as shown below. Click on VLSI Design Methodologies Course to start learning.

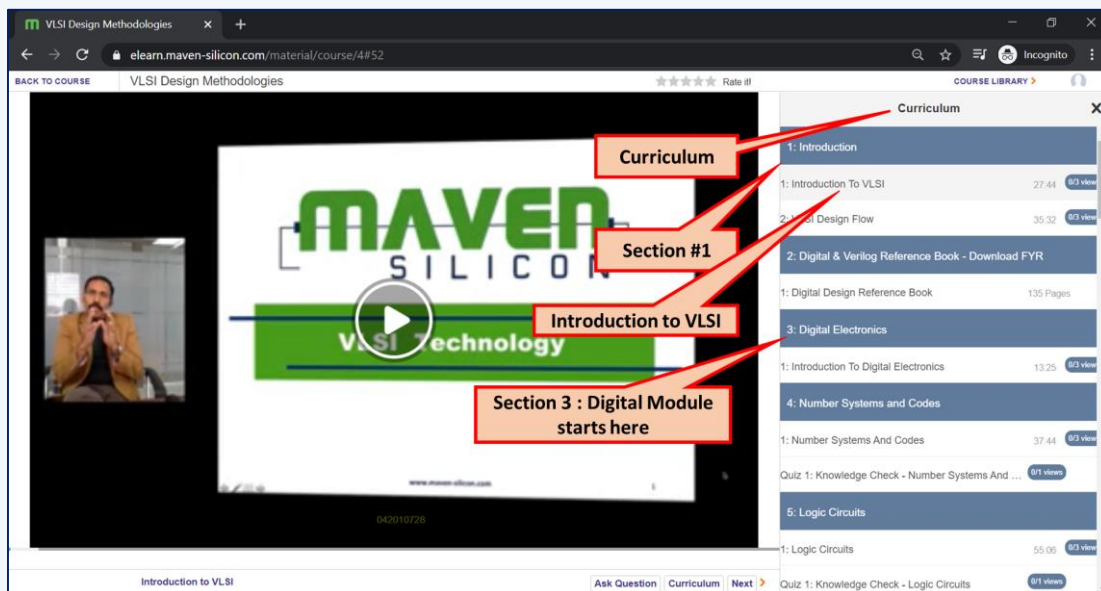




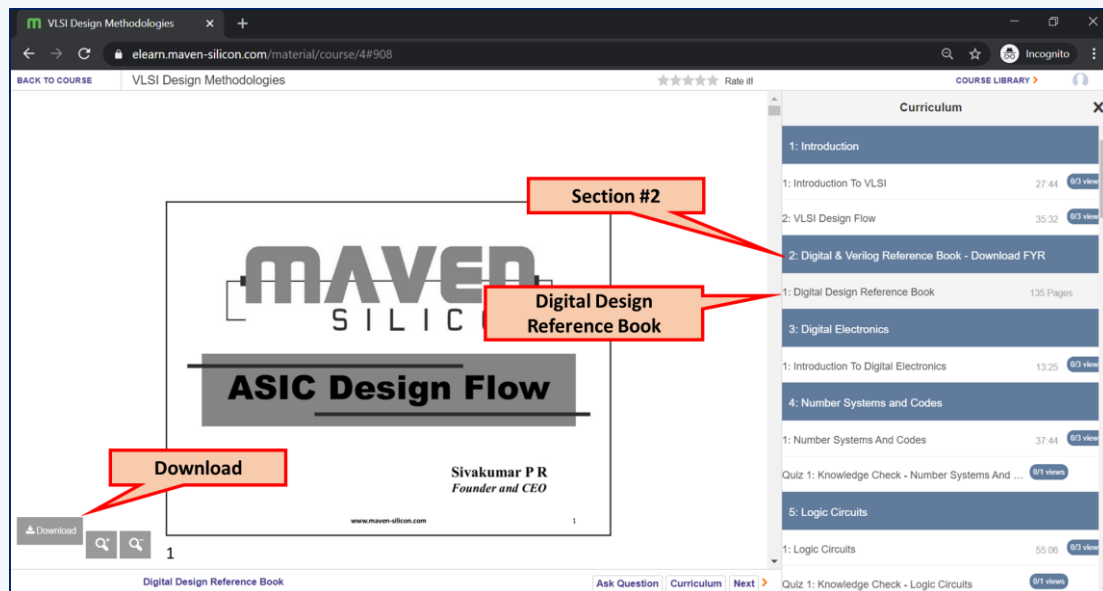
- Click on Start Learning to start learning various modules from this course.



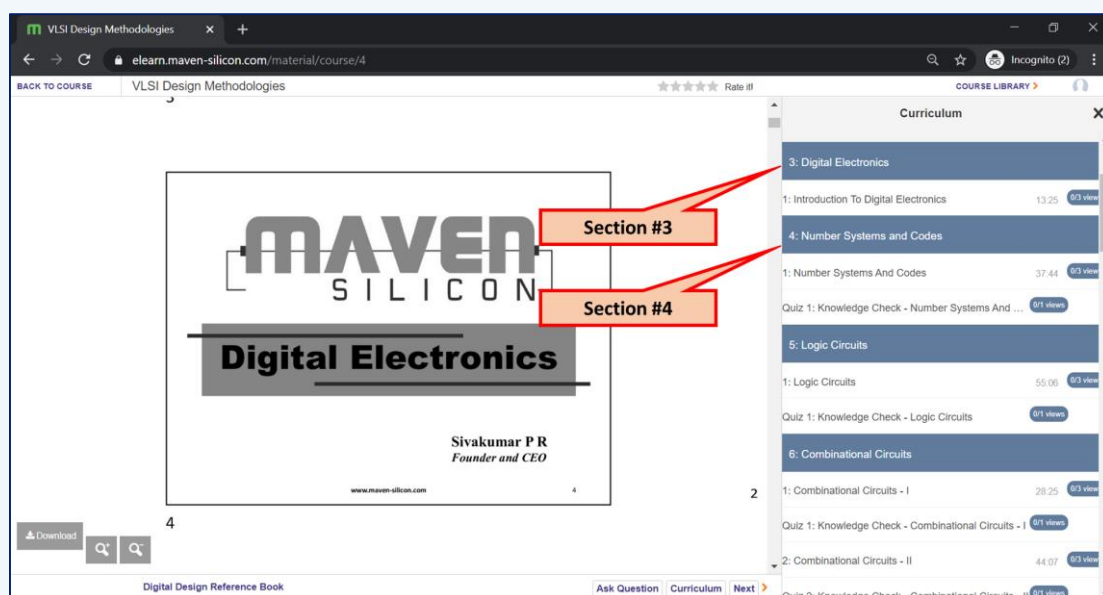
- After this, you will be redirected to Course Library Page as shown below. Start learning by watching videos from Section 1 as shown below.



9. Before stating Digital Module, Please download Digital Design Reference book from Section #2 by clicking on download button as shown below. Similarly, you can download Verilog Reference Book before starting Verilog HDL Module from Section #19.

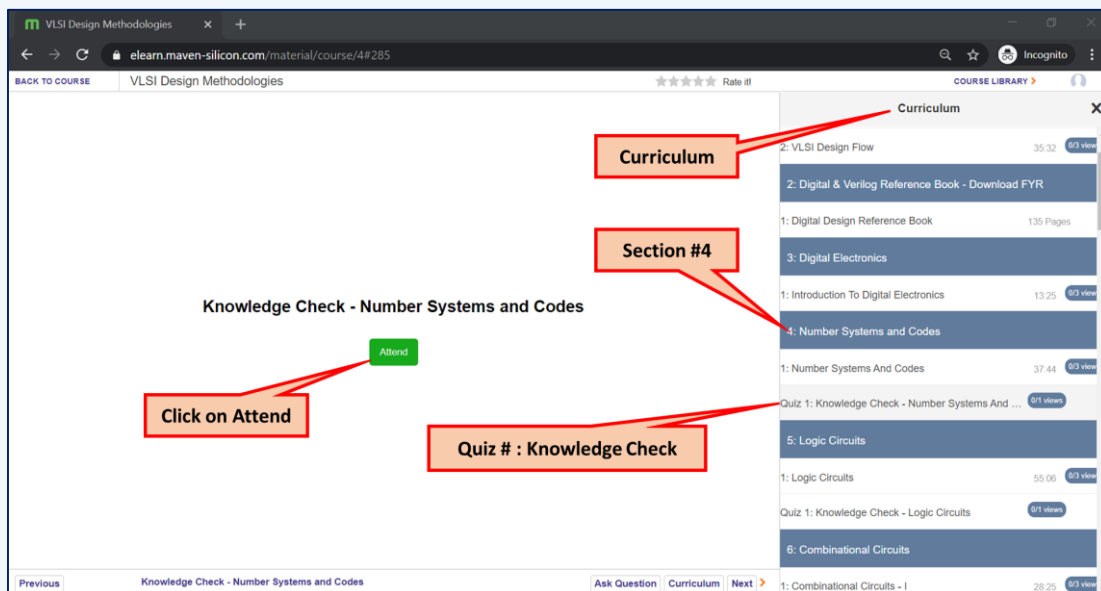


10. You can start learning Digital Design Concepts from Section #3 in the course Library as shown below. For every chapter from Section #4, you need to attempt a Knowledge Check test & work on Assignment Problems. It is recommended to complete each chapter by watching videos and attempting Quiz (Knowledge Check) in Digital Design Module from this VLSI Design Methodologies Course and work on the Assignments for each chapter.

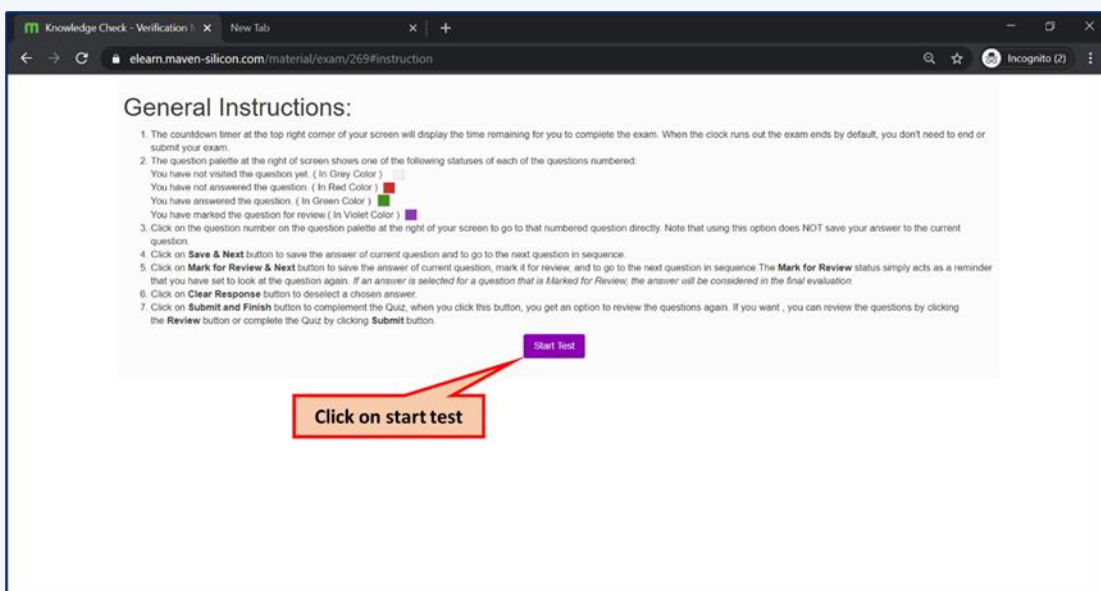




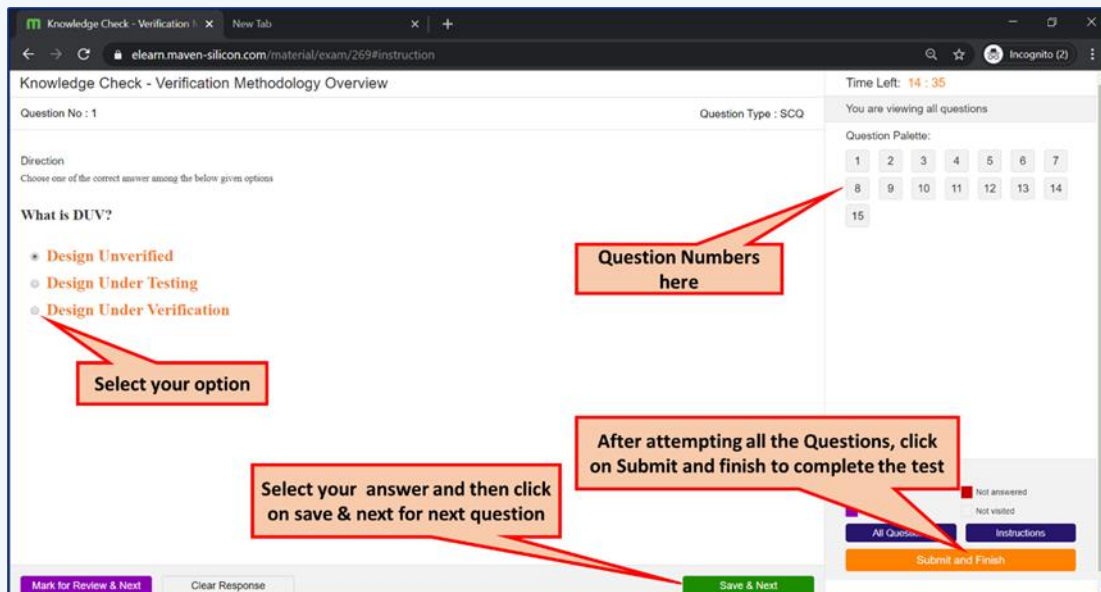
11. You can take up the Quiz for every chapter by clicking on “Quiz #: Knowledge check” and then clicking on attend as shown below.



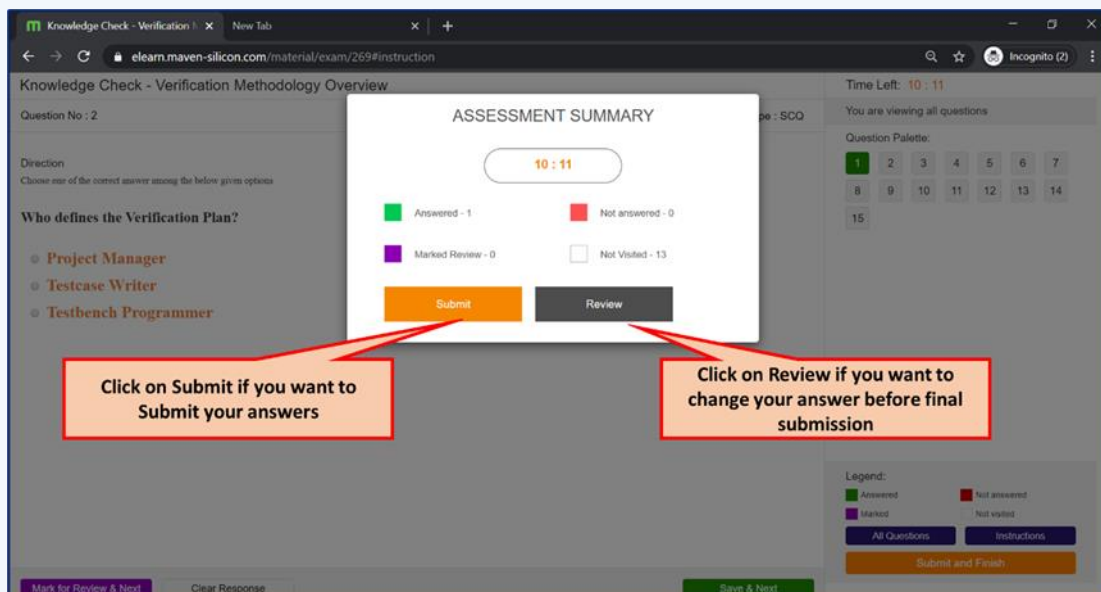
12. On the next web page, you can go through the general instructions and click on the start test button to start the test as below.



13. You can give the test as shown below and submit your answers.



14. After submitting the Quiz by clicking on Submit and finish button, you need to confirm submission as shown below.



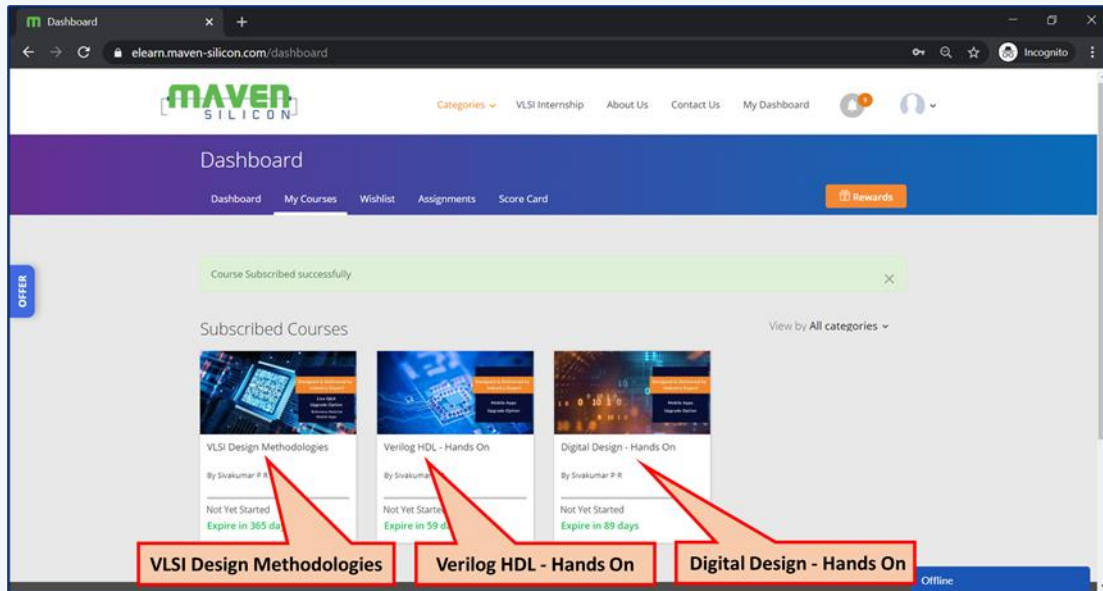
15. You can find your score and review your answers as shown below.

The screenshot shows the Maven Silicon Elearn Portal interface. At the top, there's a navigation bar with links like 'Categories', 'VLSI Internship', 'About Us', 'Contact Us', and 'My Dashboard'. Below this, a banner for 'May 8 2020 Knowledge Check - Number Systems and Codes' is displayed. A summary section shows: 25 Questions, 0 Right, 2 Wrong, 23 Unattended, 0% Accuracy, 00:08 Time Taken, and 0 Marks Scored. A 'View rank list' button is also present. Below the summary is a table with columns: Topics, Questions, Right, Wrong, Unattended, Accuracy, and Remarks. The table shows results for 'Number Systems and Codes'. At the bottom, there are three donut charts for 'Easy Questions', 'Medium Questions', and 'Hard Questions'. Two red callout boxes are present: one pointing to the 'Marks Scored' field with the text 'Scroll down to review your answers', and another pointing to the table header with the text 'Your Score Here'.

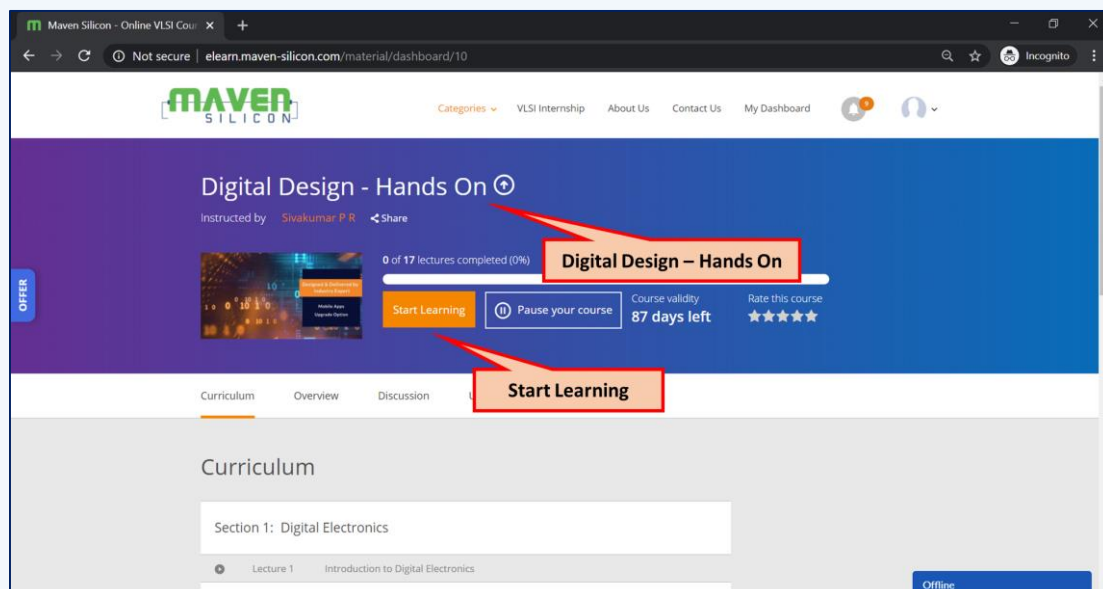
| Topics                   | Questions | Right | Wrong | Unattended | Accuracy | Remarks           |
|--------------------------|-----------|-------|-------|------------|----------|-------------------|
| Number Systems and Codes | 25        | 0     | 2     | 23         | 0%       | Needs Improvement |

16. After taking up the Quiz, as briefed earlier, you need to work on assignments as well for all the chapters in Digital Design Module. Assignments will be available in Digital Design – Hands On course. For working on Digital Assignments, you need to go to Digital Design – Hands On course from you dashboard. Click on my dashboard and view all (as shown in the image of step #9) to go the Digital Design – Hands On course as shown below.

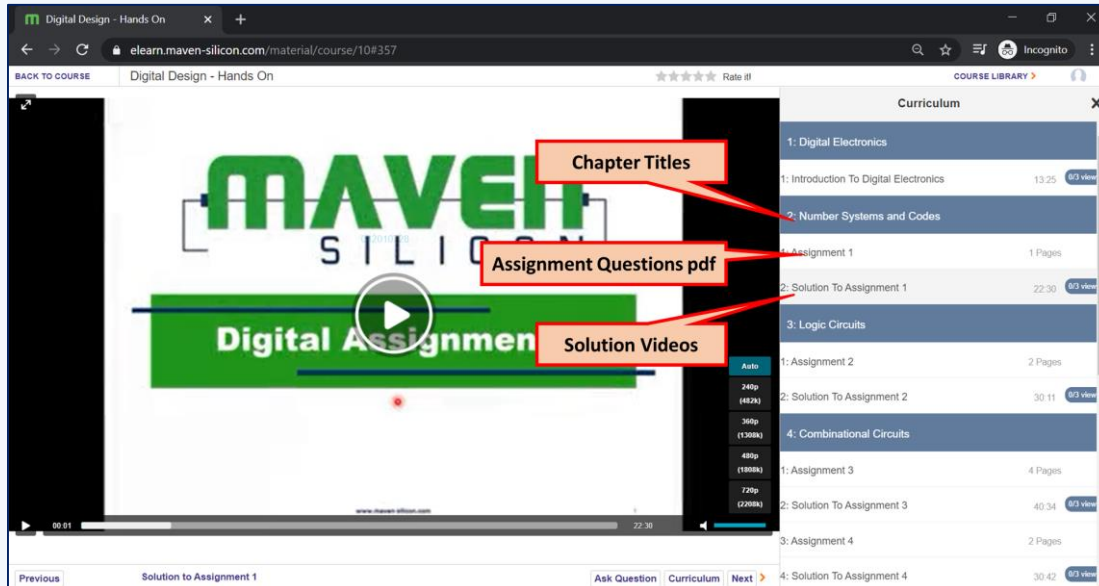
17. After this, you will be able to find all the three courses all the courses as shown below. Click on Digital Design – Hands on Course to start learning.



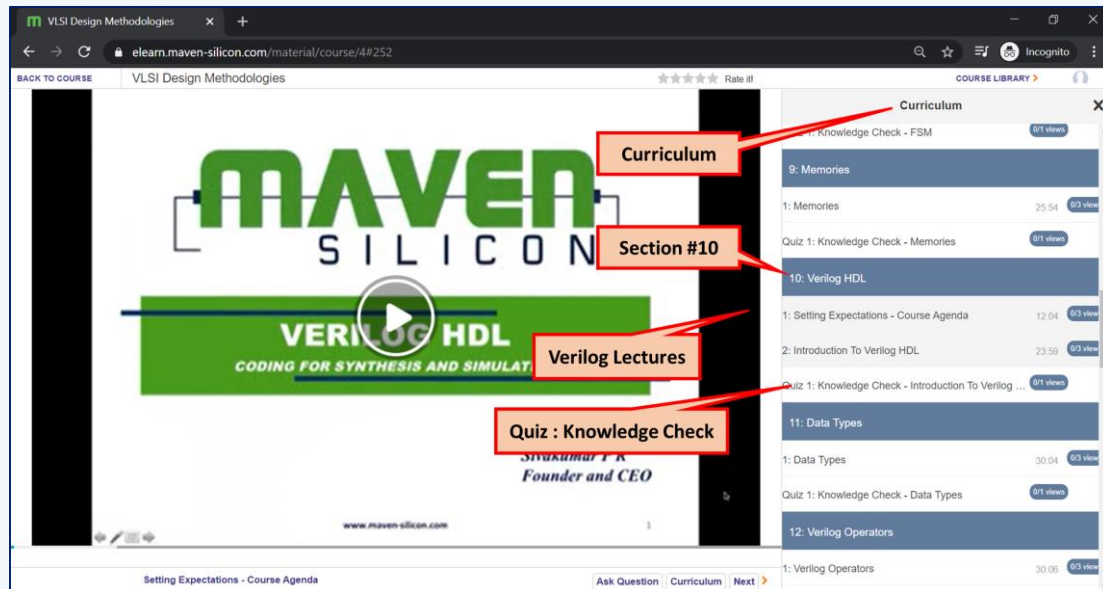
18. Click on Start Learning to start working on Assignments from this course as shown below.



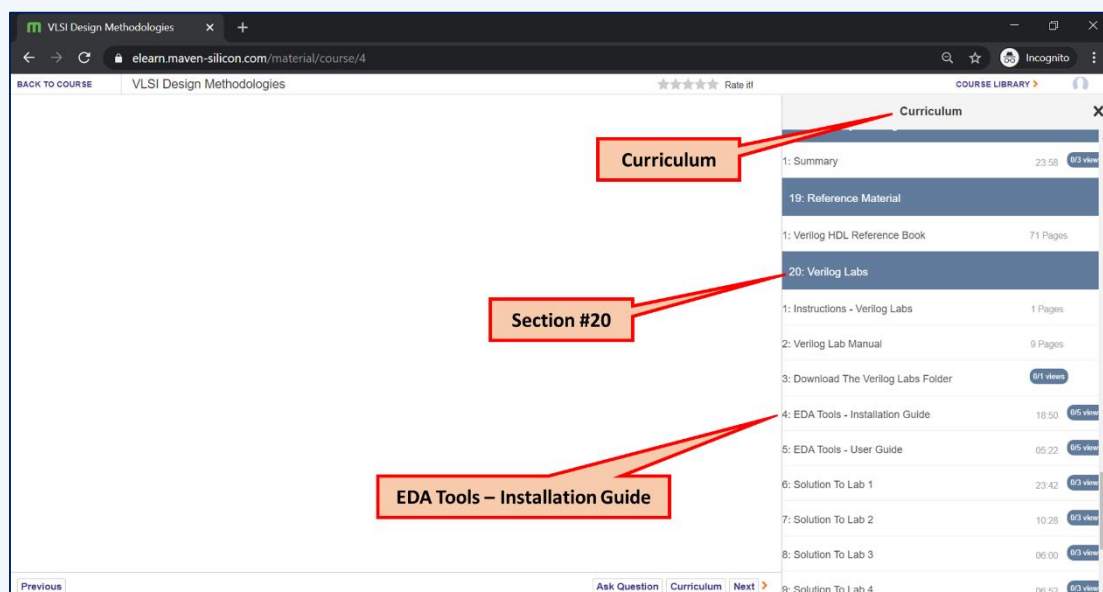
19. You can access Digital Assignment Questions from the pdf and solutions from the videos as shown below for various chapters.



20. After Completion of Digital Module, You can start learning Verilog HDL. For Verilog You can go to VLSI Design Methodologies Course and click on start learning to learn Verilog HDL Concepts (available from Section # 10) and take Quiz after completion of each chapter. As explained in step #13, you can download Verilog Reference Book from Section #19.

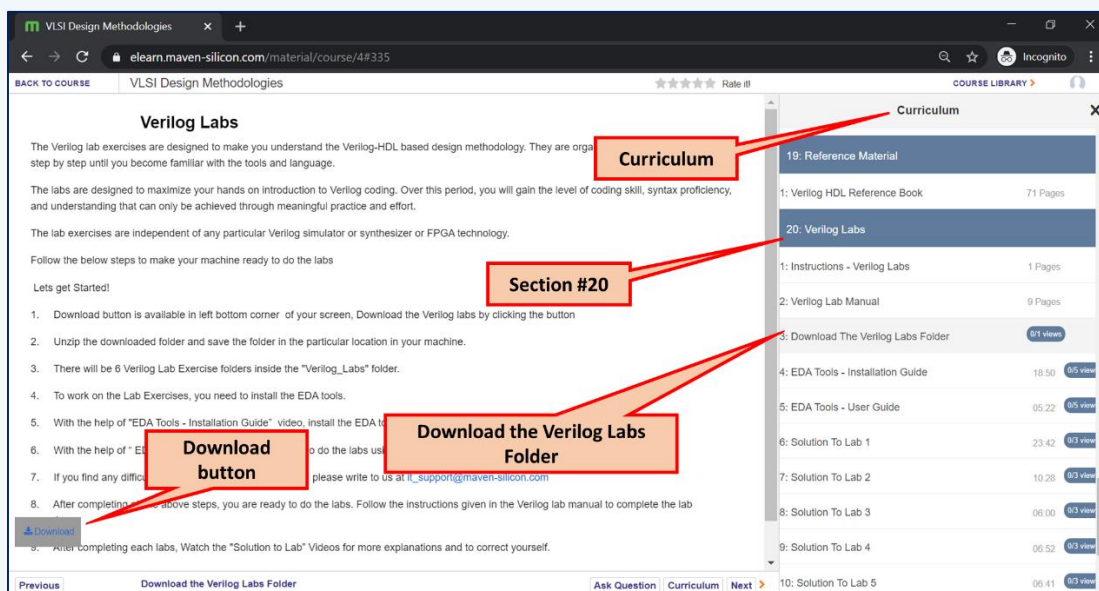


21. Along with theory, you need to work on Verilog Labs also to get Hands on Experience. To work on Verilog Labs, you need to Install Mentor Graphics Modelsim and Quartus Prime tools. You can find EDA Tools Installation Guide in section #20 as shown below.

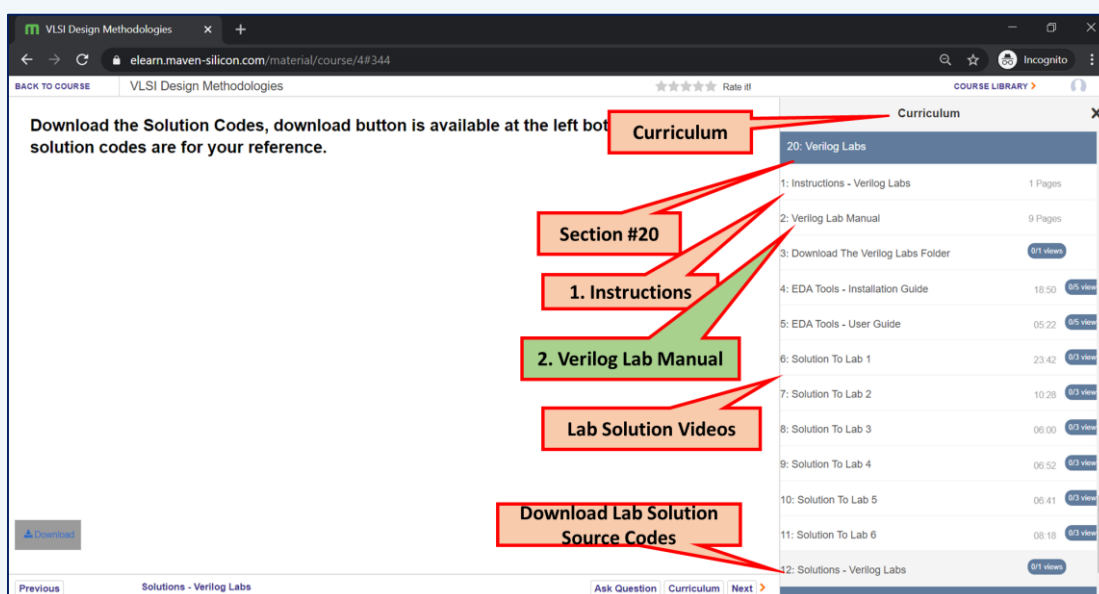




22. Now you need to download Verilog Labs. For this, you need to click on Download the Verilog Labs Folder in Section #20 and click on the Download button as shown in the below figure.



23. With this, you are done with installing EDA tools and downloading Verilog Labs. So, you can start working on Verilog Labs by following instructions given in 1 and 2 documents (Instructions – Verilog labs and Verilog Lab Manual) in section #20 as shown below. After completion of each Lab, You can watch Lab Solution Videos to get more clarity on each Lab exercise and download the solution Source codes as below.



24. After completion of Verilog HDL Module and HDL, you are ready to work Project using Verilog HDL. For Project, you can go to Verilog HDL – Hands on course from your dashboard and click on start learning so that, you will be redirected to Verilog HDL – Hands On Course Page. You can complete this project with the instructions as shown below.

**Watch Videos in Verilog HDL Review for a Quick Revision**

1. Download Project code templates
2. Understand the project Specification and complete the project.
3. In case of any doubt, download the solution source codes and analyse

25. After completion of the Project, go to VLSI Design Methodologies Course and take final test from Section #21 and generate Certificate as shown below.

**Take up the Final Exam**

**Generate Certificate**