



Link : <https://attendee.gotowebinar.com/rt/2731577623072081675>

Date	Mode of session	Modules	Time slots	Labs to be completed	Trainer
17th Feb	Live session	Context setting by Sandhya & overall plan by Susmita	10:15am to 11:15am	Nil	Susmita/ Sandhya
18th Feb	Elearn Portal/Live session	Verilog Lab1 Q&A	6:00pm to 6:30pm	Lab1	Susmita
19th Feb	Elearn Portal/Live session	Verilog Lab2 Q&A	6:00pm to 6:30pm	Lab2	Susmita
22nd Feb	Elearn Portal/Live session	Verilog Lab3 Q&A	6:00pm to 6:30pm	Lab3	Susmita
23rd Feb	Elearn Portal/Live session	Verilog Lab4 Q&A	6:00pm to 6:30pm	Lab4	Susmita
24th Feb	Elearn Portal/Live session	Verilog Lab5 Q&A	6:00pm to 6:30pm	Lab5	Susmita
25th Feb	Elearn Portal/Live session	Verilog Lab6 Q&A	6:00pm to 6:30pm	Lab6	Susmita
26th Feb	Live session	AHB-APB bridge design specification	5:00pm to 6:00pm	NIL	Susmita
1st March	Live session	AHB-APB bridge design specification	5:00pm to 6:00pm	NIL	Susmita
2nd March	Nil	Design Architecture	6:00pm to 6:30pm	NIL	Susmita
3rd March	Nil	Design Architecture	6:00pm to 6:30pm	NIL	Susmita
4th March	Live session	AHB Slave Interface rtl coding	6:00pm to 6:30pm	NIL	Susmita
5th March	Elearn Portal	Interview Preparation.	6:00pm to 6:30pm	NIL	Sweetie
8th March	Nil	AHB Slave Interface rtl coding	6:00pm to 6:30pm	NIL	Susmita
9th March	Nil	APB Controller	6:00pm to 6:30pm	NIL	Susmita
10th March	Nil	APB Controller	6:00pm to 6:30pm	NIL	Susmita
11th March	Nil	AHB-APB Bridge Top	6:00pm to 6:30pm	NIL	Susmita
12th March	Live session	AHB-APB Bridge Top	6:00pm to 6:30pm	NIL	Susmita
15th March	Nil	AHB-Master	6:00pm to 6:30pm	NIL	Susmita
16th March	Nil	AHB-Master	6:00pm to 6:30pm	NIL	Susmita
17th March	Nil	APB interface	6:00pm to 6:30pm	NIL	Susmita
18th March	Nil	Top level verification	6:00pm to 6:30pm	NIL	Susmita
19th March	Live session	Top level verification	6:00pm to 6:30pm	NIL	Susmita
22nd March	Nil	Top level verification	6:00pm to 6:30pm	NIL	Susmita
23rd March	Nil	Top level verification	6:00pm to 6:30pm	NIL	Susmita
24th March	Live session	Project review	6:00pm to 6:30pm	NIL	Susmita