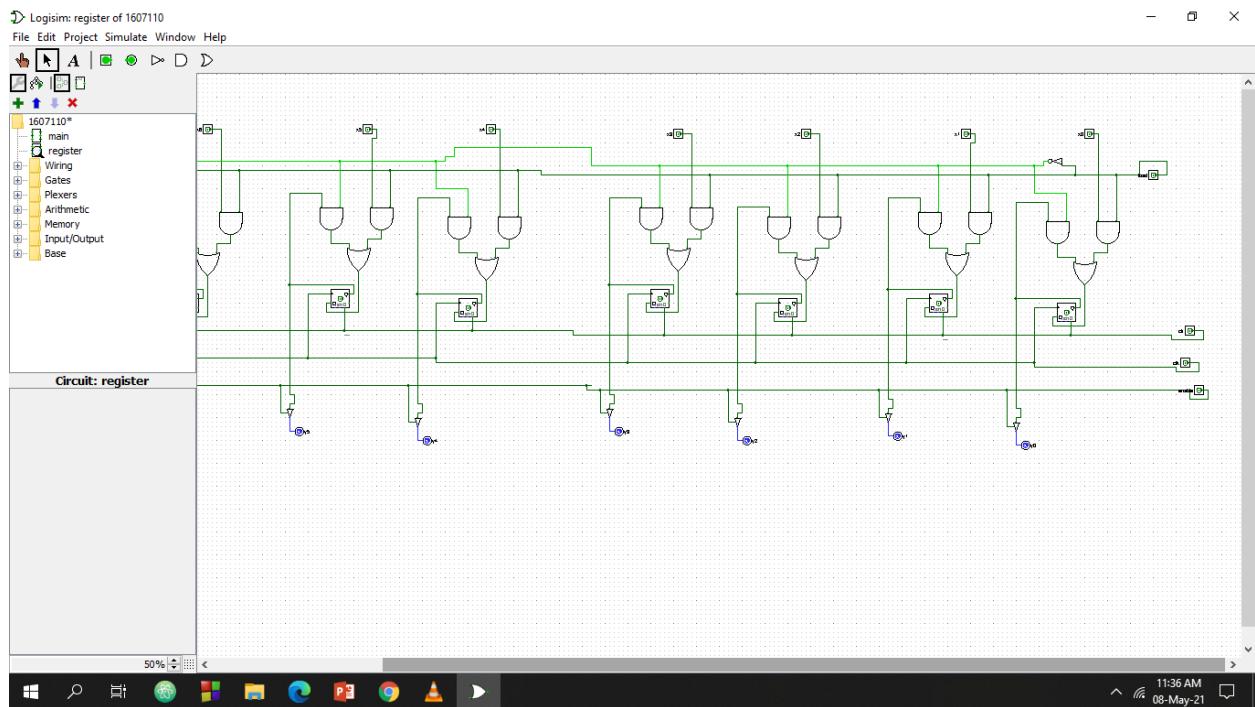
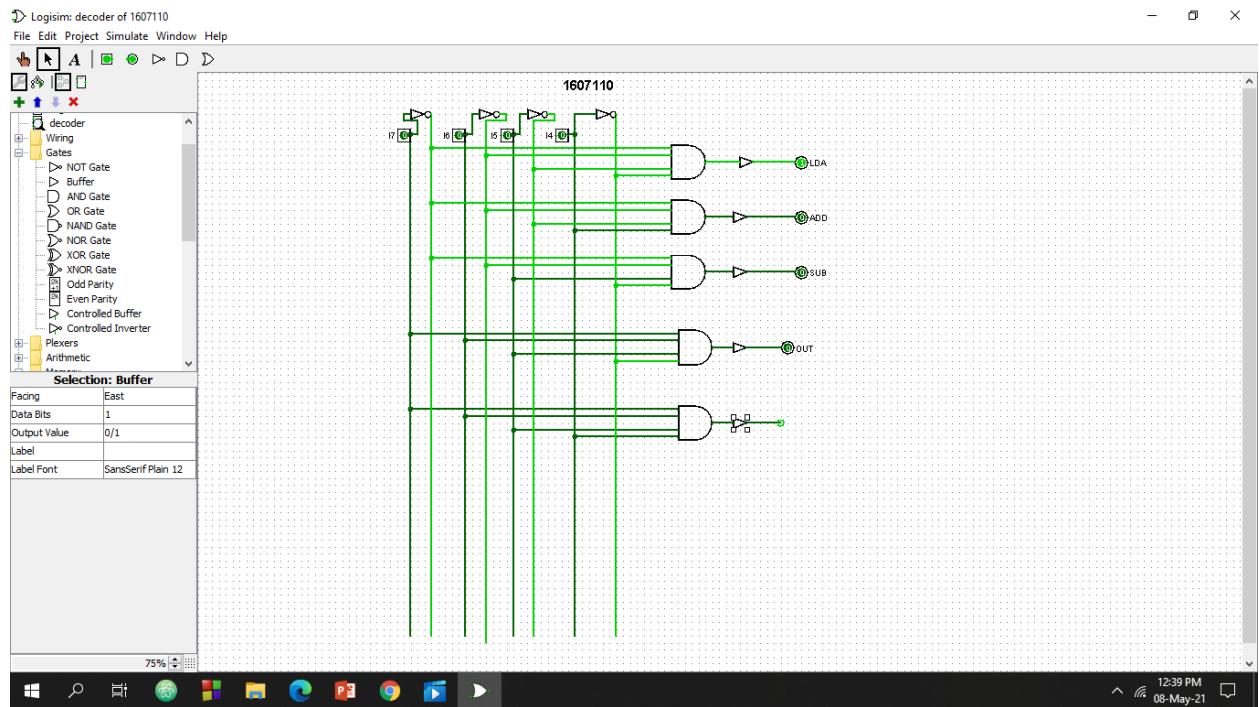
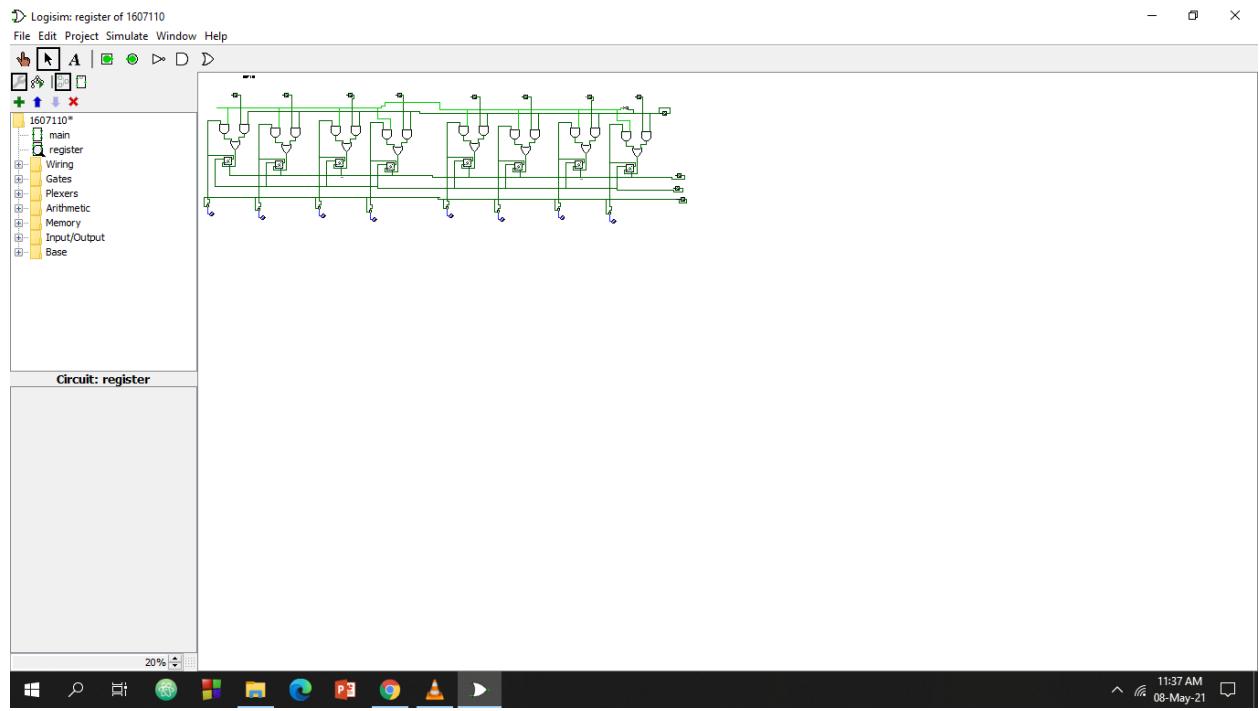
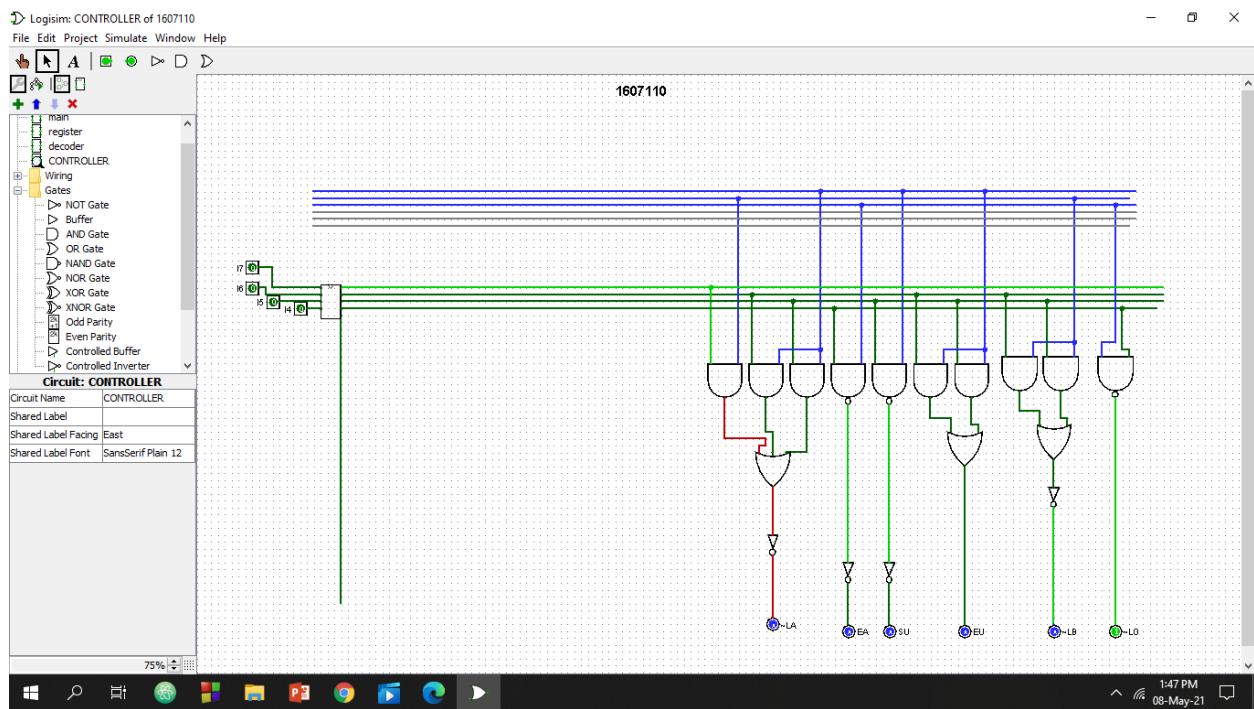
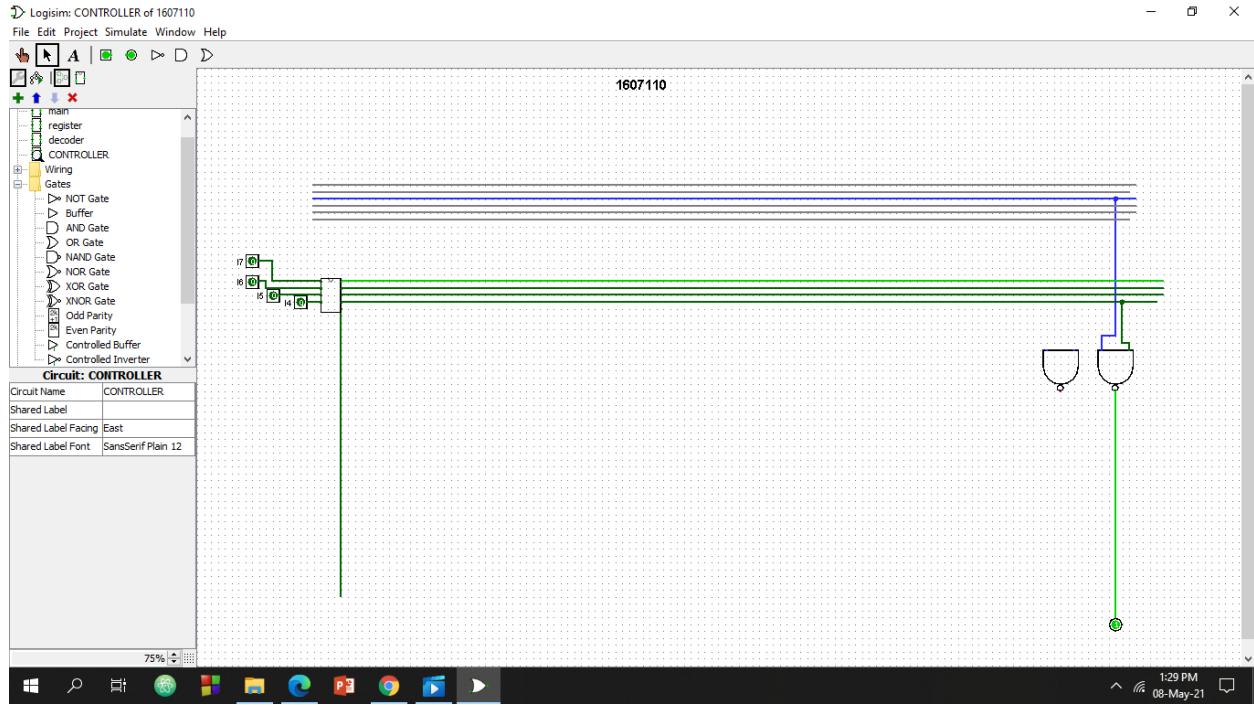
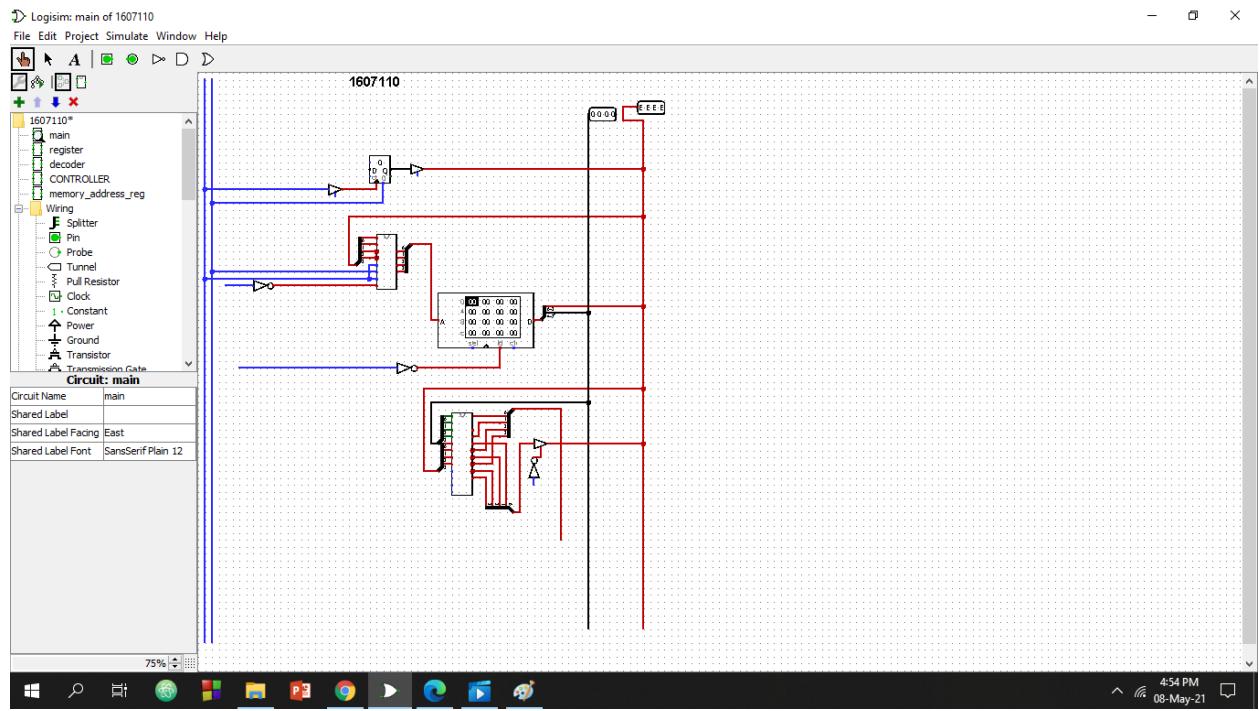
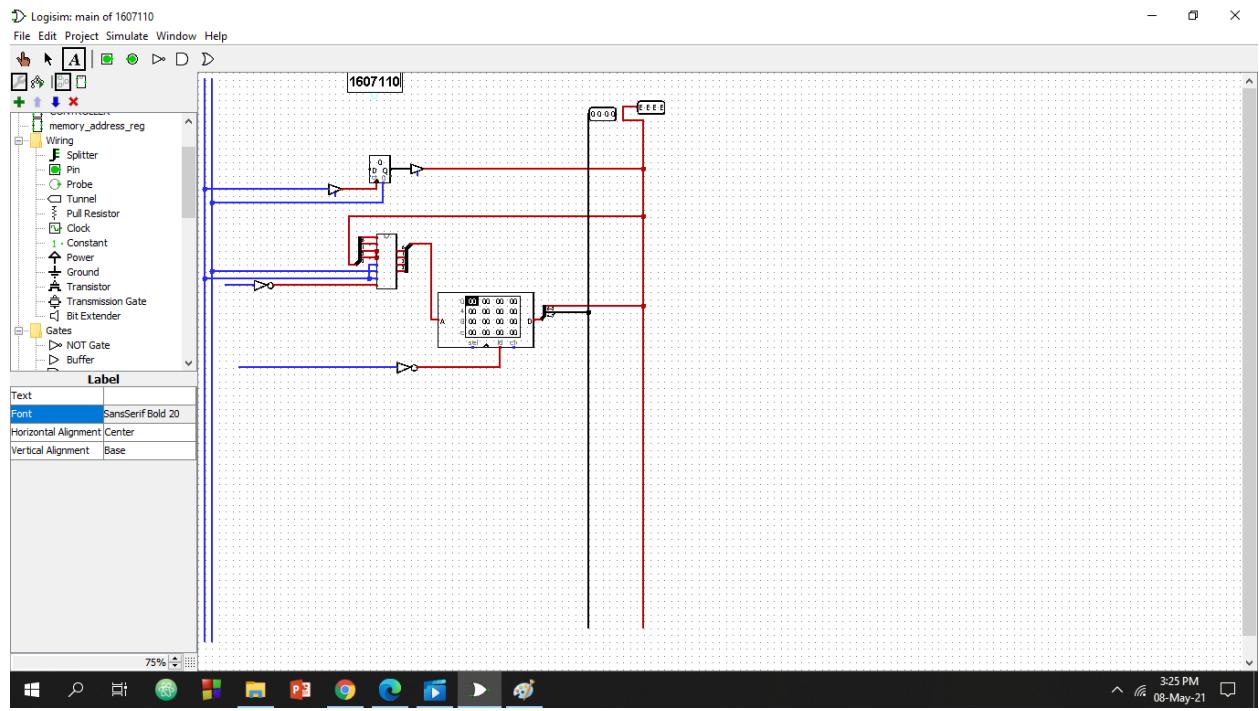


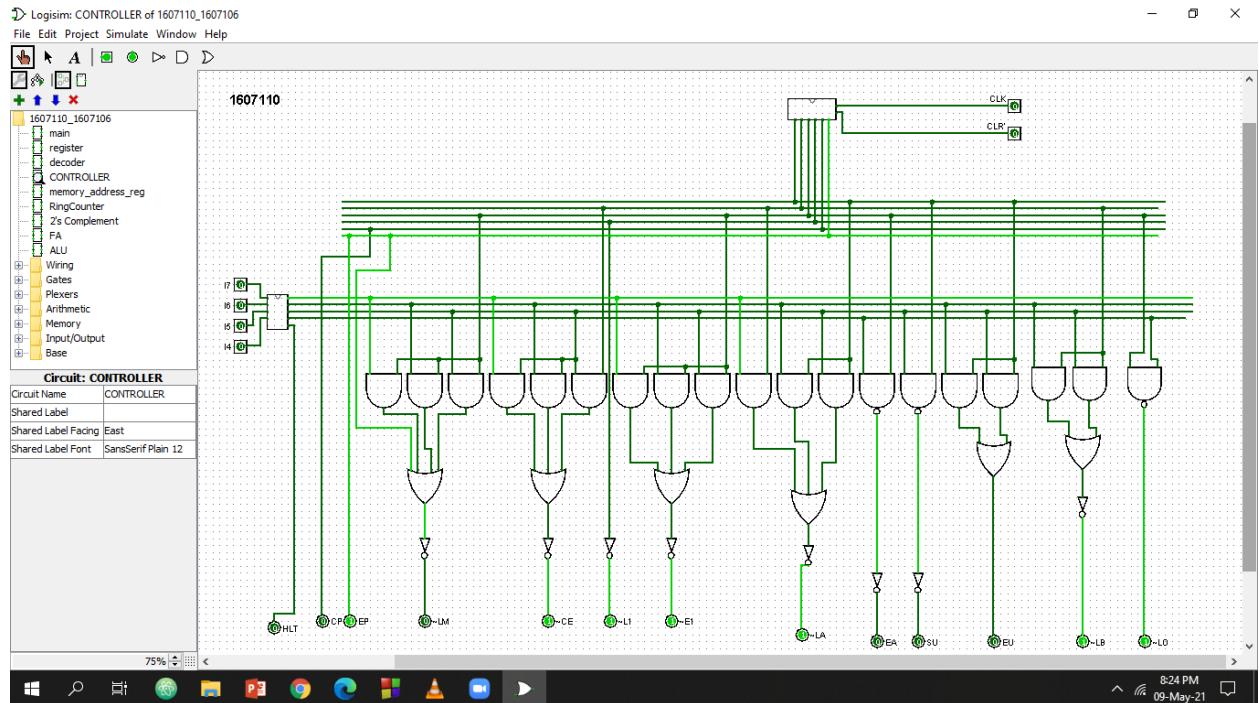
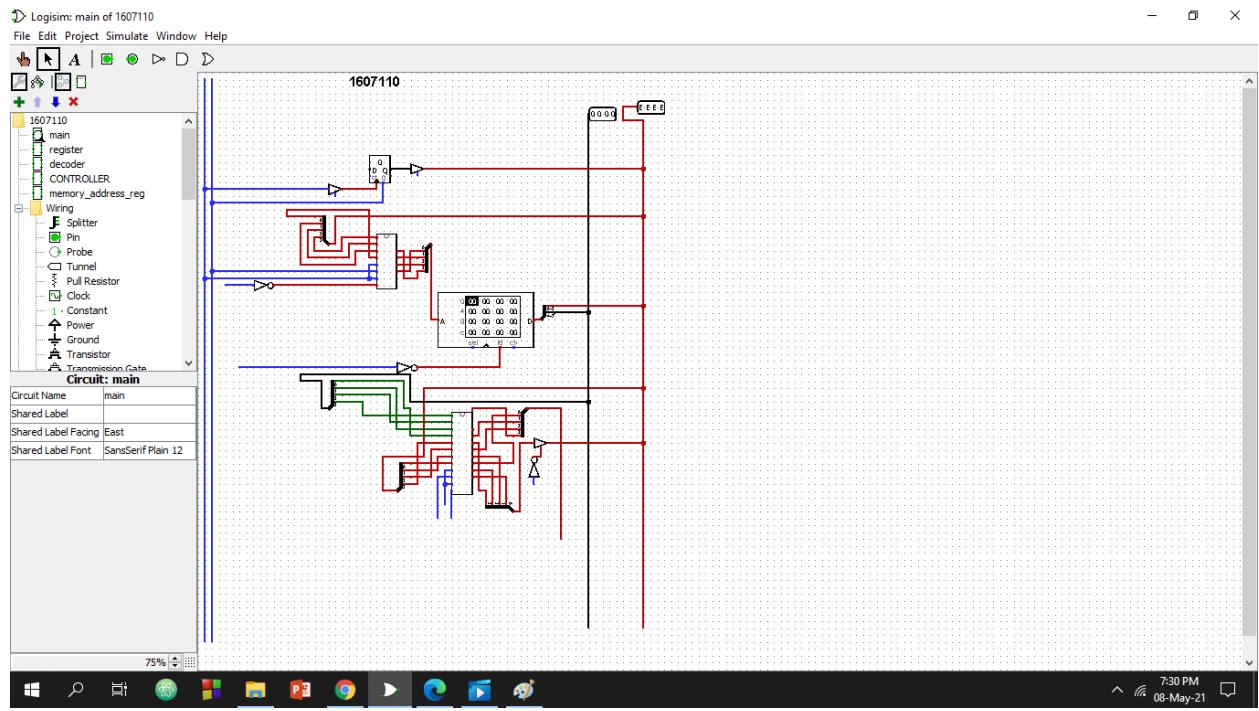
1607110 screenshots

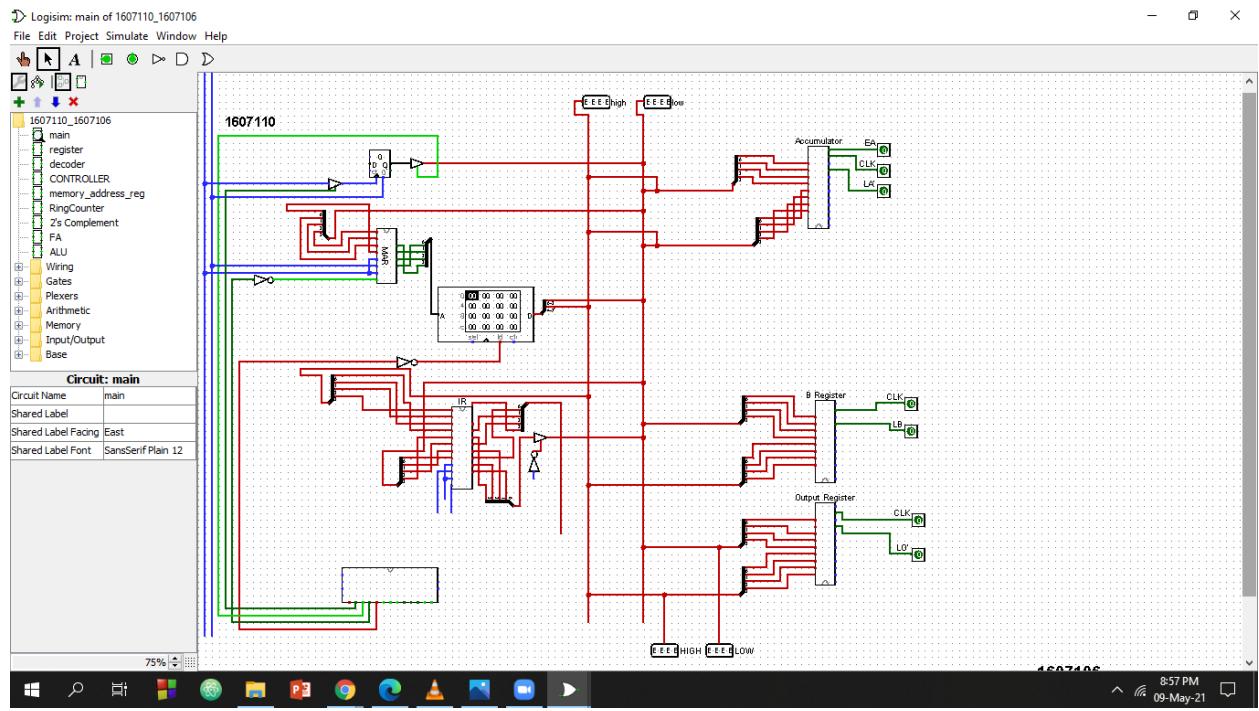


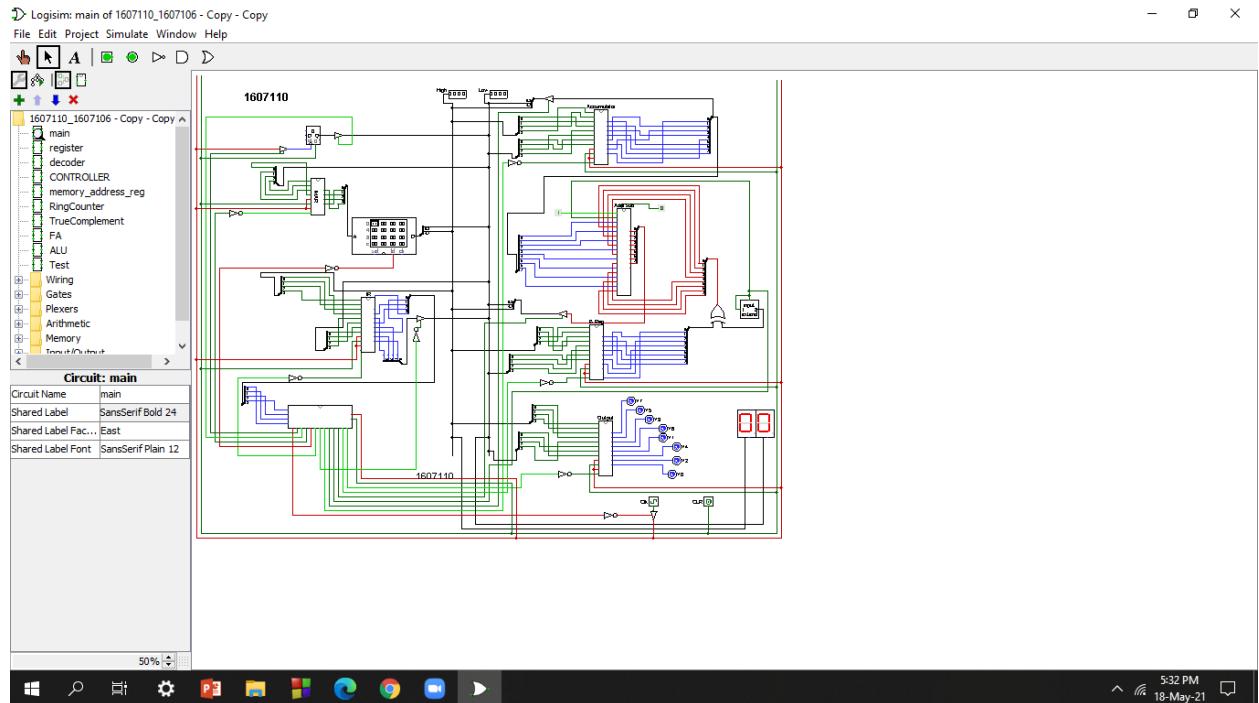
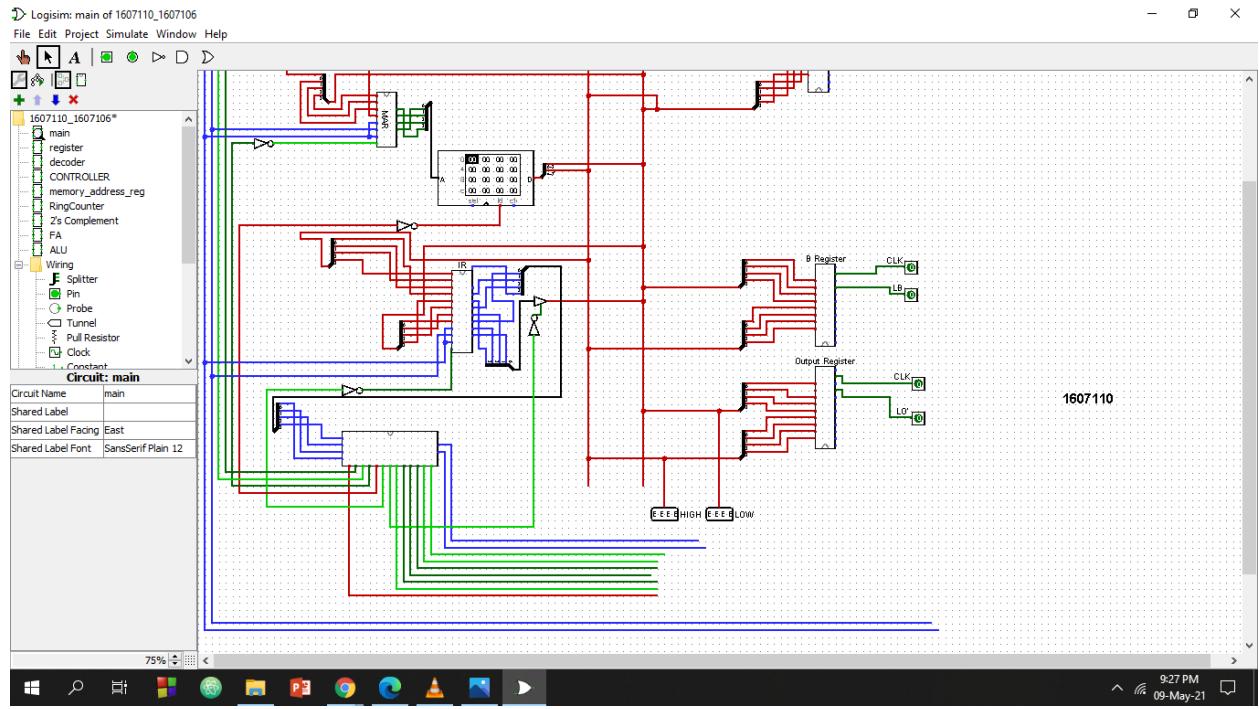


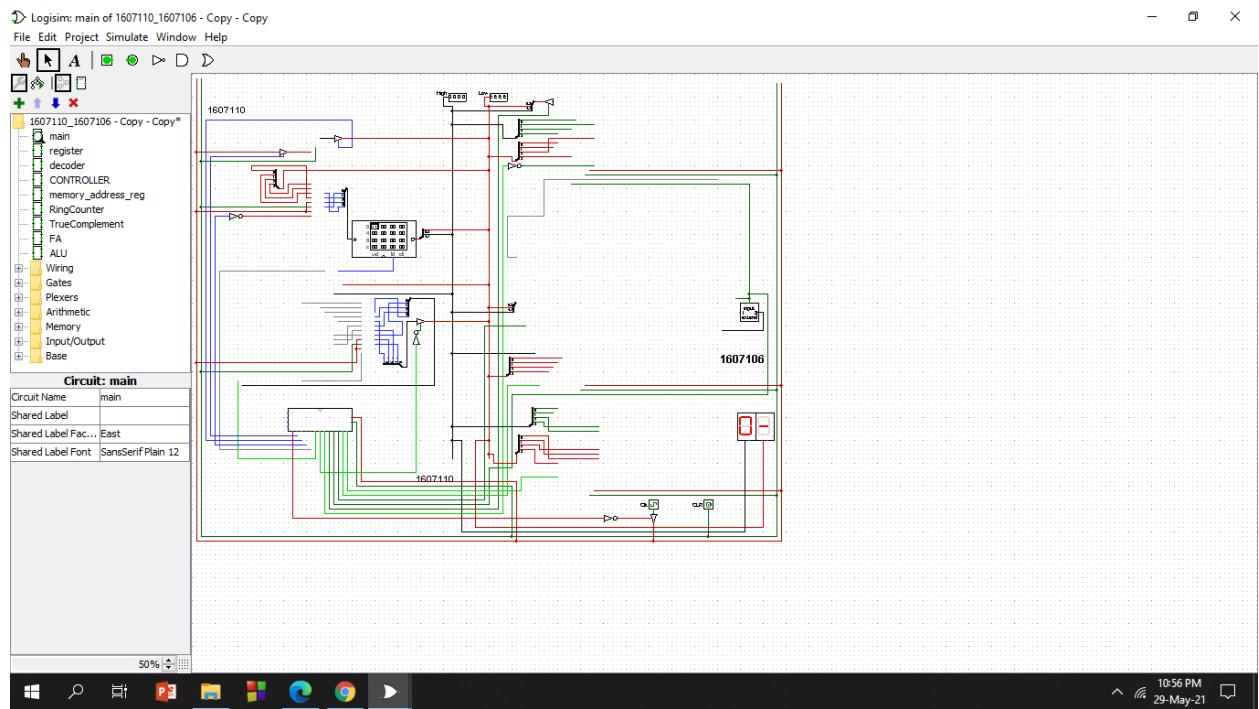
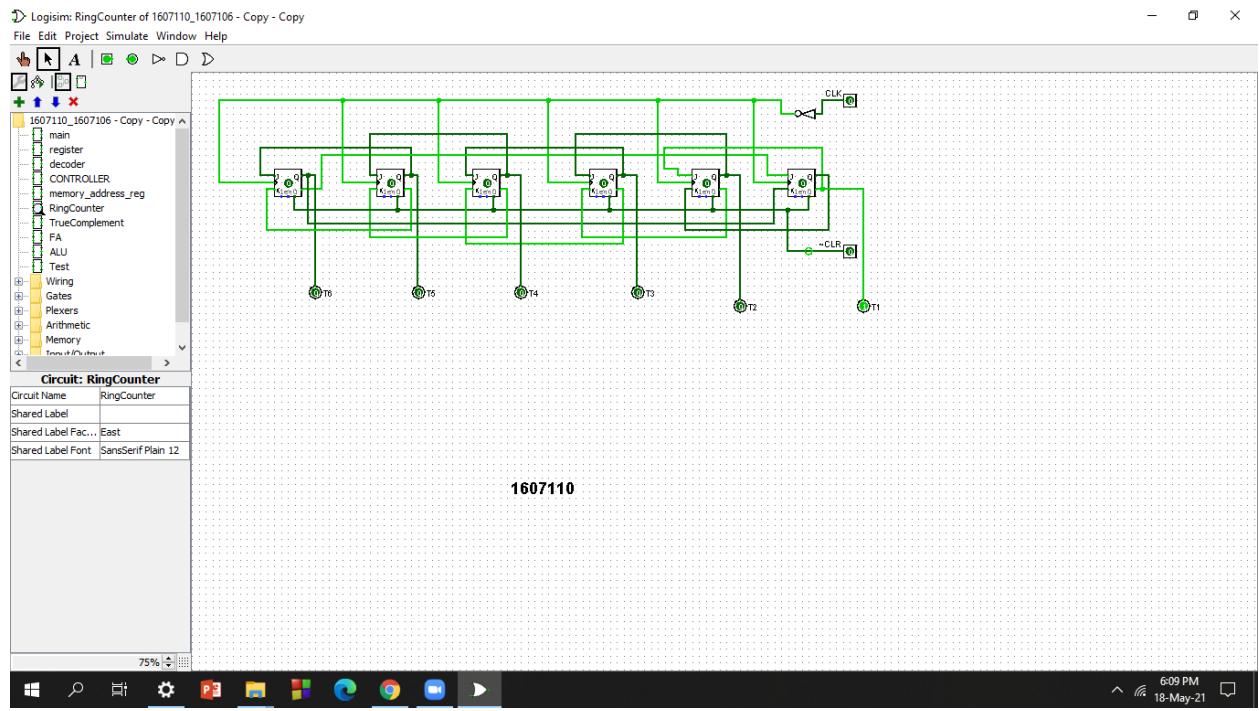


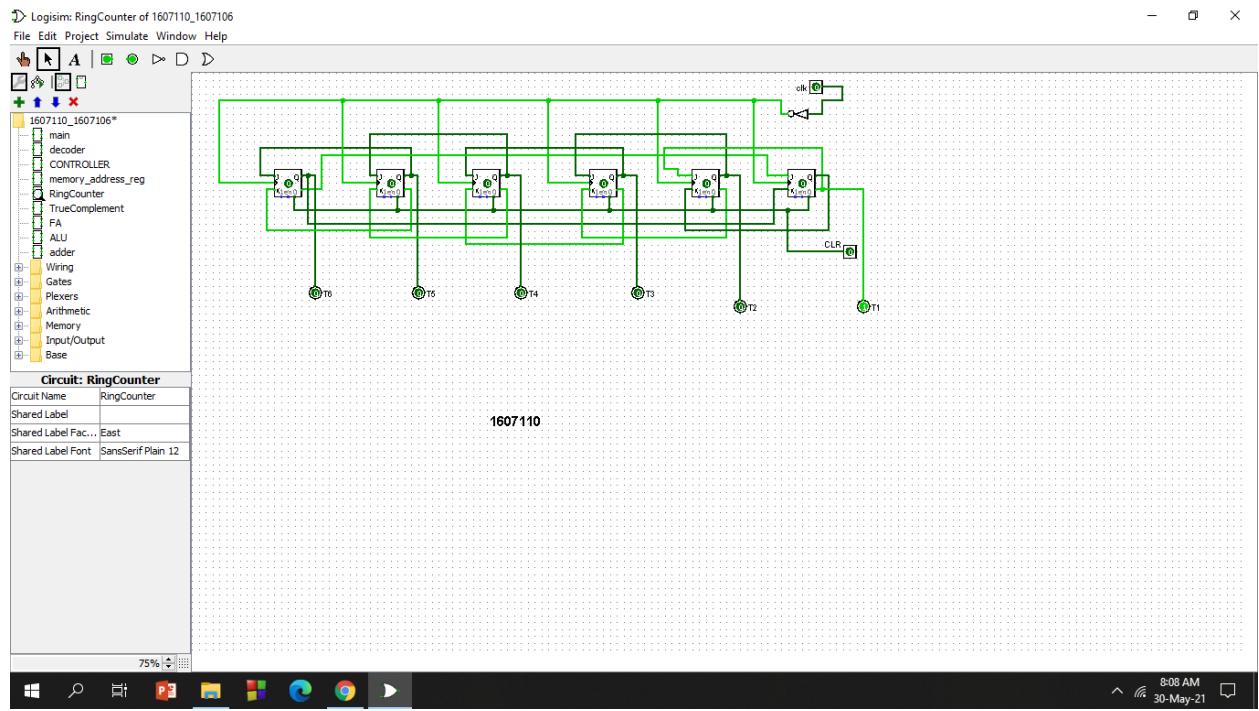
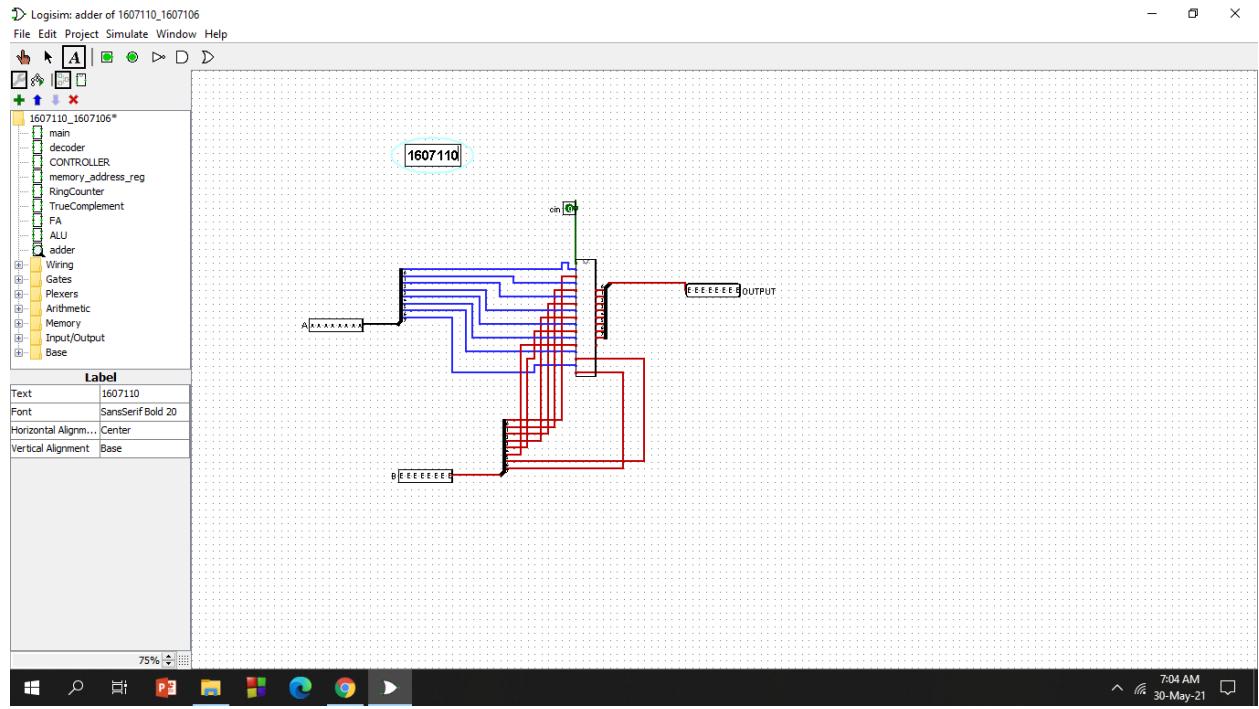


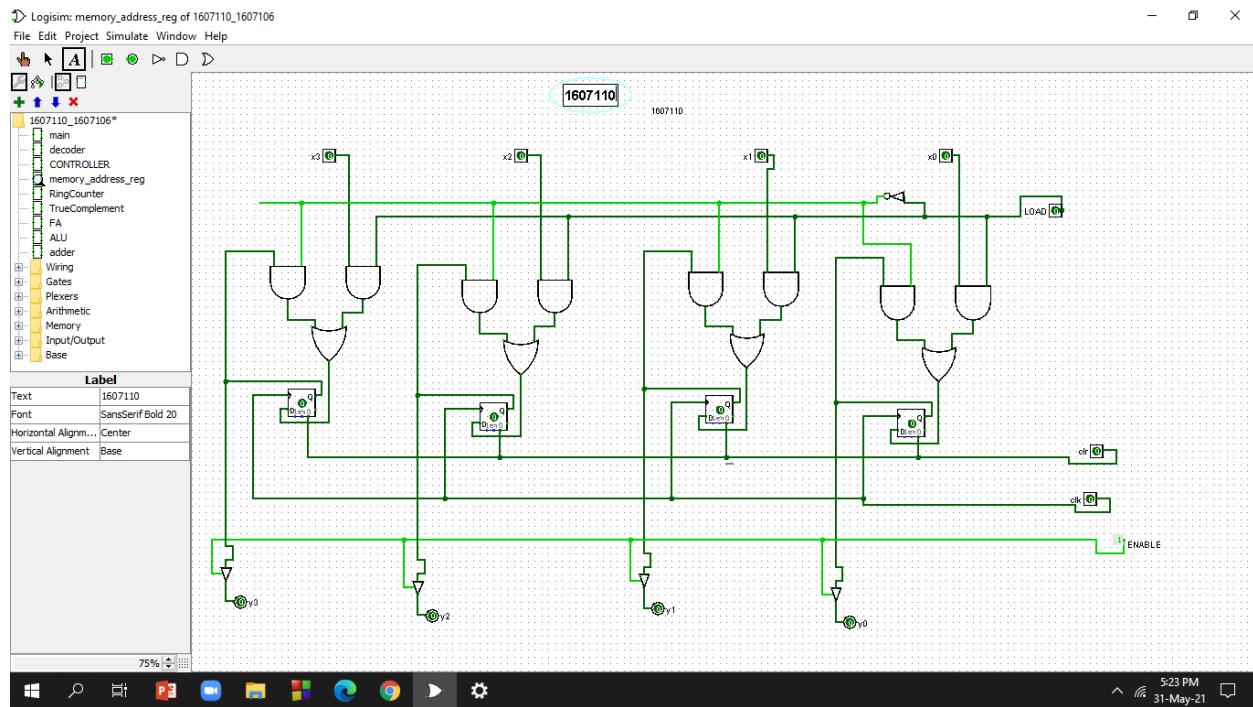
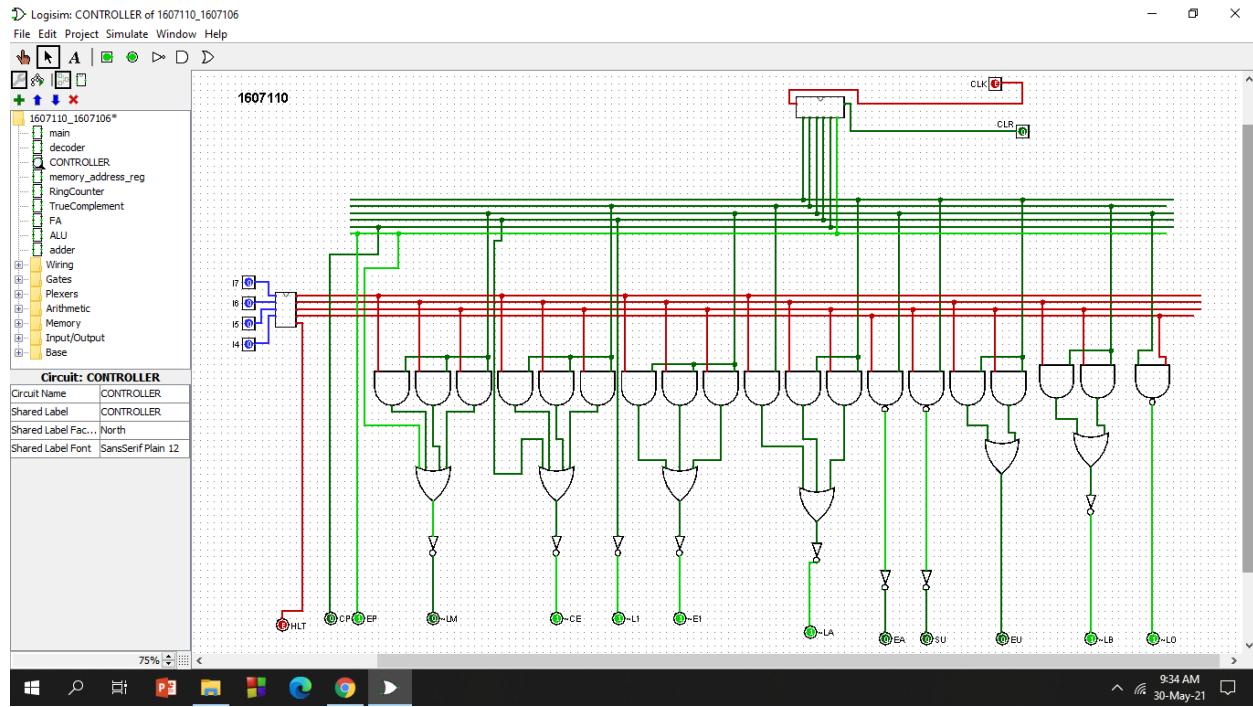


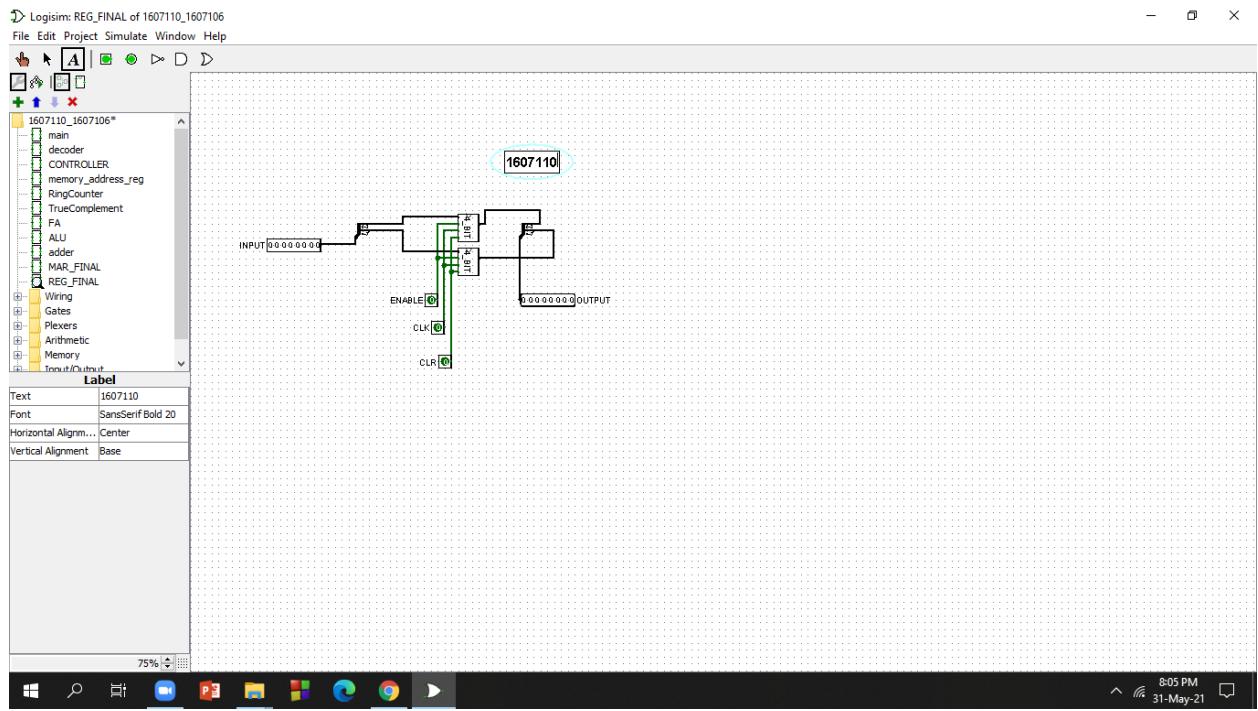
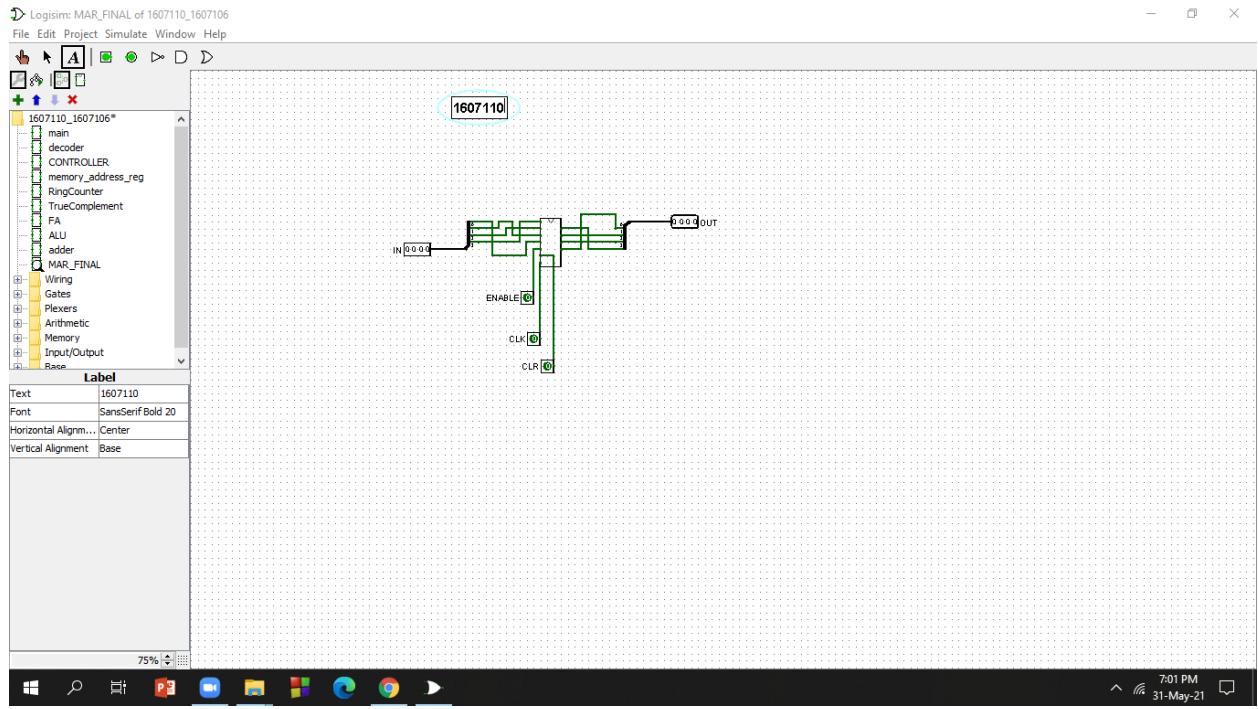


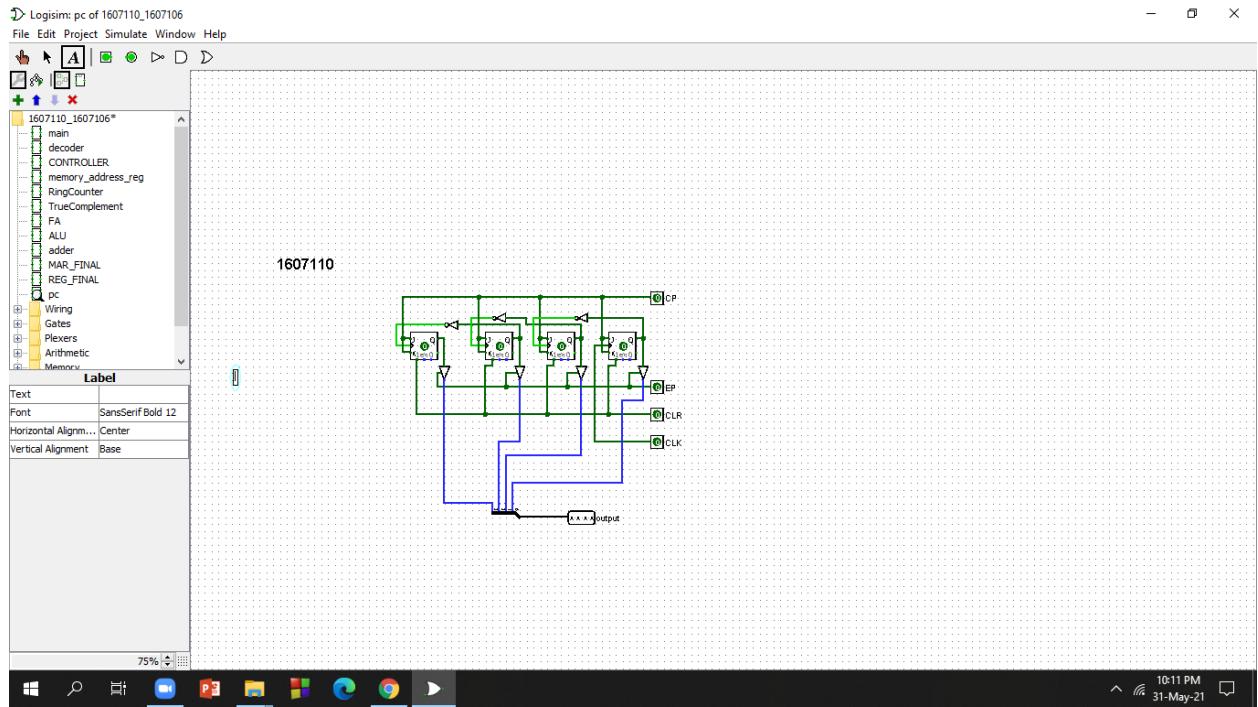
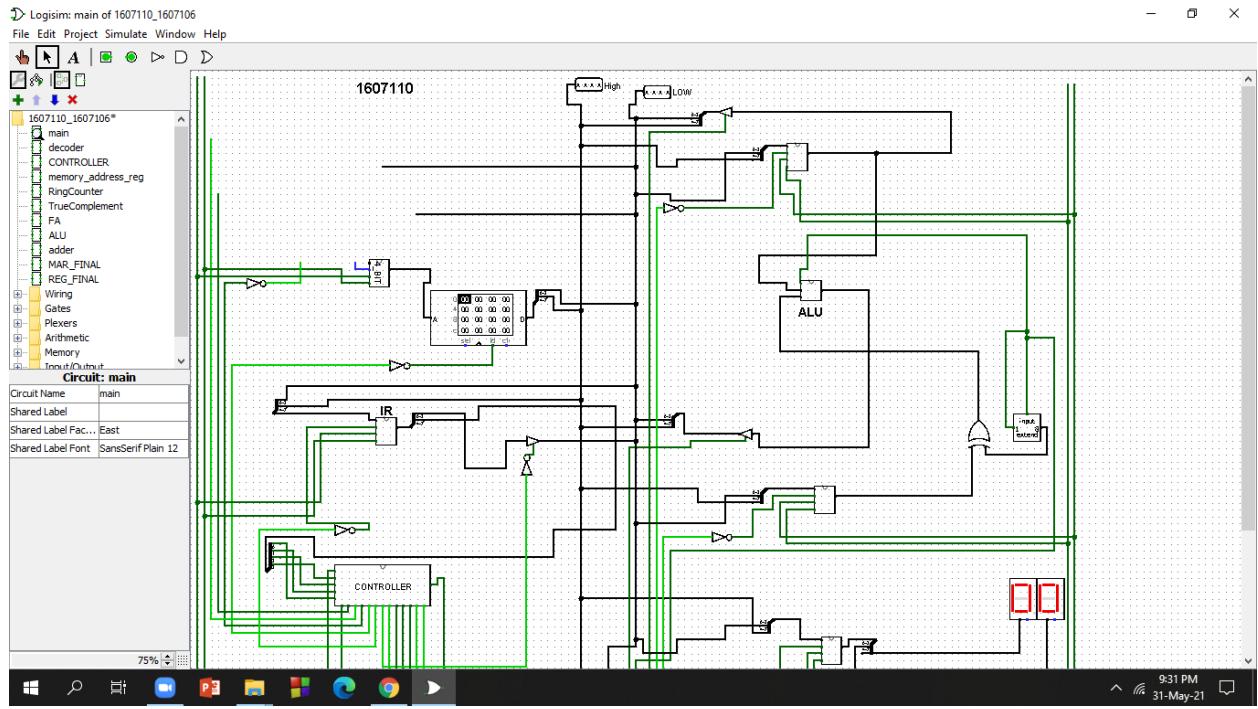


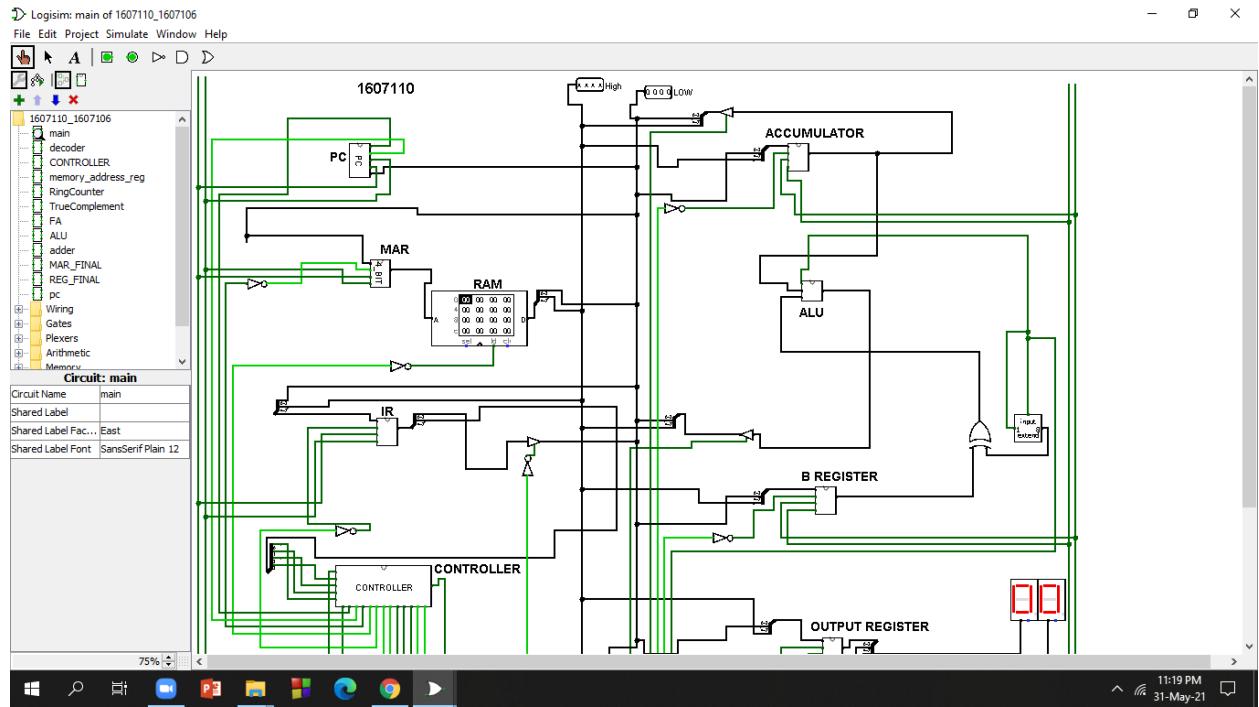
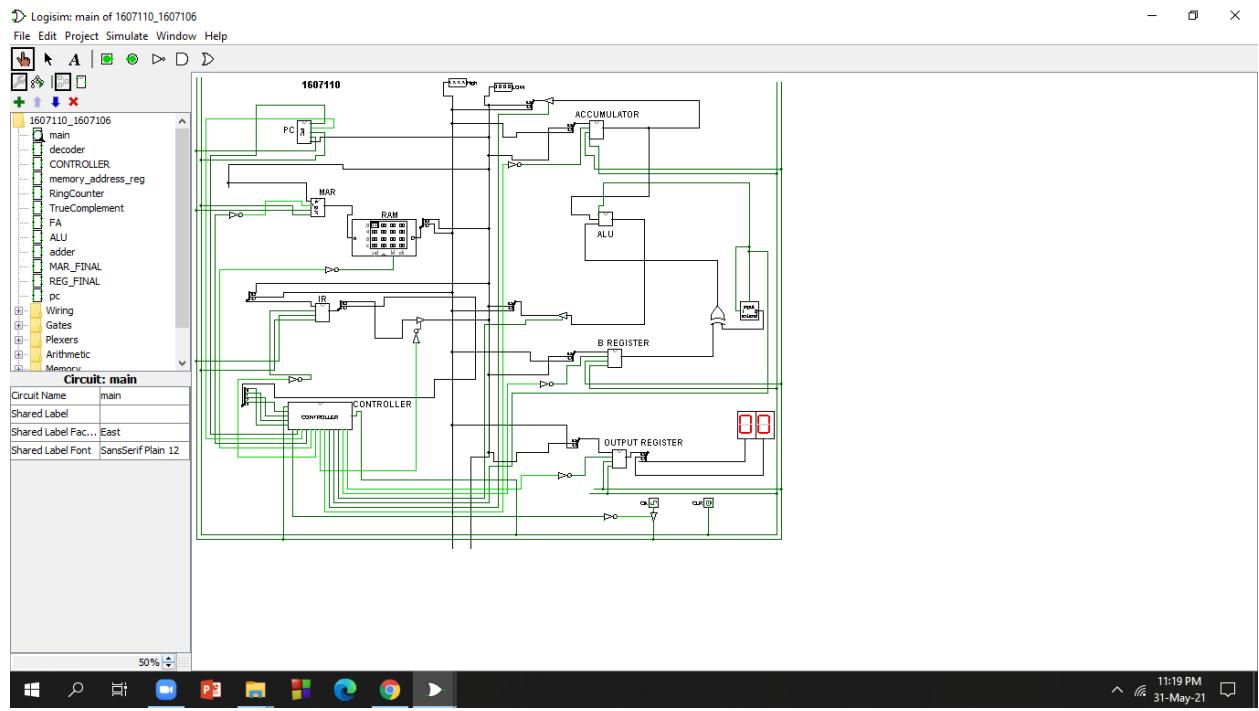


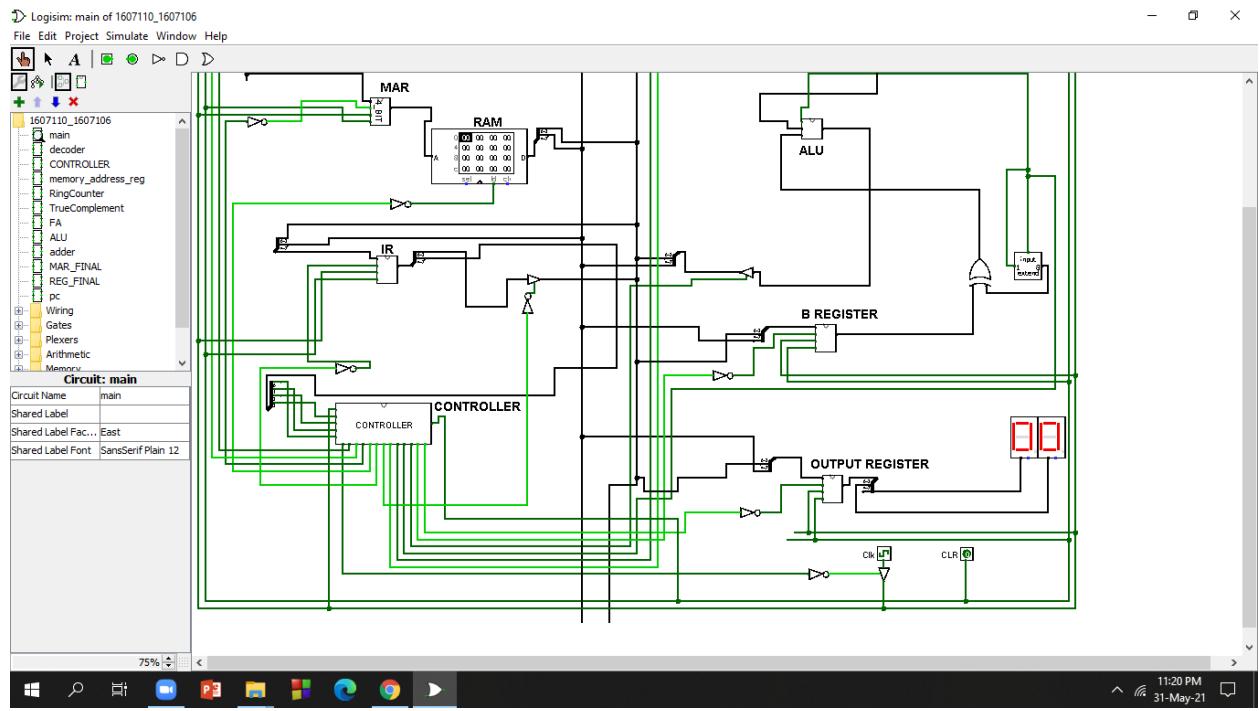












1607106 screenshots

A | D D

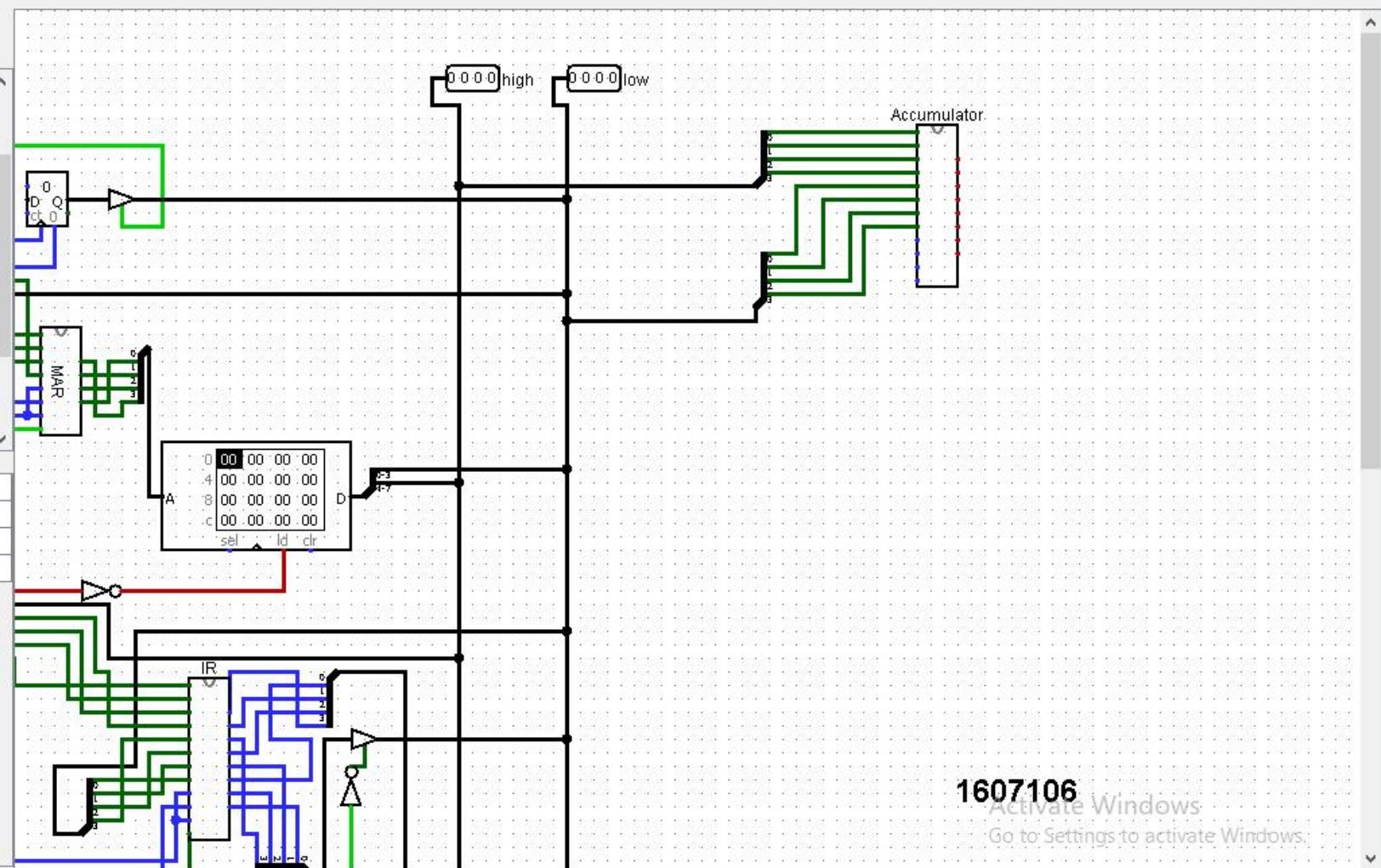
 memory_address_reg
RingCounter
2's Complement
FA
ALU

+ Wiring

- Splitter
- Pin
- Probe
- Tunnel
- Pull Resistor
- Clock
- Constant
- Power
- Ground
- Transistor
- Transmission Gate
- Bit Extender

Circuit: main

Circuit Name	main
Shared Label	
Shared Label Facing	East
Shared Label Font	SansSerif Plain 12



1607106

Activate Windows
Go to Settings to activate Windows.

Logisim: main of 1607110_1607106 - Copy

File Edit Project Simulate Window Help

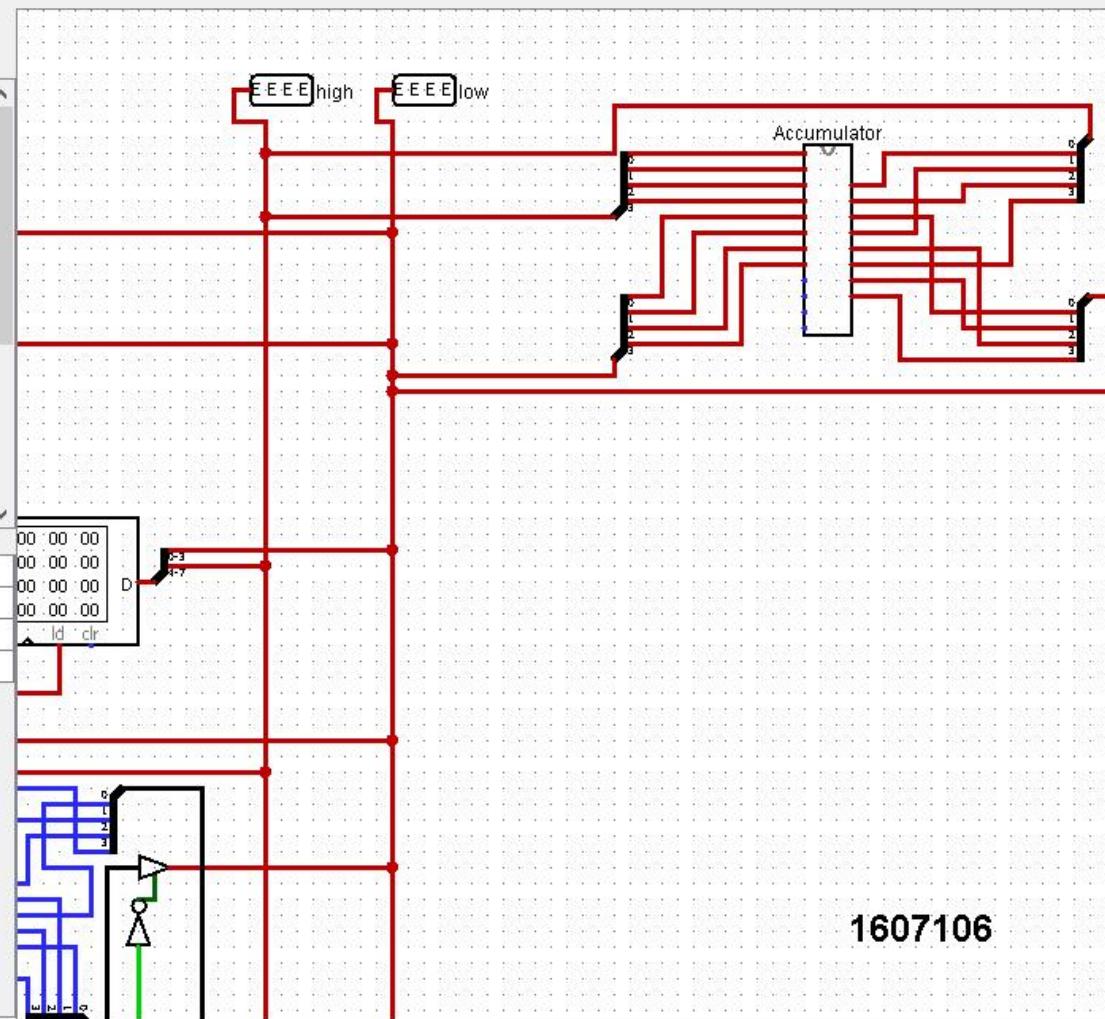


1607110_1607106 - Copy

- main
- register
- decoder
- CONTROLLER
- memory_address_reg
- RingCounter
- 2's Complement
- FA
- ALU
- Wiring
 - Splitter
 - Pin
 - Probe
 - Tunnel
 - Pull Resistor
 - Clock
 - Constant

Circuit: main

Circuit Name	main
Shared Label	
Shared Label Facing	East
Shared Label Font	SansSerif Plain 12



1607106

Activate Windows
Go to Settings to activate Windows.

100%



11:35 AM
5/10/2021

File Edit Project Simulate Window Help

A set of small tool icons including a hand, selection, copy, paste, and zoom.



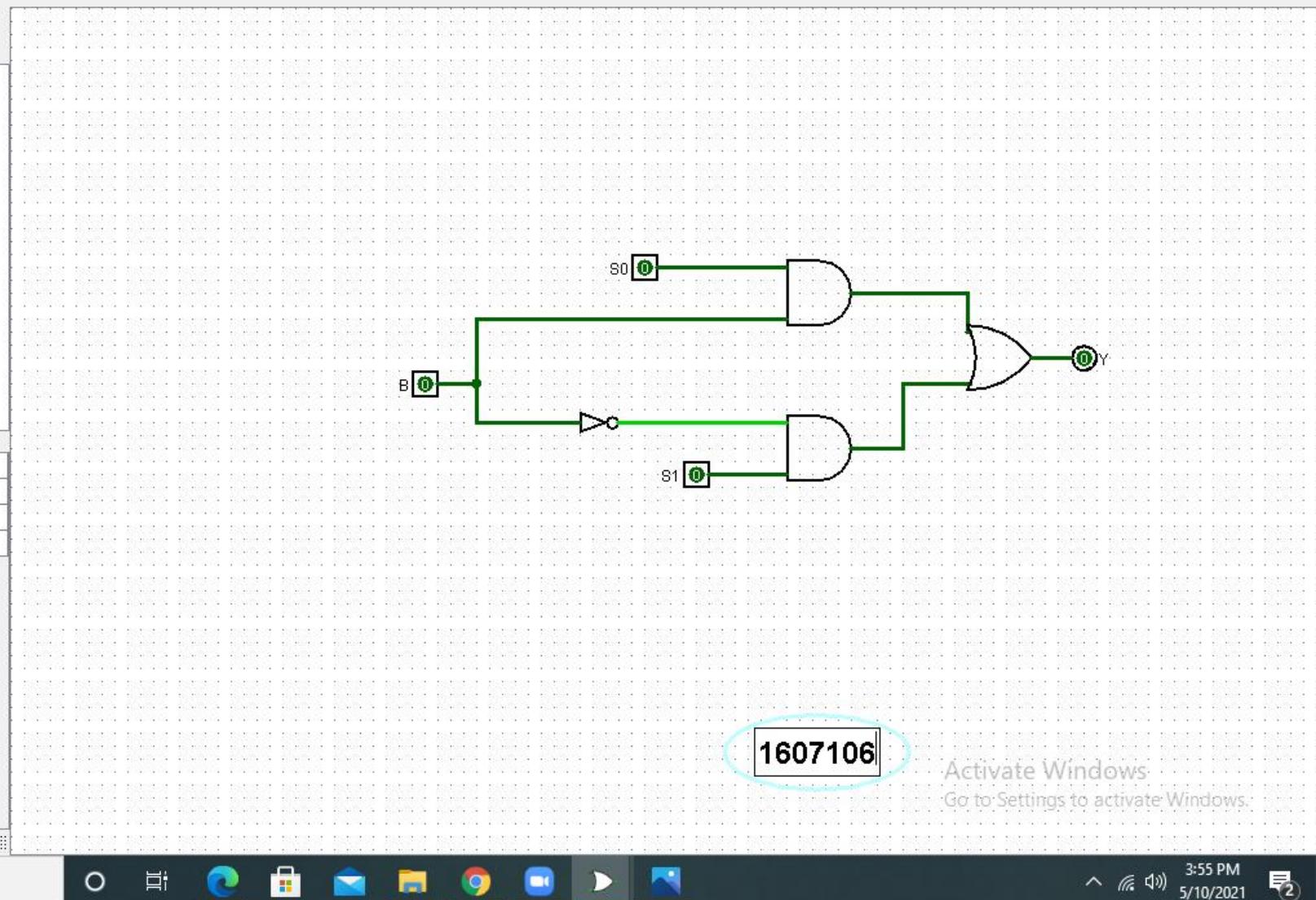
1607110_1607106 - Copy*

- main
- register
- decoder
- CONTROLLER
- memory_address_reg
- RingCounter
- TrueComplement

+... Wiring
+... Gates
+... Plexers
+... Arithmetic
+... Memory
+... Input/Output
+... Base

Label

Text	1607106
Font	SansSerif Bold 24
Horizontal Alignment	Center
Vertical Alignment	Base



1607106

Activate Windows
Go to Settings to activate Windows.

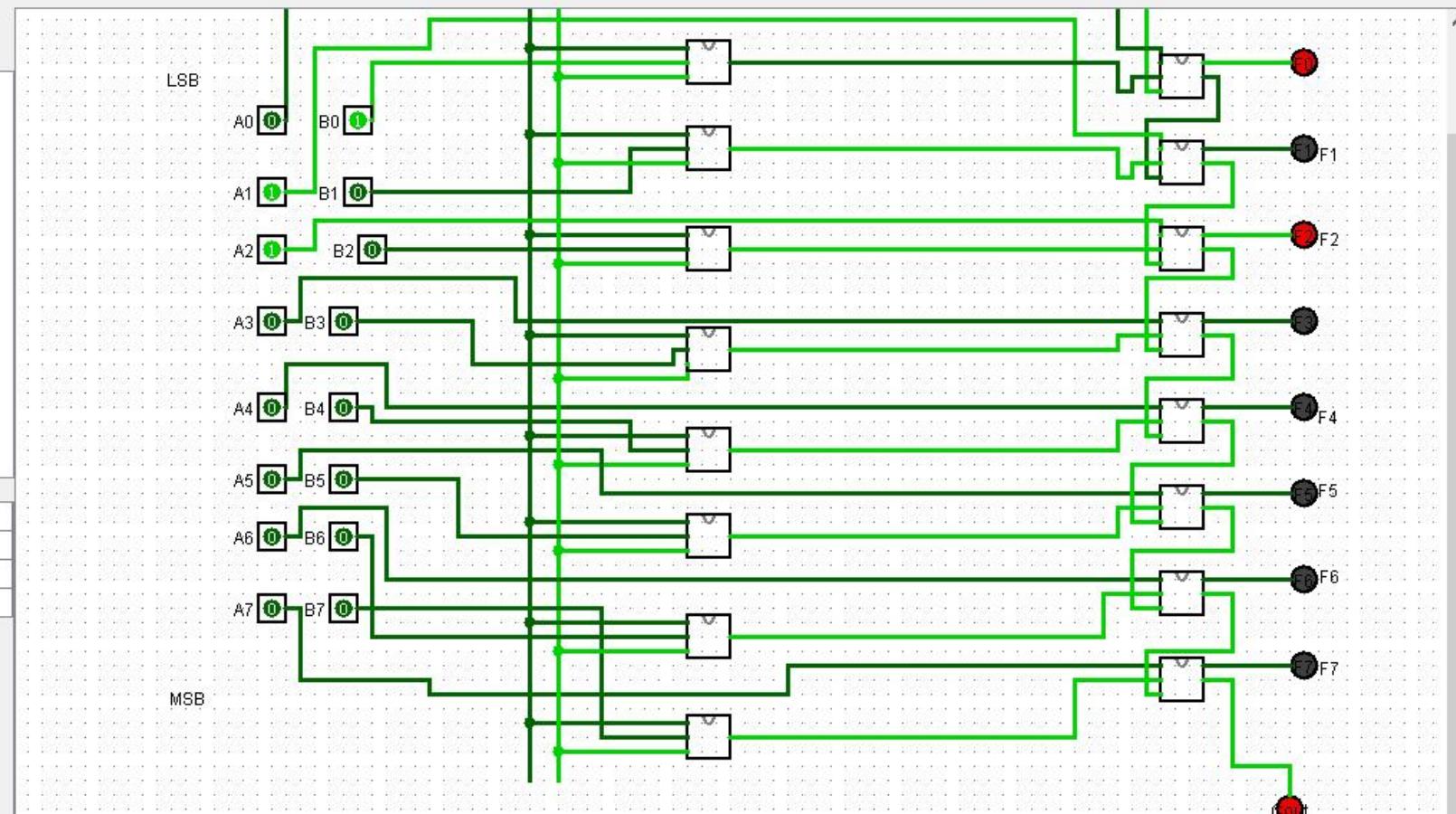
Label

Text 1607106

Font SansSerif Bold 24

Horizontal Alignment Center

Vertical Alignment Base



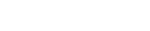
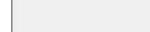
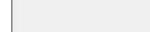
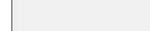
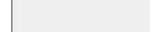
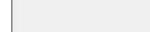
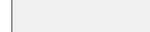
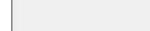
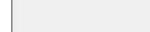
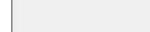
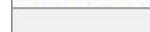
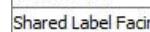
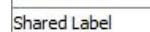
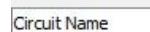
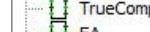
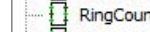
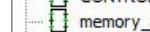
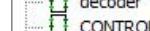
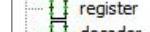
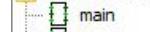
1607106

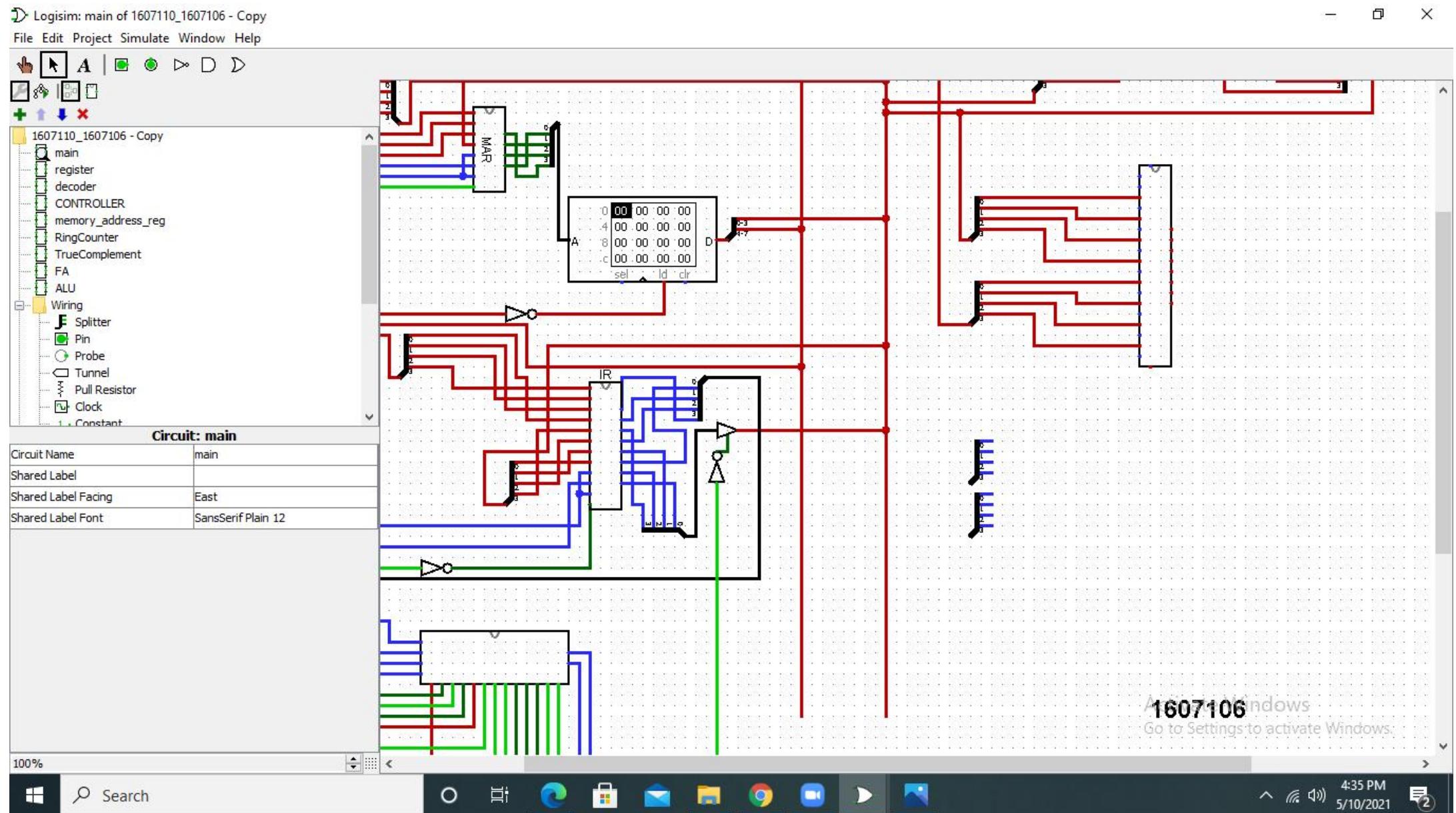
Activate Windows
Go to Settings to activate Windows!

Logisim: ALU of 1607110_1607106 - Copy

File Edit Project Simulate Window Help

A | D | D





Logisim: main of 1607110_1607106 - Copy

File Edit Project Simulate Window Help

A | D | D



+ up down x

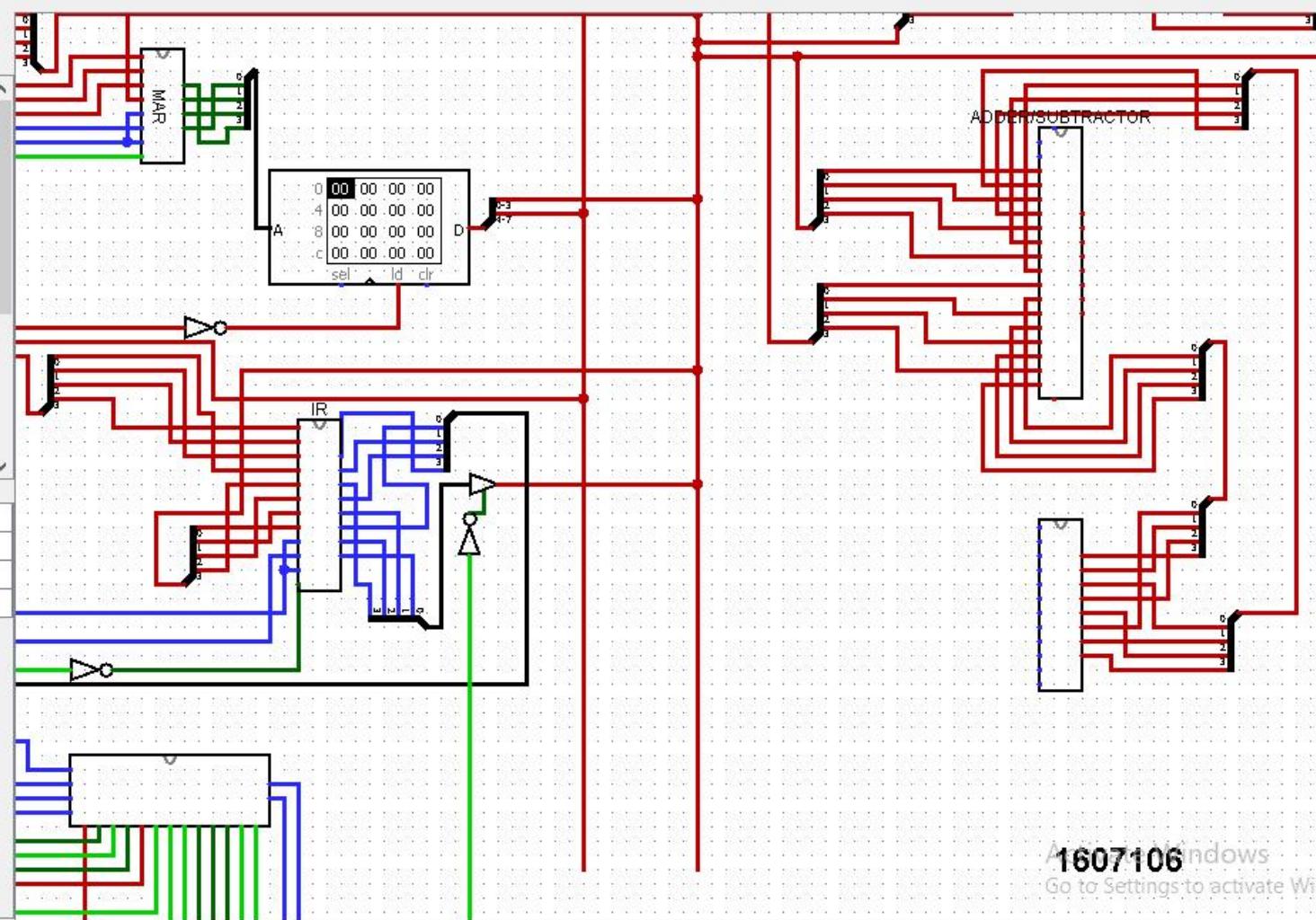
1607110_1607106 - Copy

- main
- register
- decoder
- CONTROLLER
- memory_address_reg
- RingCounter
- TrueComplement
- FA
- ALU

- Wiring
 - Splitter
 - Pin
 - Probe
 - Tunnel
 - Pull Resistor
 - Clock
 - Constant

Circuit: main

Circuit Name	main
Shared Label	
Shared Label Facing	East
Shared Label Font	SansSerif Plain 12



Windows
Go to Settings to activate Windows!

100%



Search



4:45 PM
5/10/2021



A | D | D



1607110_1607106 - Copy

main

register

decoder

CONTROLLER

memory_address_reg

RingCounter

TrueComplement

FA

ALU

Wiring

Splitter

Pin

Probe

Tunnel

Pull Resistor

Clock

Constant

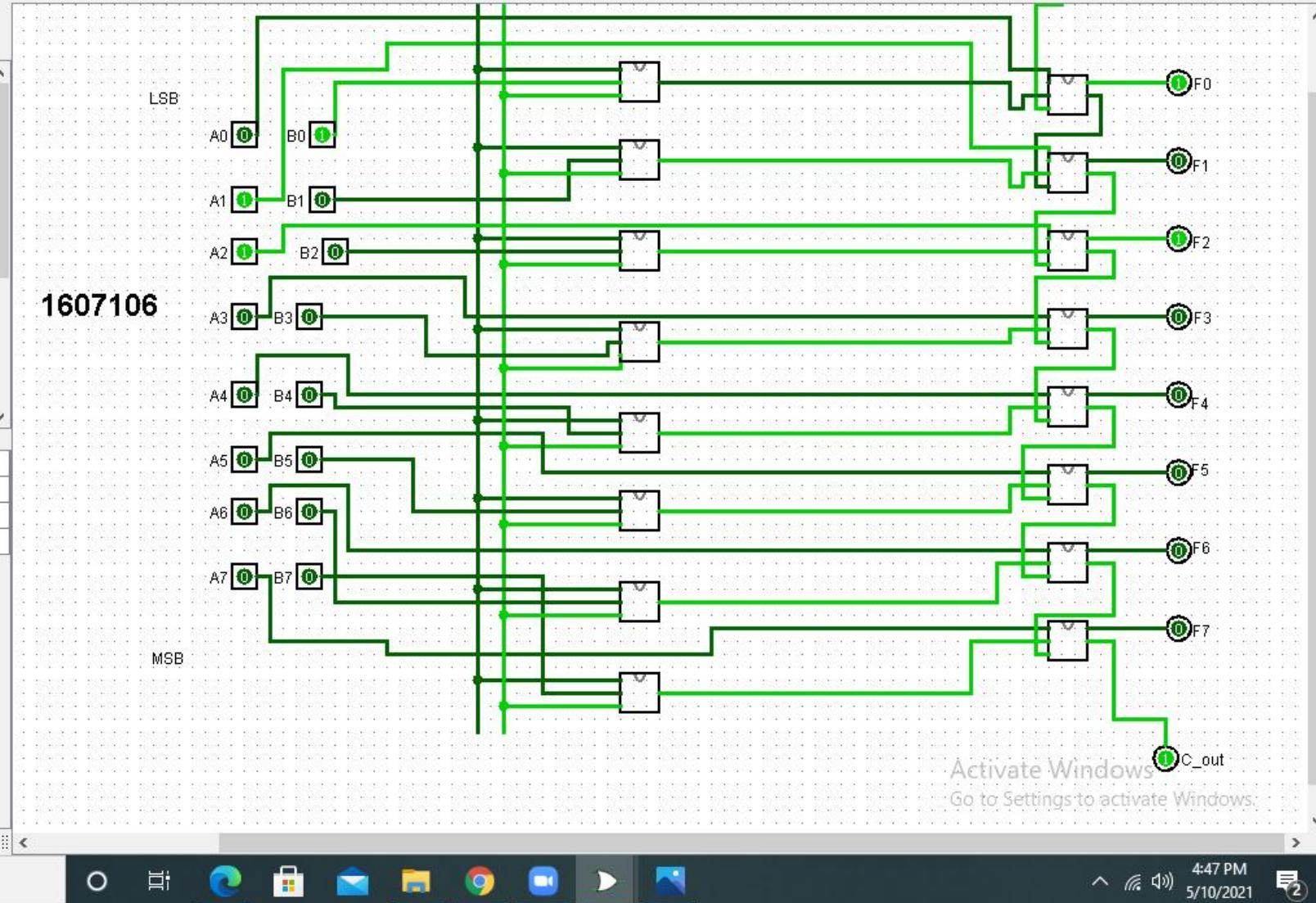
Circuit: ALU

Circuit Name ALU

Shared Label

Shared Label Facing East

Shared Label Font SansSerif Plain 12



Logisim: main of 1607110_1607106 - Copy

File Edit Project Simulate Window Help

A | D | D



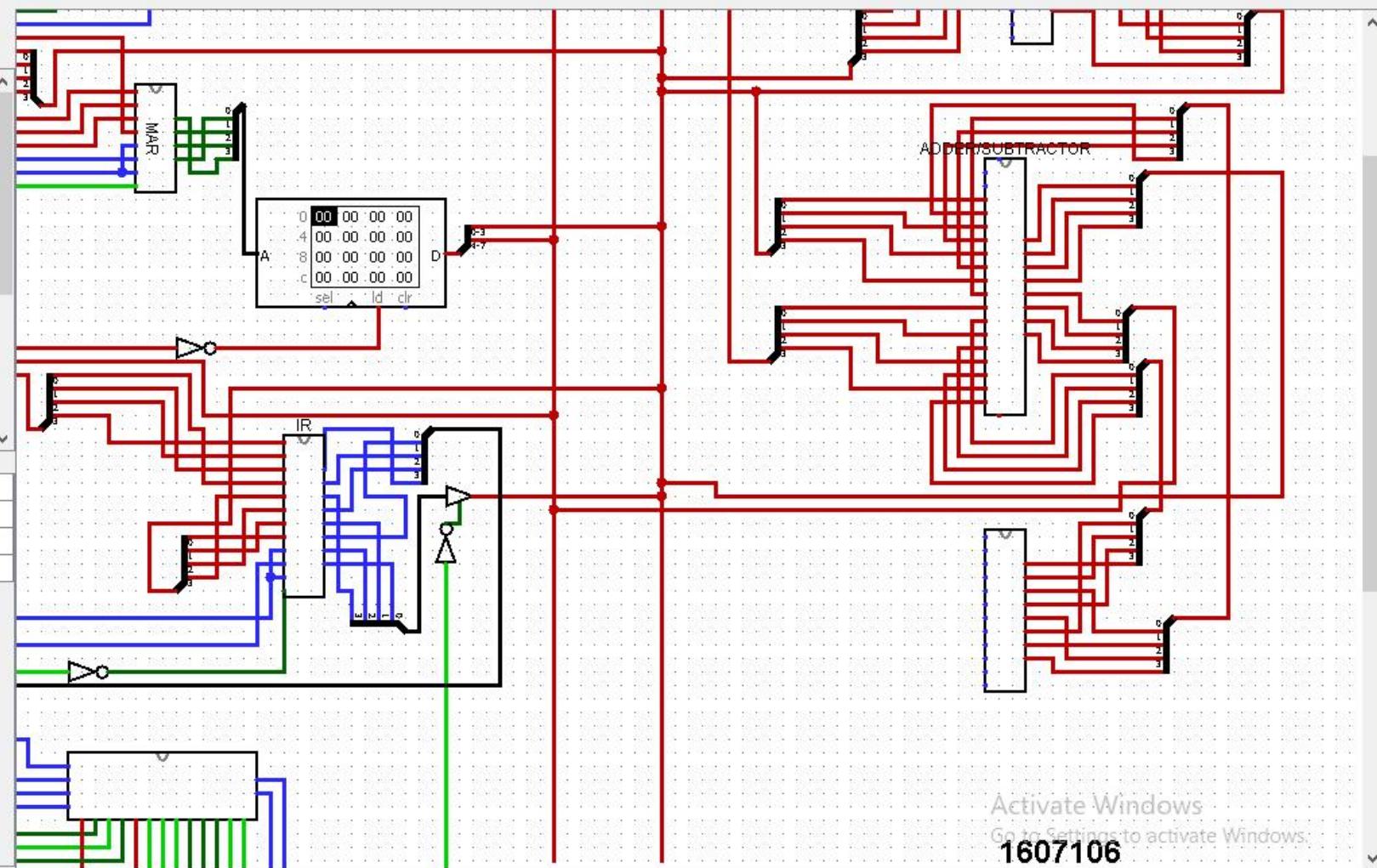
1607110_1607106 - Copy

- main
- register
- decoder
- CONTROLLER
- memory_address_reg
- RingCounter
- TrueComplement
- FA
- ALU

- Wiring
 - Splitter
 - Pin
 - Probe
 - Tunnel
 - Pull Resistor
 - Clock
 - Constant

Circuit: main

Circuit Name	main
Shared Label	
Shared Label Facing	East
Shared Label Font	SansSerif Plain 12



Activate Windows
Go to Settings to activate Windows:
1607106

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Logisim: main of 1607110_1607106 - Copy

File Edit Project Simulate Window Help

A | D | D

Ground
Transistor
Transmission Gate
Bit Extender

Gates
Plexers
Arithmetic
Memory

Input/Output
Button
Joystick
Keyboard
LED
7-Segment Display
Hex Digit Display
LED Matrix
TTY

Base

Circuit: main

Circuit Name	main
Shared Label	
Shared Label Facing	East
Shared Label Font	SansSerif Plain 12

1607110

1607106

Activate Windows:
Go to Settings to activate Windows.

100%



Search



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Logisim: main of 1607110_1607106 - Copy

File Edit Project Simulate Window Help

↶ A ↷ ↴ ↵ ↸ ↹

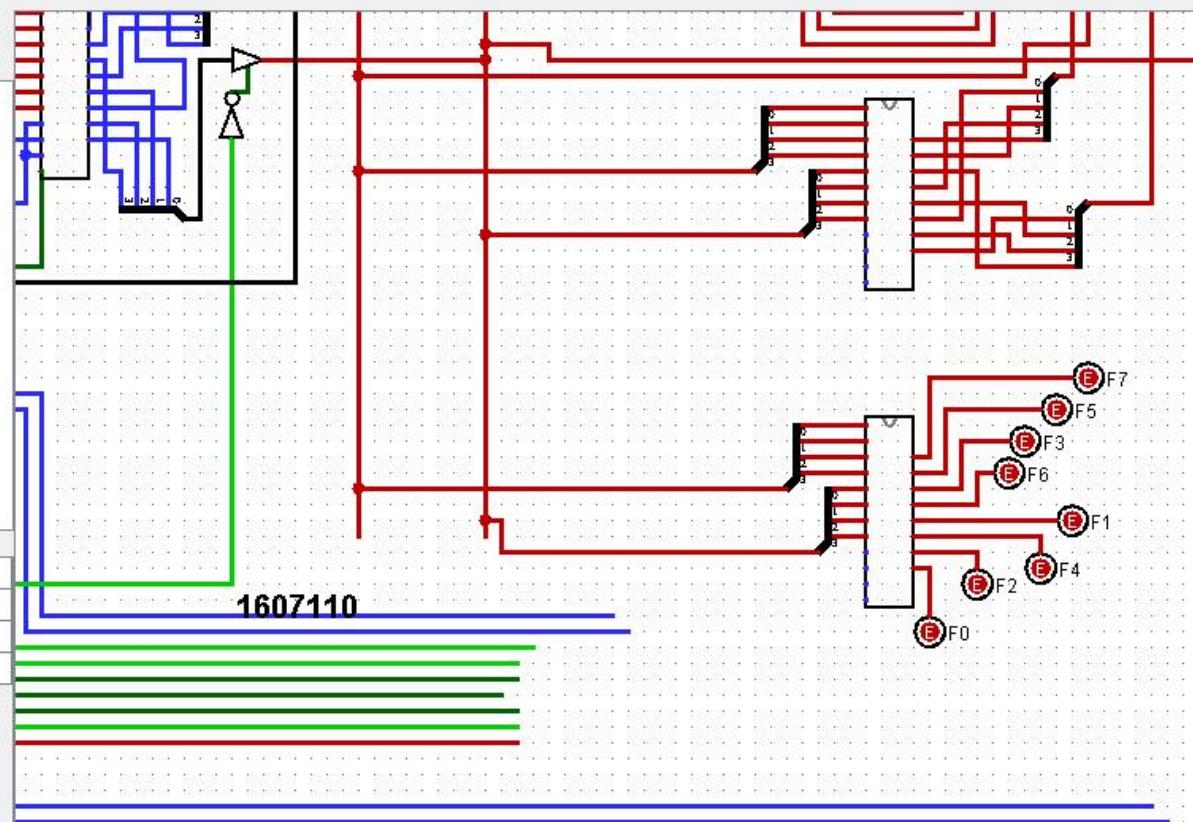


1607110_1607106 - Copy

- main
- register
- decoder
- CONTROLLER
- memory_address_reg
- RingCounter
- TrueComplement
- FA
- ALU
- Wiring
- Gates
- Plexers
- Arithmetic
- Memory
- Input/Output
- Base

Circuit: main

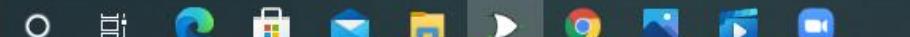
Circuit Name	main
Shared Label	
Shared Label Facing	East
Shared Label Font	SansSerif Plain 12



Activate Windows 1607106
Go to Settings to activate Windows.

100%

Search



8:04 PM

5/11/2021

3

Logisim: main of 1607110_1607106 - Copy

File Edit Project Simulate Window Help

A | D | D

memory_address_reg

RingCounter

TrueComplement

FA

ALU

Wiring

Splitter

Pin

Probe

Tunnel

Pull Resistor

Clock

Constant

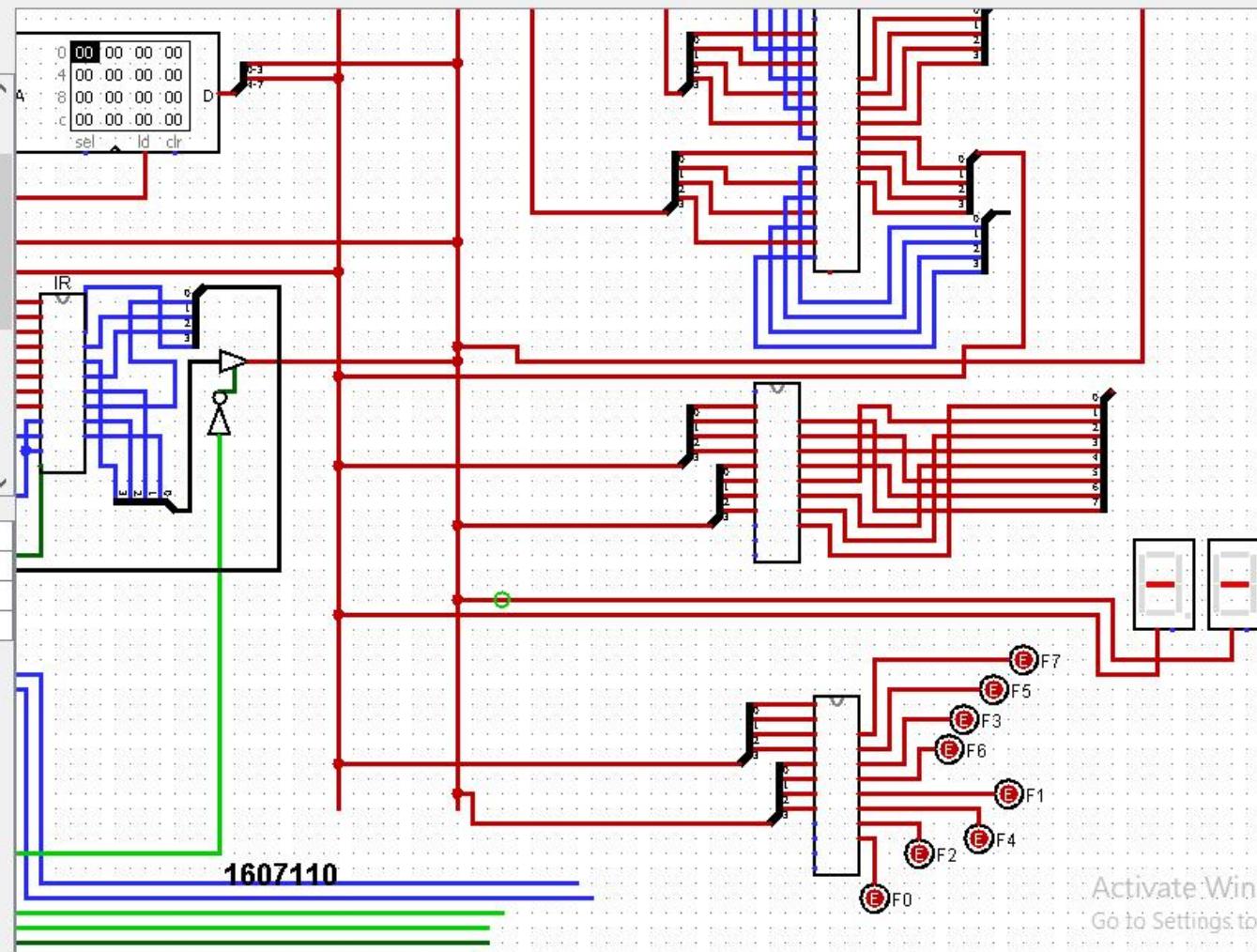
Power

Ground

Transistor

Transmission Gate

Bit Extender

Activate Windows
Go to Settings to activate Windows.
[Get Started](#)

100%

Search



8:19 PM

5/11/2021

3

Logisim: main of 1607110_1607106 - Copy

File Edit Project Simulate Window Help

↶ A ↷ ↴ ↵ ↸ ↹

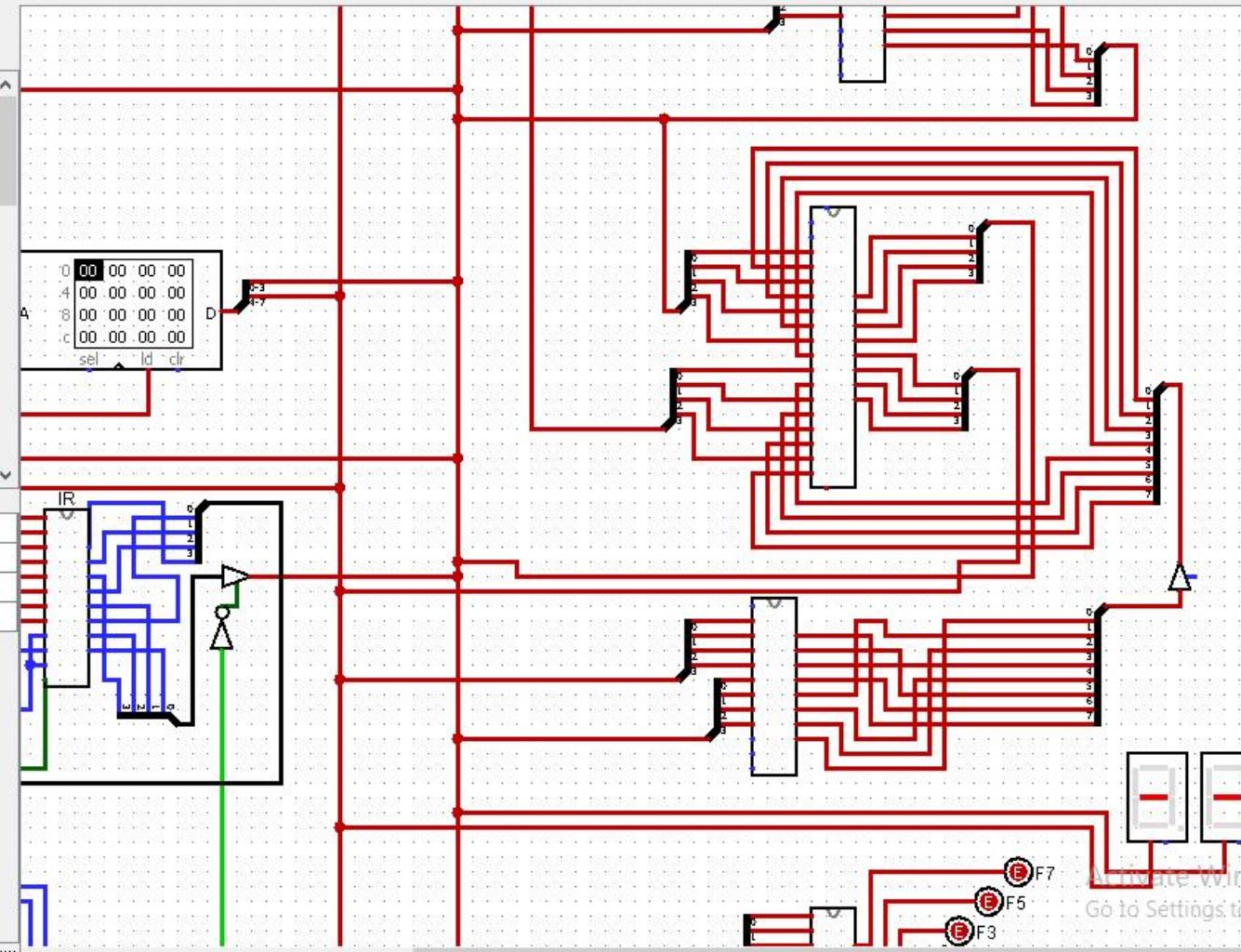


1607110_1607106 - Copy

- main
- register
- decoder
- CONTROLLER
- memory_address_reg
- RingCounter
- TrueComplement
- FA
- ALU
- Wiring
 - Splitter
 - Pin
 - Probe
 - Tunnel
 - Pull Resistor
 - Clock
 - Constant

Circuit: main

Circuit Name main
Shared Label
Shared Label Facing East
Shared Label Font SansSerif Plain 12



Activate Windows
Go to Settings to activate Windows.

100%



Search



8:26 PM
5/11/2021

3

Logisim: ALU of 1607110_1607106 - Copy

File Edit Project Simulate Window Help

A | D | D



1607110_1607106 - Copy

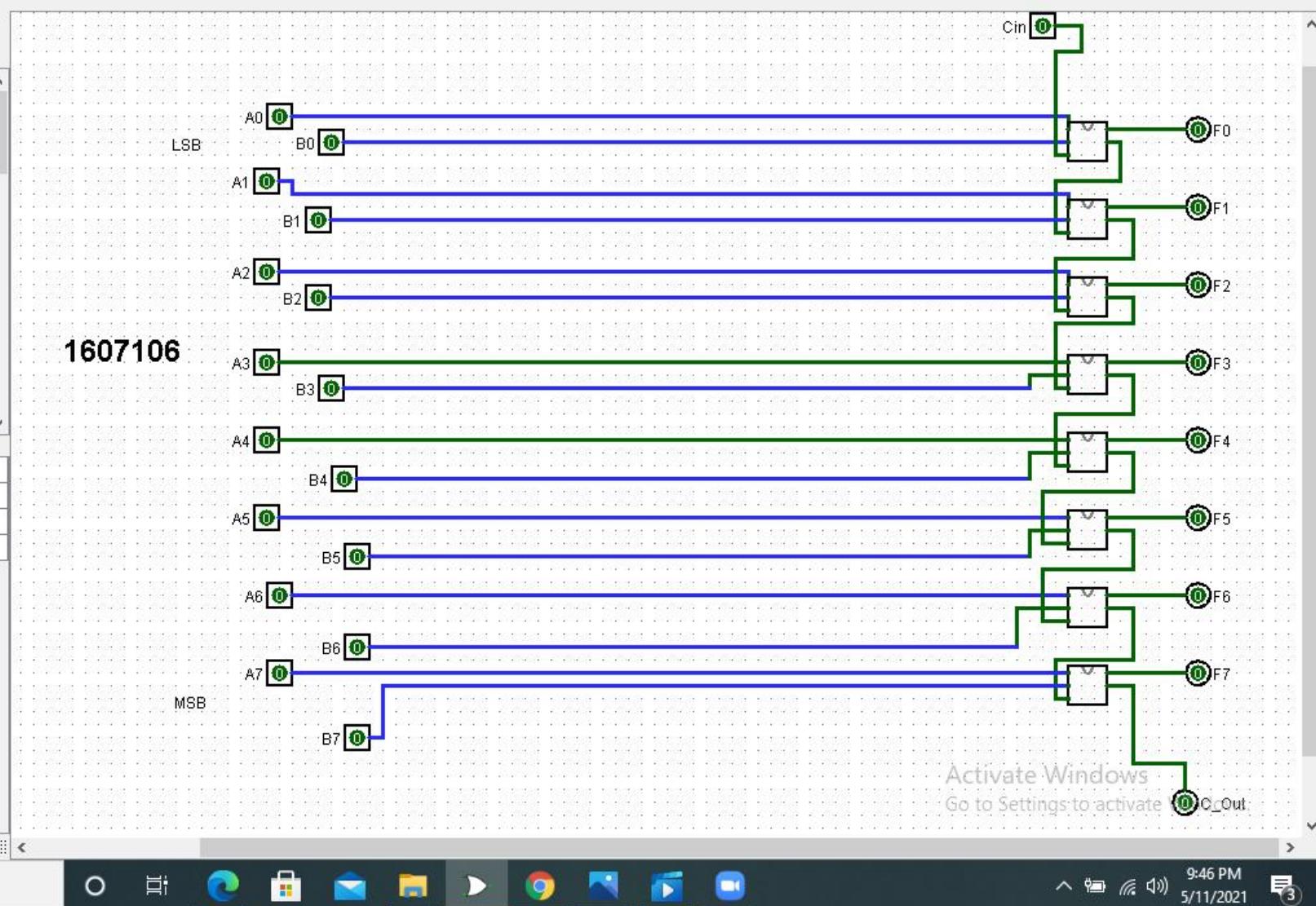
- main
- register
- decoder
- CONTROLLER
- memory_address_reg
- RingCounter
- TrueComplement
- FA
- ALU

- Wiring
 - Splitter
 - Pin
 - Probe
 - Tunnel
 - Pull Resistor
 - Clock
 - Constant

Circuit: ALU

Circuit Name	ALU
Shared Label	
Shared Label Facing	East
Shared Label Font	SansSerif Plain 12

1607106



Activate Windows
Go to Settings to activate

100%

Search



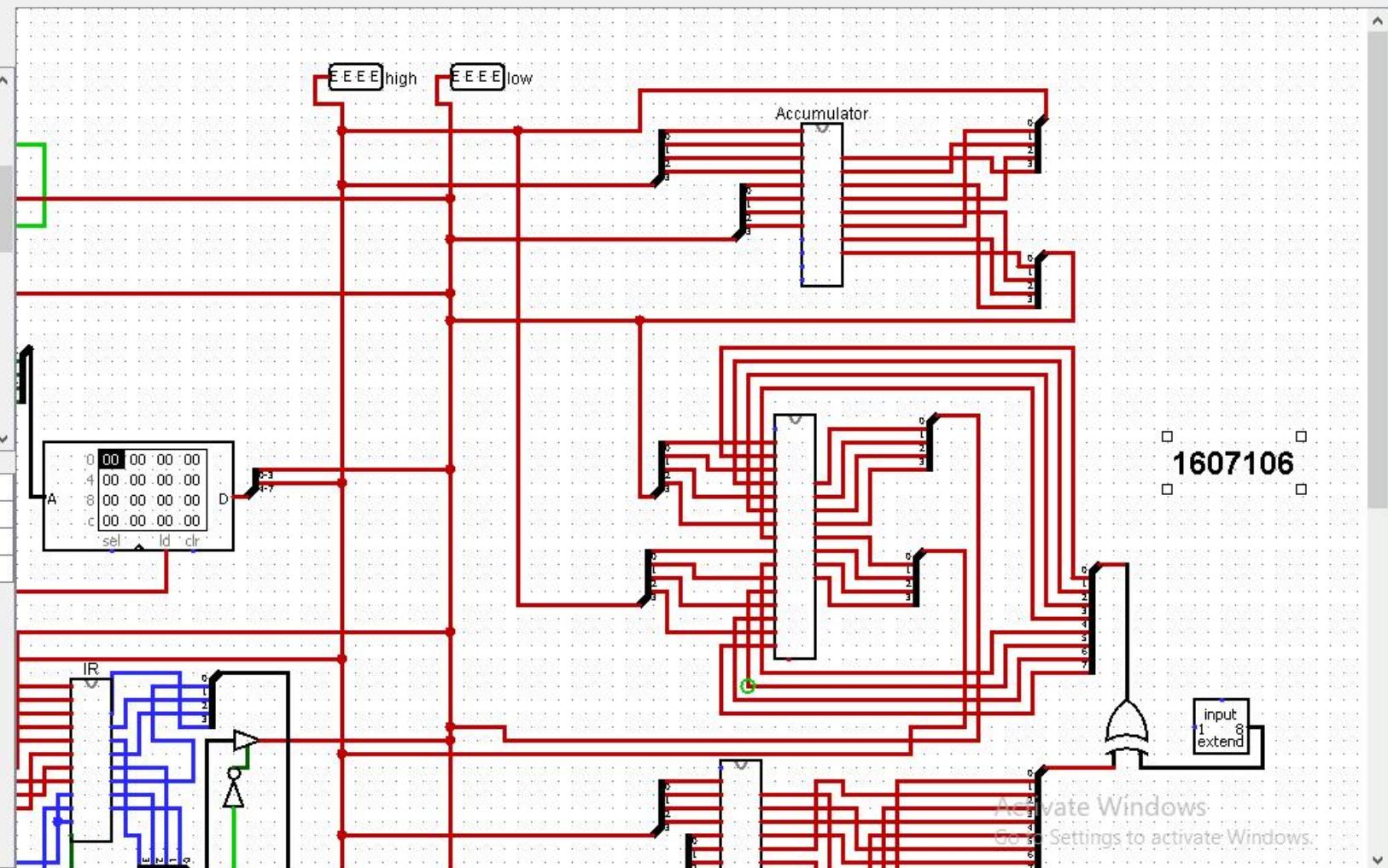
9:46 PM
5/11/2021



- Pull Resistor
- Clock
- Constant
- Power
- Ground
- Transistor
- Transmission Gate
- Bit Extender
- Gates
 - NOT Gate
 - Buffer
 - AND Gate
 - OR Gate
 - NAND Gate
 - NOR Gate
 - XOR Gate
 - XNOR Gate
 - Odd Parity

Selection: Label

Text	1607106
Font	SansSerif Bold 24
Horizontal Alignment	Center
Vertical Alignment	Base



Search



10:05 PM
5/11/2021



Logisim: main of 1607110_1607106 - Copy

File Edit Project Simulate Window Help

A | D | D

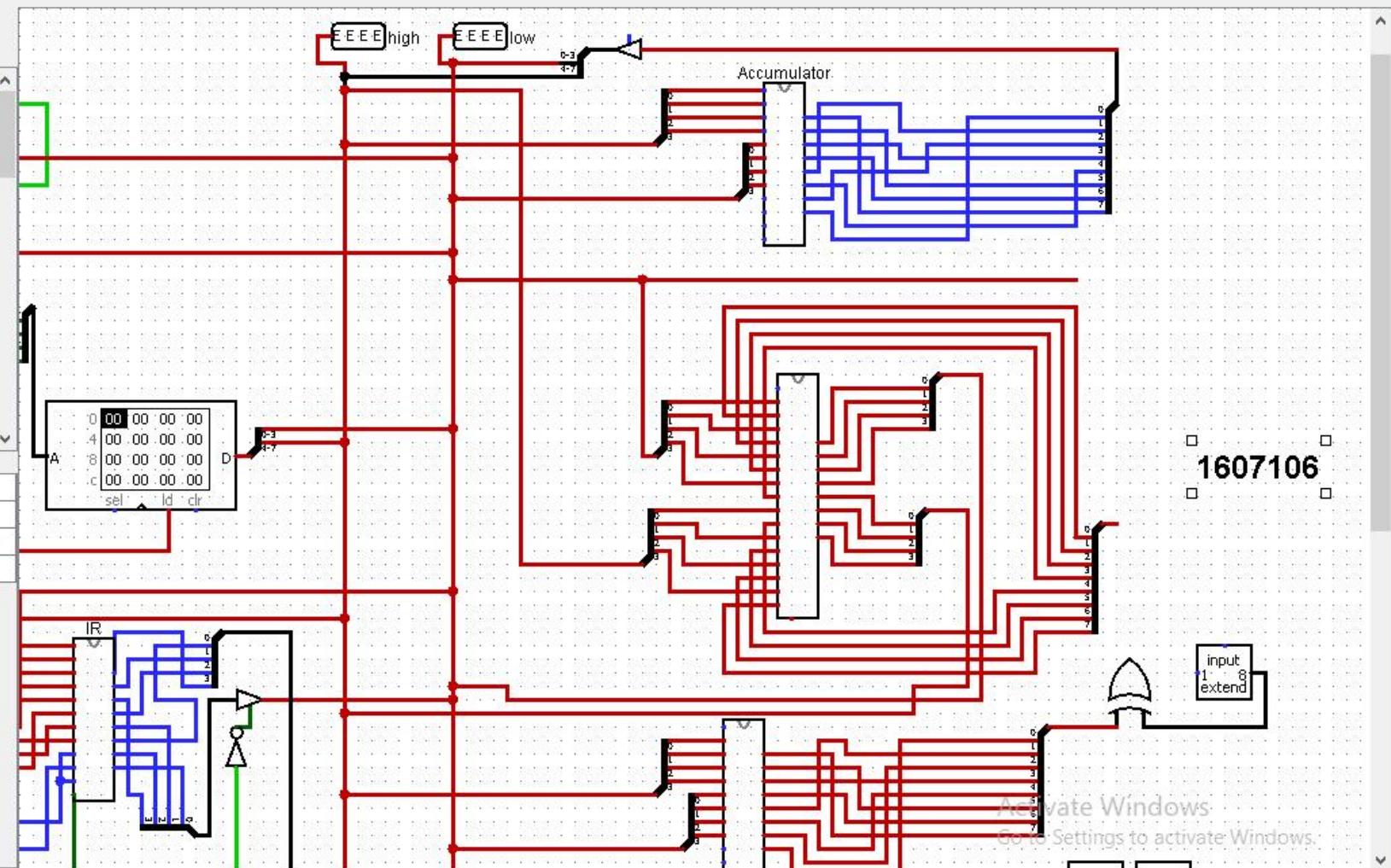


1607110_1607106 - Copy*

- main
- register
- decoder
- CONTROLLER
- memory_address_reg
- RingCounter
- TrueComplement
- FA
- ALU
- Wiring
 - Splitter
 - Pin
 - Probe
 - Tunnel
 - Pull Resistor
 - Clock
 - Constant

Selection: Label

Text	1607106
Font	SansSerif Bold 24
Horizontal Alignment	Center
Vertical Alignment	Base



100%

Logisim: main of 1607110_1607106 - Copy

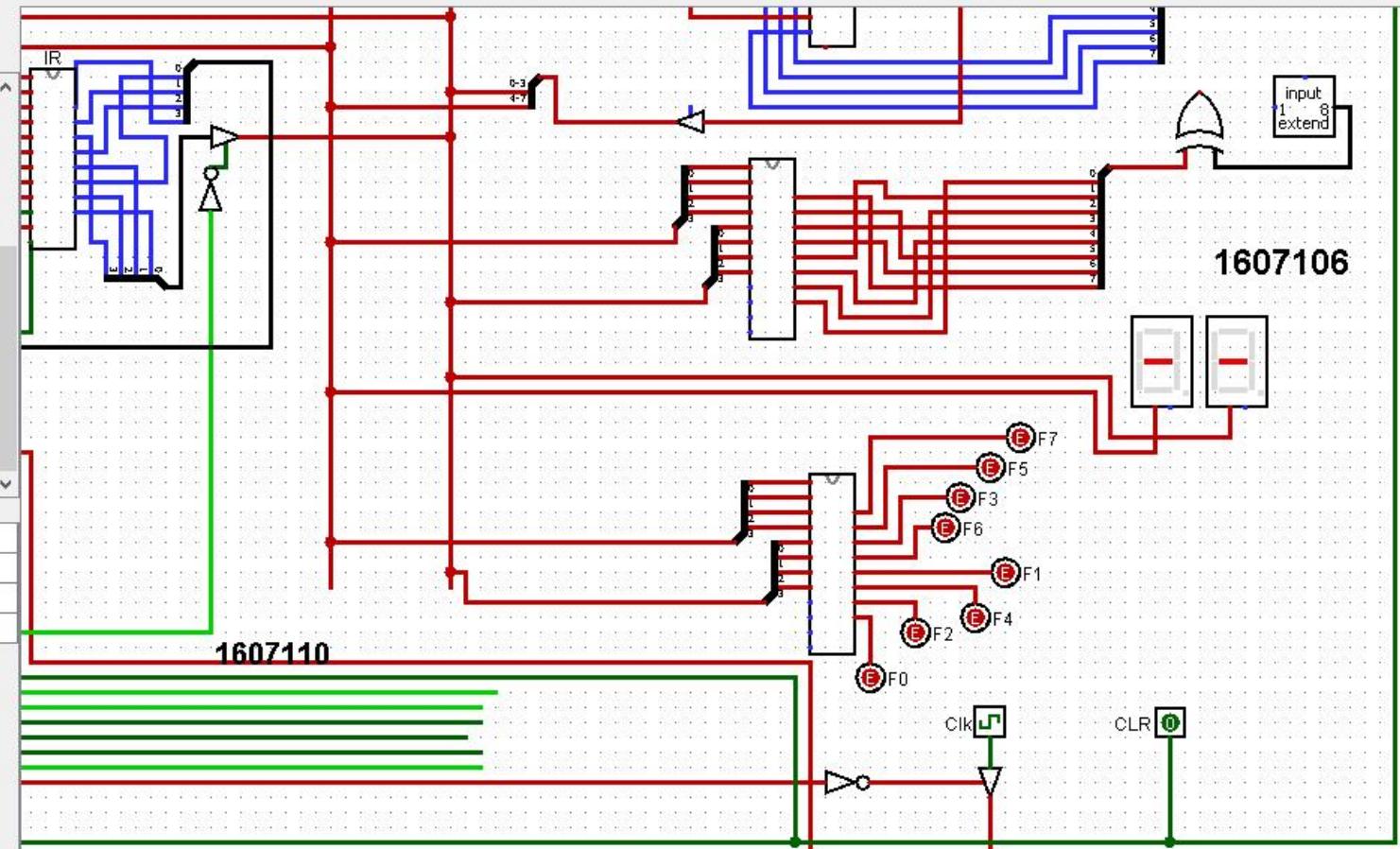
File Edit Project Simulate Window Help

A | D D

- + Gates
 - ▷ NOT Gate
 - ▷ Buffer
 - ▷ AND Gate
 - ▷ OR Gate
 - ▷ NAND Gate
 - ▷ NOR Gate
 - ▷ XOR Gate
 - ▷ XNOR Gate
 - Odd Parity
 - Even Parity
 - ▷ Controlled Buffer
 - ▷ Controlled Inverter
- + Plexers
- + Arithmetic
- + Memory
- + Input/Output
- + Base

Circuit: main

Circuit Name	main
Shared Label	
Shared Label Facing	East
Shared Label Font	SansSerif Plain 12



Activate Windows
Go to Settings to activate Windows.

100%



Search

11:01 AM
5/12/2021

Logisim: main of 1607110_1607106 - Copy

File Edit Project Simulate Window Help

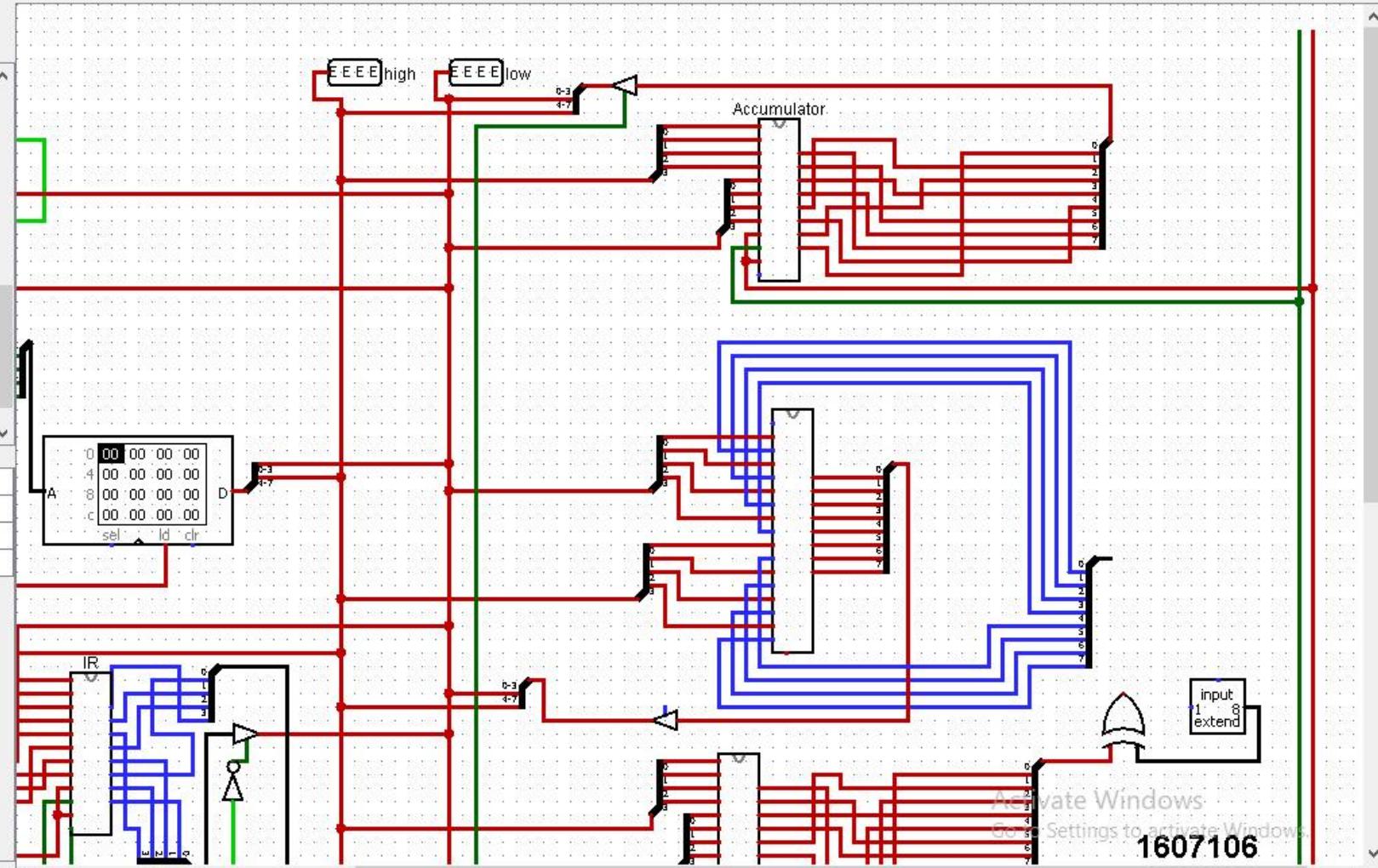
A | D



- Multiplier
- Divider
- Negator
- Comparator
- Shifter
- Bit Adder
- Bit Finder
- Memory
 - D Flip-Flop
 - T Flip-Flop
 - JK Flip-Flop
 - SR Flip-Flop
 - Register
 - Counter
 - Shift Register
 - Random Generator
- RAM
- ROM

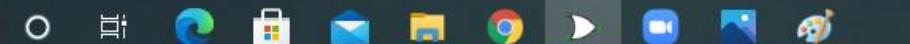
Circuit: main

Circuit Name	main
Shared Label	
Shared Label Facing	East
Shared Label Font	SansSerif Plain 12



100%

Search

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Logisim: main of 1607110_1607106 - Copy

File Edit Project Simulate Window Help

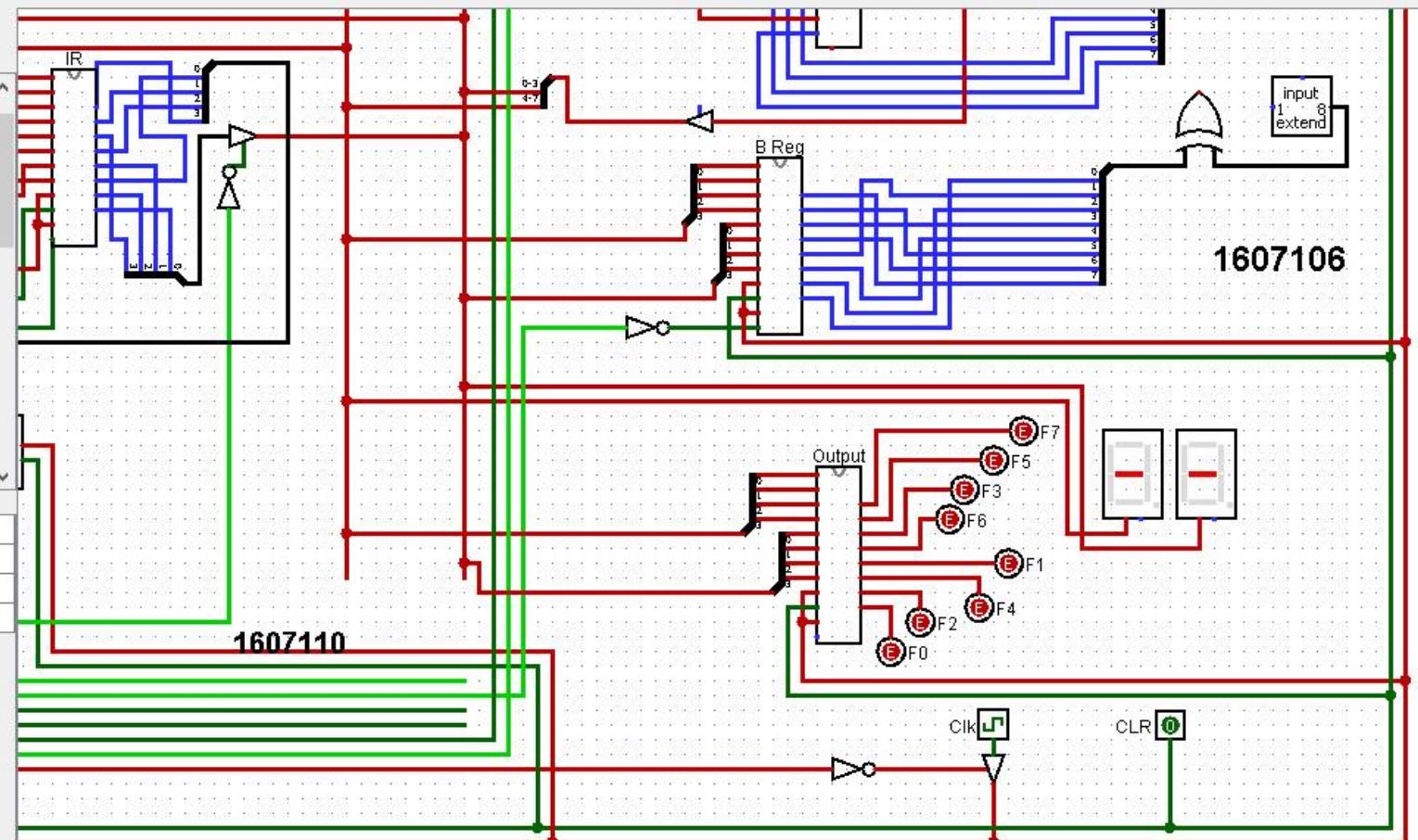
A | D D



- register
- decoder
- CONTROLLER
- memory_address_reg
- RingCounter
- TrueComplement
- FA
- ALU
- Wiring
- Gates
 - NOT Gate
 - Buffer
 - AND Gate
 - OR Gate
 - NAND Gate
 - NOR Gate
 - XOR Gate
 - XNOR Gate

Circuit: main

Circuit Name	main
Shared Label	
Shared Label Facing	East
Shared Label Font	SansSerif Plain 12



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11:38 AM

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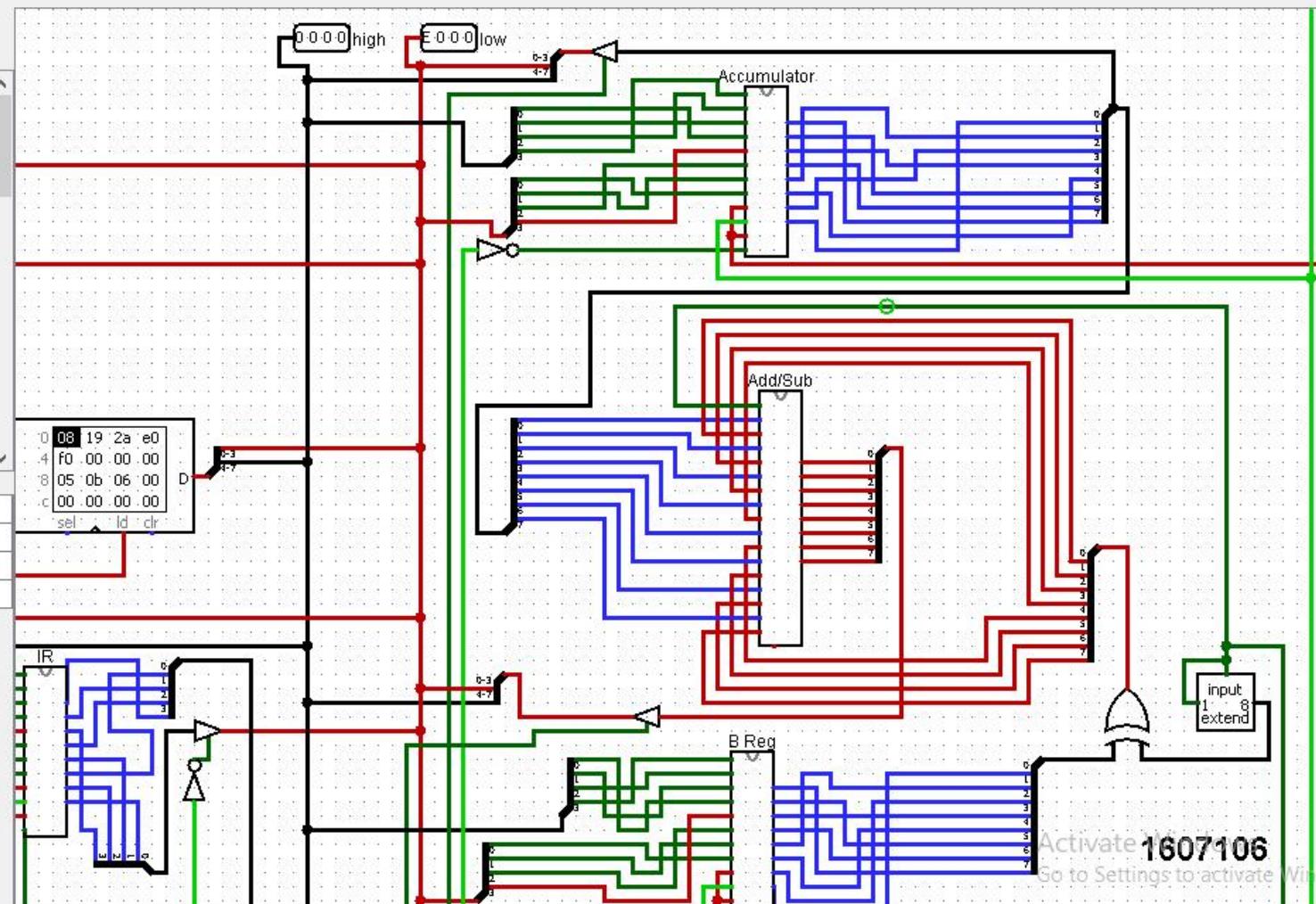


1607110_1607106 - Copy

- main
- register
- decoder
- CONTROLLER
- memory_address_reg
- RingCounter
- TrueComplement
- FA
- ALU
- Wiring
 - Splitter
 - Pin
 - Probe
 - Tunnel
 - Pull Resistor
 - Clock
 - Constant

Circuit: main

Circuit Name	main
Shared Label	
Shared Label Facing	East
Shared Label Font	SansSerif Plain 12

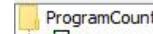
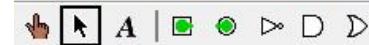


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Logisim: main of ProgramCounter

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ProgramCounter



main



Wiring



Gates



Plexers



Arithmetic



Memory



D Flip-Flop



T Flip-Flop



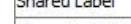
JK Flip-Flop



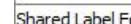
SR S-R Flip-Flop



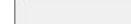
Register



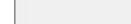
Counter



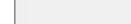
Shift Register



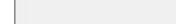
Random Generator



RAM



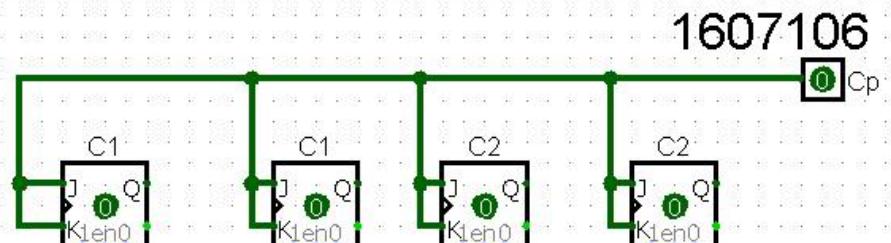
ROM



Input/Output

Circuit: main

Circuit Name	main
Shared Label	
Shared Label Facing	East
Shared Label Font	SansSerif Plain 12



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Logisim: main of ProgramCounter

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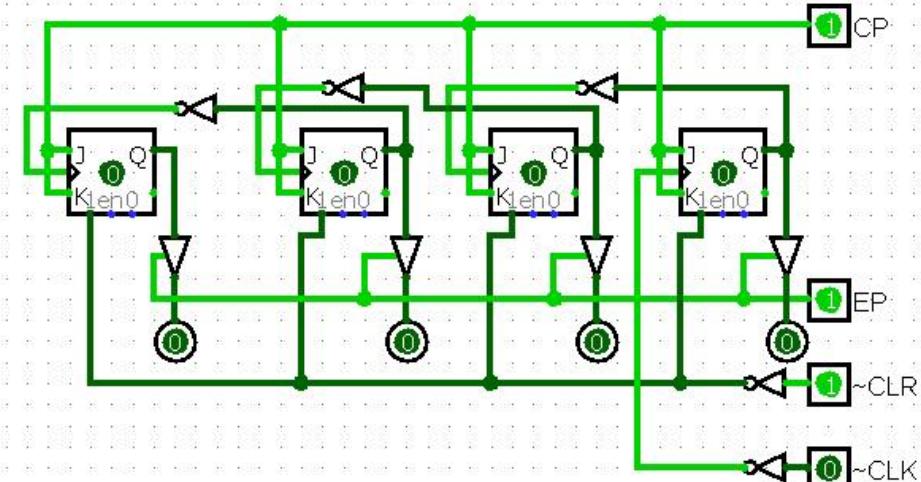
ProgramCounter

- main
- Wiring
- Splitter
- Pin
- Probe
- Tunnel
- Pull Resistor
- Clock
- Constant
- Power
- Ground
- Transistor
- Transmission Gate
- Bit Extender
- Gates
 - NOT Gate
 - Buffer

Circuit: main

Circuit Name	main
Shared Label	
Shared Label Facing	West
Shared Label Font	SansSerif Plain 12

1607106



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