Sheet1

			LOW NIBBLE															
		X0	X1	X2	X3	X4	X5	X6	X7	X8	X9	XA	XB	XC	XD	XE	XF	i
	0X	BRK s	ORA (zp,x)	CLE	SEE	TSB zp	ORA zp	ASL zp	RMB0 zp	PHP s	ORA#	ASL A	TSY	TSB a	ORA a	ASL a	BBR0 r	0.4
		7 6	5 5	2 1	2 1	4 4	3 3	4 4	4 4	3 2	2 2	1 1	1 1	5 5	4 4	5 5	4 3	0X
	1X	BPL/BNC r	ORA (zp),y	ORA (zp),z	BPL/BNC rr	TRB zp	ORA zp,x	ASL zp,x	RMB1 zp	CLC	ORA a,y	INC A	INZ	TRB a	ORA a,x	ASL/SFL a,x	BBR1 r	1X
		2 2	5 5	5 5	3 3	4 4	3 3	4 4	4 4	1 1	4 4	1 1	1 1	5 5	4 4	5 5	4 3	١٨
	2X	JSR a	AND (zp,x)	JSR (a)	JSR (a,x)	BIT zp	AND zp	ROL zp	RMB2 zp	PLP s	AND#	ROL A	TYS	BIT a	AND a	ROL a	BBR2 r	2X
		5 5	5 5	7 7	7 7	4 3	3 3	4 4	4 4	3 2	2 2	1 1	1 1	5 4	4 4	5 5	4 3	2/
	3X	BMI/BNS r	AND (zp),y	AND (zp),z	BMI/BNS rr	BIT zp,x	AND zp,x	ROL zp,x	RMB3 zp	SEC	AND a,y	DEC A	DEZ	BIT a,x	AND a,x	ROL a,x	BBR3 r	3X
		2 2	5 5	5 5	3 3	4 3	3 3	4 4	4 4	1 1	4 4	1 1	1 1	5 4	4 4	5 5	4 3	<u> </u>
	4X	RTI s	XOR (zp,x)	NEG A	ASR A	ASR zp	XOR zp	LSR zp	RMB4 zp	PHA s	XOR#	LSR A	TAZ	JMP a	XOR a	LSR a	BBR4 r	4X
		5 4	5 5	2 1	2 1	4 4	3 3	4 4	4 4	3 2	2 2	1 1	1 1	3 3	4 4	5 5	4 3	
	5X	BVC r	XOR (zp),y	XOR (zp),z	BVC rr	ASR zp,x	XOR zp,x	LSR zp,x	RMB5 zp	CLI	XOR a,y	PHY s	TAB	AUG	XOR a,x	LSR/SFR a,x	BBR5 r	5X
		2 2	5 5	5 5	3 3	4 4	3 3	4 4	4 4	1 1	4 4	3 2	1 1	4 1	4 4	5 5	4 3	
	6X	RTS s	ADC (zp,x)	RTN#	BSR rr	STZ zp	ADC zp	ROR zp	RMB6 zp	PLA s	ADC#	ROR A	TZA	JMP (a)	ADC a	ROR a	BBR6 r	6X
щ		4 3	5 5	7 4	5 5	3 3	3 3	4 4	4 4	3 2 SEI	2 2	1 1	1 1	5 5	4 4	5 5	4 3	
HIGH NIBBLE	7X 8X	BVS r	ADC (zp),y	ADC (zp),z	BVS rr	STZ zp,x	ADC zp,x	ROR zp,x	RMB7 zp		ADC a,y	PLYs	TBA	JMP (a,x)	ADC a,x	ROR a,x	BBR7 r	7X
Ë		2 2	5 5	5 5 STA (d.SP),Y	3 3	3 3	3 3	4 4	4 4	2 1 DEY	8IT#	3 2	1 1 STY a,x	5 5 STY a	4 4 STA a	5 5 STX a	4 3	
퓬		BRA r 2 2	STA (zp,x) 5 5	6 5	BRA rr 3 3	STY zp 3	STA zp 3 3	STX zp 3 3	SMB0 zp	DEY	2 2 2	1 1	4 4	4 4	31A a 4	4 4	BBS0 r 4 3	8X
主	9X	BCC r	STA (zp),y	STA (zp),z	BCC rr	STY zp,x	STA zp,x	STX zp,y	SMB1 zp	TYA	STA a,y	TXS	STX a,y	STZ a	STA a,x	STZ a,x	BBS1 r	
		2 2	5 5 5 5	5 5	3 3	3 3 3	3 3	3 3	3 NIB 1 2p	1 1 1	4 4	1 1 1	4 4	4 4	4 4	312 a,x	4 3	9X
		LDY#	LDA (zp,x)	LDX#	LDZ#	LDY zp	LDA zp	LDX zp	SMB2 zp	TAY	LDA#	TAX	LDZ a	LDYa	LDA a	LDX a	BBS2 r	
	AX BX	2 2	5 5	2 2 2	2 2	3 3	3 3	3 3	4 4	1 1 1	2 2 2	1 1 1	4 4	4 4	4 4	4 4	4 3	AX
		BCS r	LDA (zp),y	LDA (zp),z	BCS rr	LDY zp,x	LDA zp,x	LDX zp,y	SMB3 zp	CLV	LDA a,y	TSX	LDZ a,x	LDY a,x	LDA a.x	LDX a,y	BBS3 r	
		2 2	5 5	5 5	3 3	3 3	3 3	3 3	4 4	1 1 1	4 4	1 1 1	4 4	4 4	4 4	4 4	4 3	BX
	сх	CPY#	CMP (zp,x)	CPZ#	DEW zp	CPY zp	CMP zp	DEC zp	SMB4 zp	INY	CMP#	DEX	ASW a	CPY a	CMP a	DEC a	BBS4 r	
		2 2	5 5	2 2	6 6	3 3	3 3	4 4	4 4	1 1	2 2	1 1	7 7	4 4	4 4	5 5	4 3	CX
	DX	BNE/BZC r	CMP (zp),y	CMP (zp),z	BNE/BZC rr	CPZ zp	CMP zp,x	DEC zp,x	SMB5 zp	CLD	CMP a,y	PHX s	PHZ s	CPZ a	CMP a,x	DEC a,x	BBS5 r	5.
		2 2	5 5	5 5	3 3	3 3	3 3	4 4	4 4	1 1	4 4	3 2	3 2	4 4	4 4	5 5	4 3	DX
	EX	CPX#	SBC (zp,x)	LDA (d.SP),Y	INW zp	CPX zp	SBC zp	INC zp	SMB6 zp	INX	SBC#	NOP	ROW a	CPX a	SBC a	INC a	BBS6 r	EX
		2 2	5 5	6 5	6 6	3 3	3 3	4 4	4 4	1 1	2 2	1 1	7 7	4 4	4 4	5 5	4 3	ΕX
	FX	BEQ/BZS r	SBC (zp),y	SBC (zp),z	BEQ/BZS rr	PHW ##/s	SBC zp,x	INC zp,x	SMB7 zp	SED	SBC a,y	PLX s	PLZ s	PHW a/s	SBC a,x	INC a,x	BBS7 r	FX
		2 2	5 5	5 5	3 3	5 4	3 3	4 4	4 4	1 1	4 4	3 2	3 2	7 7	4 4	5 5	4 3	FX
		X0	X1	X2	X3	X4	X5	X6	X7	X8	X9	XA	XB	XC	XD	XE	XF	
CE02	CE02V	45 42	80 80	74 67	57 55	56 53	48 48	60 60	64 64	25 20	48 48	24 20	51 44	72 67	64 64	72 76	64 48	

OPCode						
Cycles on	Cycles on					
Original	V-Version					

Cyan Numbers indicate potential additional cycles, like for branches

Darker Gray = 6502

Gray = added by 65C02

Lighter Gray = added by 65CE02

Average CPI	Average MIPS (1MHz)					
WIP! 65CE02: 3.531	65CE02: 0.283					
WIP!65CE02V: 3.344	65CE02V: 0.299					