DECOMPOSING A MULTI-CONTROLLED TOFFOLI GATE

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The problem

We want to decompose a MCX gate with 14 control qubits and a maximum of 5 ancilla qubits using a minimal number of single and double qubits CX gates.

Solution

Our approach will be to write the MCX gates with 14 control qubits in terms of other MCX gates with fewer control qubits. Which then using the function transpile of qiskit can be decomposed into single and double qubit gates. This can be done in several ways, however, in order to achieve the minimum circuit depth using 5 ancilla qubits in our circuit, the optimal solution is to use ten 3-qubit gates and one 4-qubit gates as shown in figure 1. After writing the circuit using MCX gates with 3 and 4 controls, using the function transpile, these two kinds of gates are efficiently decomposed into a sequence of one and two-qubit gates. These decomposition result in a circuit of depth 130 with 198 U gates and 176 CX gates.

The 5 ancilla qubits are prepared in the state $|0\rangle$.

The code

Imports the necessary qiskit functions.

```
from qiskit import QuantumRegister, ClassicalRegister,
QuantumCircuit, transpile
```

We set up the circuit with 14 control qubits and 5 ancilla qubits, and one target qubit.

```
qr = QuantumRegister(14, 'c') # 14 control qubits.
target_qubit = QuantumRegister(1,'t') # target qubit.
anc = QuantumRegister(5, 'ancilla') # ancilla qubits.
qc = QuantumCircuit(qr,target_qubit, anc)
```

Then we create ten 3-qubit gates and one 4-qubit gate.

```
# 3-qubit gates.
q qc.mcx(qr[0:3],anc[0])
3 qc.mcx(qr[3:6],anc[1])
4 qc.mcx(qr[6:9],anc[2])
5 qc.mcx(qr[9:12],anc[3])
6 qc.mcx([qr[12],qr[13],anc[0]],anc[4])
8 # 4-qubit gate
9 qc.mcx(anc[1:5],target_qubit[0])
10
11
_{12} # Apply the 3-qubit gates in reverse to return controls and
      ancillas to original state.
13 qc.mcx([qr[12],qr[13],anc[0]],anc[4])
14 qc.mcx(qr[9:12],anc[3])
15 qc.mcx(qr[6:9],anc[2])
16 qc.mcx(qr[3:6],anc[1])
17 qc.mcx(qr[0:3],anc[0])
qc.draw('mpl')
```

Using the function transpile of qiskit, we then decompose the above circuit into single and double qubit gates. We have chosen the basis set as {U,CX}, as it gives the minimum circuit depth.

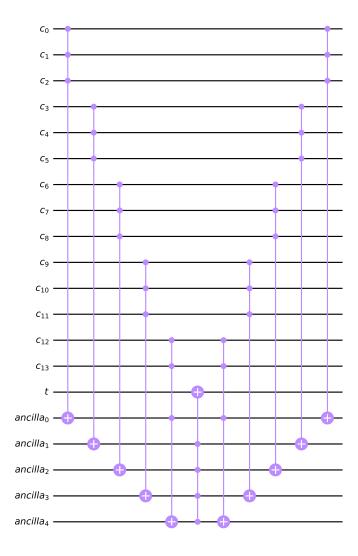


Figure 1: The MCX gate with 14 control qubits and 5 auxiliary qubits is split into ten 3-qubit gates and one 4-qubit gates.