**DDS frequency Change:**

platform\_xilinx/dac\_core.c

dds\_default\_setup(phy, DDS\_CHAN\_TX1\_I\_F1, 90000, 1000000, 250000);

=> dds\_default\_setup(phy, DDS\_CHAN\_TX1\_I\_F1, 90000, 25000, 250000);

**TX Attenuation Change:**

main.c

in default\_init\_param:

10000, //tx\_attenuation\_mdB \*\*\* adi,tx-attenuation-mdB

=> 0, //tx\_attenuation\_mdB \*\*\* adi,tx-attenuation-mdB

**For AD9364 Compatibility:**

main.c

in default\_init\_param:

1, //two\_rx\_two\_tx\_mode\_enable \*\*\* adi,2rx-2tx-mode-enable

=> 0, //two\_rx\_two\_tx\_mode\_enable \*\*\* adi,2rx-2tx-mode-enable