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Q. 13

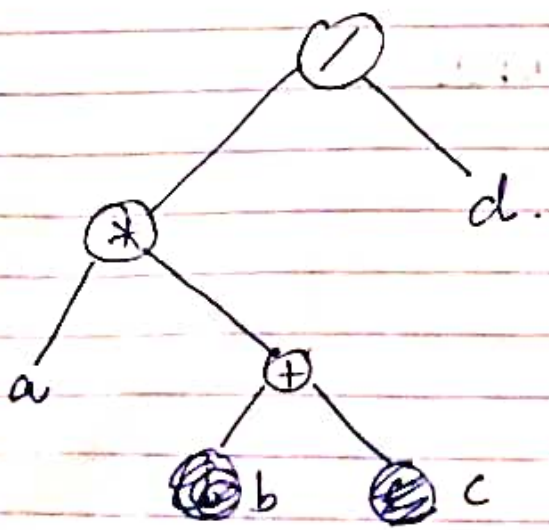
INTERMEDIATE CODE GENERATOR

- 1. readily executed by the machine.
- i) ~~Syntax tree / Abstract syntax tree~~
- ii) ~~3 address code~~
- Form of I.C.
- i) Syntax tree / Abstract syntax tree
- ii) ~~3 address~~ Prefix notation
- iii) 3 address code.

Syntax tree:

- operator - ^{parent} node / internal node.
- operand - leaf node.

8. $a * (b + c) / d.$



- i) precedence.
- ii) associativity.

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Postfix notation

- operator after operand.

Q. $(a+b) * c$

$ab + c *$

Q. $a + (b * c)$

~~$bc + a +$~~

~~$bc + a +$~~

$abc * +$

Q. $(a-b) * (c/d)$

$ab - cd / *$

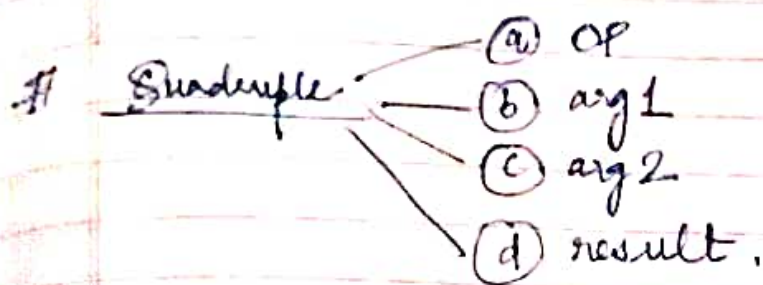
3 address code:

- at most 3 addresses.
- only 1 operator on RHS.

→ Represented in 3 ways:

- i) Quadruple
- ii) Triple
- iii) Indirect triple

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$$a = b * -c + b * -c$$

$$t1 = -c$$

$$t2 = b * t1$$

$$t3 = b * -c$$

$$t4 = b * t3$$

$$t5 = t2 + t4$$

~~accc~~

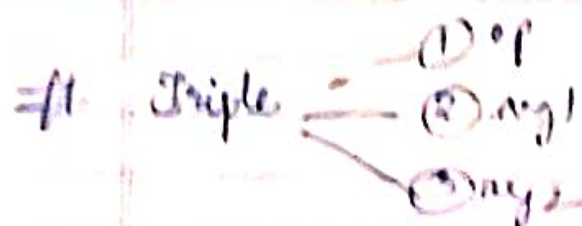
(1) Quadruple.

Location	OP	arg 1	arg 2	result
(0)	-	c		t1
(1)	*	b	t1(0)	t2
(2)	-	c		t3
(3)	*	b	t3(2)	t4
(4)	+	t2(1)	t4(3)	t5

Limitation.

- Stored in symbol table. (lots of memory).

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③ Triple

Location	op	arg1	arg2
(0)	-	c	
(1)	b+	b	(0)
(2)	-	c	
(3)	x	b	(2)
(4)	+	(1)	(3)

≠1 Indirect table triple

extra pointers tables with contains pointers to the triple.

③ Indirect triple

pointer	triple	Addr	Tris
1000	(0)	1001	(0)
1002	(1)	1002	(1)
		/	(2)
			(3)
			(4)

09-24.

B. $a + b \times c / f + b \times a.$

$t1 = c \uparrow f$
 $t2 = b \times c$
 $t3 = t2 / t1$
 $t4 = b \times a$
 $t5 = a + t3$
 $t6 = t5 + t4$

i) Precedence
 ii) associativity
Proof.
 $a + b \times c / t1 + b \times a$
 $a + t2 / t1 + b \times a$
 $a + t3 + b \times a$
 $a + t3 + t4$
 $t5 + t4$
 $t6$

Quadruple.

Location	op	arg1	arg2	result
(0)	\uparrow	e	f	t1
(1)	\times	b	c	t2
(2)	/	t2	t1	t3
(3)	\times	b	a	t4
(4)	+	a	t3	t5
(5)	+	t5	t4	t6

Triple:

Location	op	arg1	arg2
(0)	\uparrow	e	f
(1)	\times	b	c
(2)	/	(1)	(0) (0)
(3)	\times	b	a
(4)	+	a	(2)
(5)	+	(4)	(3)

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Indirect triple.

pointer	tuple	
1000	(0)	
1002	(1)	- 0000
1004	(2)	- 0010
1006	(3)	- 0100
1008	(4)	- 0110
1010	(5)	- 1000
		<u>1010</u>

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- Study from slides / Naresh will share.
- different forms of 3 address codes.

Exceptions of 3 address codes:-

Exception 01

$x = op\ y$ \rightarrow arg1 field.
 \downarrow \downarrow
 result field operator field

arg2 = 3

arg2 field remains unused.

Exception 02.

param #1 \rightarrow arg1 field.
 \downarrow
 operator field.

eg: call p, n \rightarrow procedure
 number
 position

arg2 & result field is not used.

sum(a,b)

call sum, 2.

09.11.

Exception 03.

To represent unconditional & conditional jump statements, we place label of the target in the result field.

Characteristics of 3 address code:

- i) max^m of 3 addresses to represent any statement
- ii) implemented as a record with the address field.

— general form:

$$a = b \text{ op } c$$

 (constants, names, compiler generated temporaries).

3 address Instruction Forms:

i) Assignment Statement

$x = y \text{ op } z$
 $x = \text{op } y$

- assigns result obtained after solving RHS expr to left side operand.
- at least 1 op. on RHS.

ii) Copy Statement

$x = y$

Copies/assigns value of operand y to x.

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classmate

iii) Conditional jump.

x rel op y goto x

operands. relational operator. tag / label of target statement

- If a rel op y. gets it satisfied -
 - control is sent directly to location specified by label x.
 - statements in between is skipped.
- If a rel op y. fails -
 - control not transferred.
 - usual flow of execution

iv) Unconditional jump.

- X - tag / label label of target st.
- ~~can~~ on executing, control is directly sent to loc. specified by X.
- all st. b/w are skipped.

v) Procedure call.

param x call p return y.

x as a parameter function

★ - Learn how to change while / if else to 3 address code (Imp. End term)

8.09.24.

Basic Blocks & Flow graph.

- Basic Blocks - is a set of statements that always executes in a sequence one after the other.
- 1 entry-point - 1 exit-point
- it should not contain any conditional/unconditional control statement in ^{any} middle of the block.

Eg. $a = b + c + d.$ // is a basic block.

$$t1 = b + c$$

$$t2 = t1 + d.$$

$$a = t2.$$

Eg. if $A < B$ then 1 else 0 // Not a basic block

(1) if $A < B$ goto (4)

(2) $T1 = 0$

(3) goto (5)

(4) $T1 = 1$

(5)

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Partitioning intermediate code into basic blocks.

Rule 01: Determining leader.

- 1st st. of code
- st. to which is target of cond./uncond. goto st.
- st. immediately after a goto st.

Rule 02: Determining Basic block.

- All st. that follow a leader till next leader is basic block.
- 1st block after 1st leader is initial block.

Q.

1. $PROD = 0$
2. $I = 1$
3. $T2 = \text{addr}(A) - 4$
4. $T4 = \text{addr}(B) - 4$
5. $T1 = 4 * i$
6. $T3 = T2[T1]$
7. $PROD = PROD + T3$
8. $I = I + 1$
9. If $I \leq 20$ goto (5).
10. $j = j + 1$
11. $K = K + 1$
12. If $j \leq 5$ goto (7)
13. $i = i + j$

09/04/

① Determining leader

1
5
7
10
13

② Determining basic block

B1.
PROD = 0
I = 1
T2 = addr(A) - 4
T4 = addr(C) - 4

B2.
T1 = 4 * I
T3 = T2[T1]

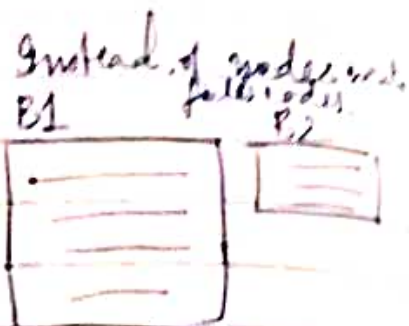
B3.
PROD = PROD + T3
I = I + 1
If I <= 20 goto B2

B4.
j = j + 1
k = k + 1
If j <= 5 goto B3

B5.
i = i + j

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directed graph (Basic Block + flow info).



8.

- 1 $a = 1$
- 2 $c = 1$
- 3 $t1 = 10491$
- 4 $t2 = t1 + c$
- 5 $t3 = 8 \times t2$
- 6 $t4 = t3 - 88$
- 7 $a[t4] = a0$
- 8 $c = c + 1$
- 9 if $c \leq 10$ goto (3)
- 10 $n = n + 1$
- 11 if $n \leq 10$ goto (2)
- 12 $n = 1$
- 13 $t5 = c - 1$

14 $16 = 88 + 15$
 15 $a[16] = 1.0$
 16 $n = n + 1$
 17 if $n \leq 10$ goto (13)

14. ① Labels.

1	1
3	2
10	3
2	10
12	12
13	13.

14. ② B.B.

B4

$n = 1$ B1

$n = n + 1$
 if $n \leq 10$ goto B2

$c = 1$ B2

$n = 1$ B5

B3.

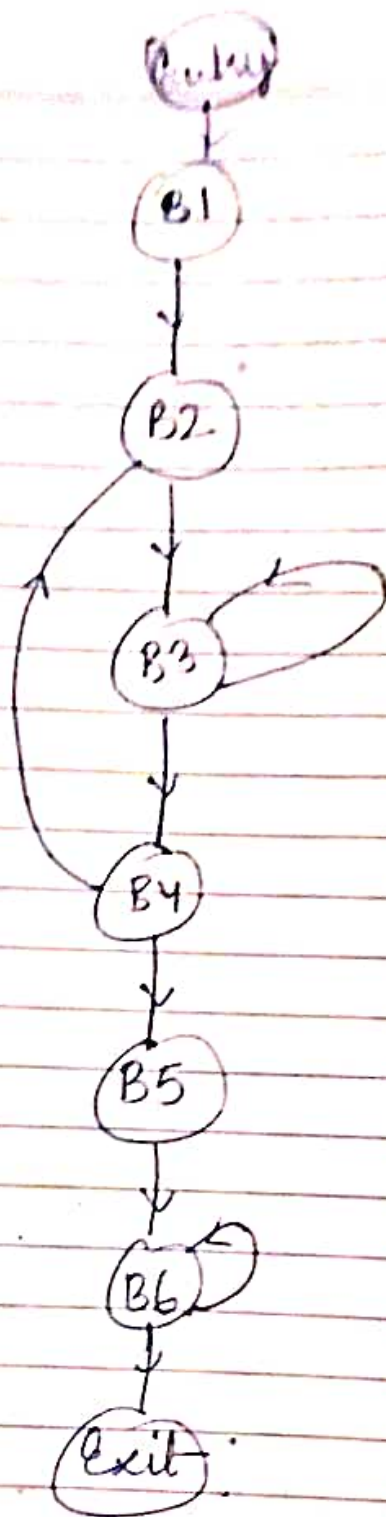
$t1 = 10 + n$
 $t2 = t1 + c$
 $t3 = 3 * t2$
 $t4 = 13 - 88$
 $a[t4] = 0.0$
 $c = c + 1$
 if $c \leq 10$ goto B3

$t5 = c - 1$
 $t6 = 88 * t5$
 $a[t6] = 1.0$
 $n = n + 1$
 if $n \leq 10$ goto B6

B6

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DAG representation.

- Directed acyclic graph is a special kind of Abstract Syntax Tree.
- Each node of it contains a unique value.

- for optimising basic block
- used in 3 Address code
- dead code / sub-expr elimination

Construction of DAG.

Rule 01: Interior node always represent the operators (also store result of expr.)
Exterior node always represent identifiers/constants.

Rule 02:

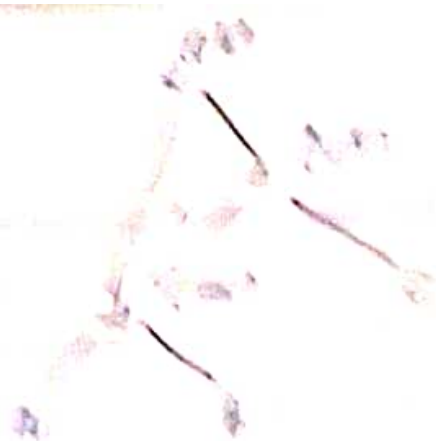
Rule 03:

$x = y$ are not performed unless necessary.

8. $t1 = a + b$

$t2 = t1 + c$

$t3 = t1 * t2$



$a = b \vee c$
 $b = a \wedge d$
 $c = a \wedge d$
 $d = b \vee c$



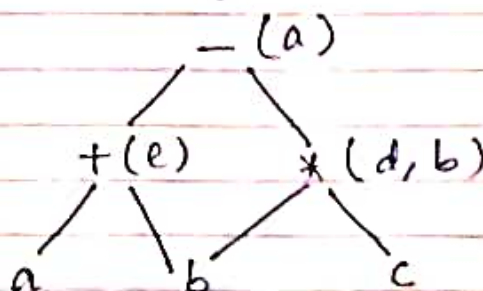
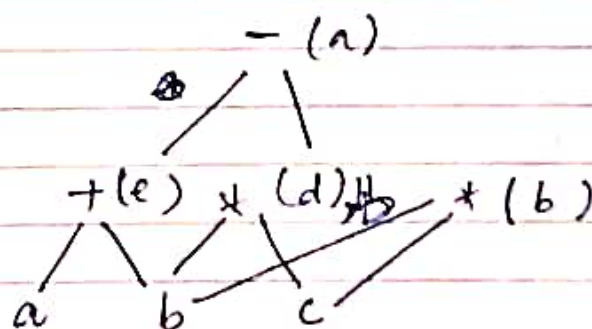
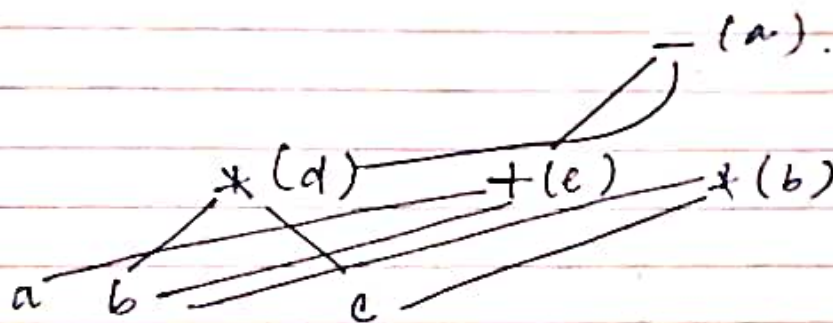
$$d = b * c$$

$$e = \overline{a + b}$$

$$b = \overline{b * c}$$

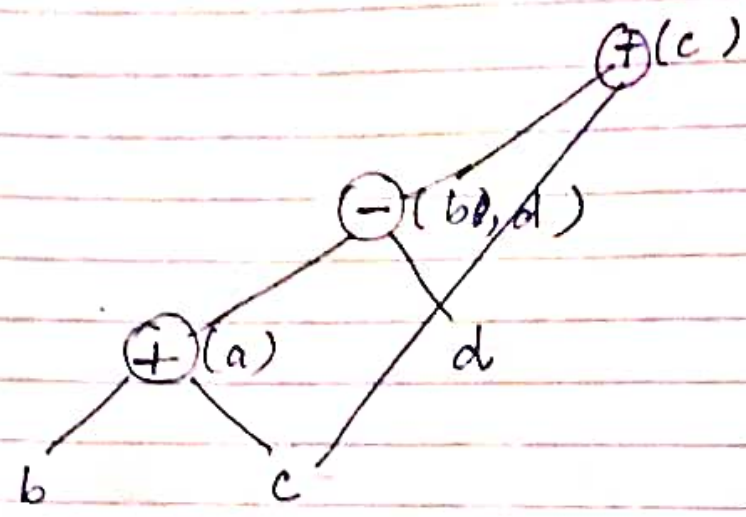
$$a = e - d$$

$$b * c$$

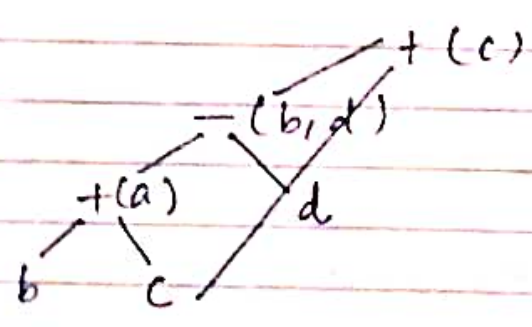


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8. $a = b + c$
 $b = a - d$
 $c = b + c$
 $d = a - d$



↓



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$$8 \quad ((\underbrace{(a+a)}_{t1} + \underbrace{(a+a)}_{t2})) + ((\underbrace{(a+a)}_{t3} + \underbrace{(a+a)}_{t4}))$$

$$t1 = a + a$$

$$t2 = a + a$$

$$t3 = a + a$$

$$t4 = a + a$$

$$t5 = t1 + t2$$

$$t6 = t3 + t4$$

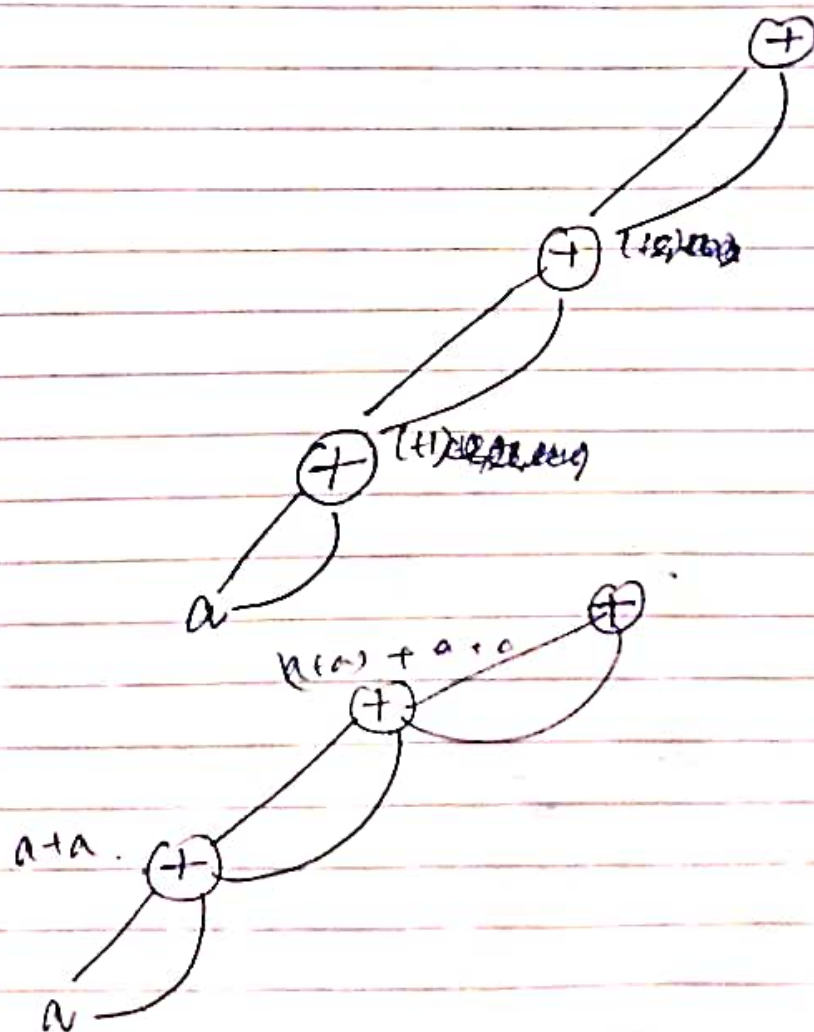
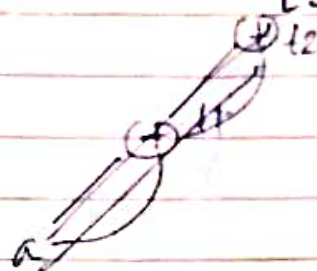
$$t7 = t5 + t6$$

optimised

$$t1 = a + a$$

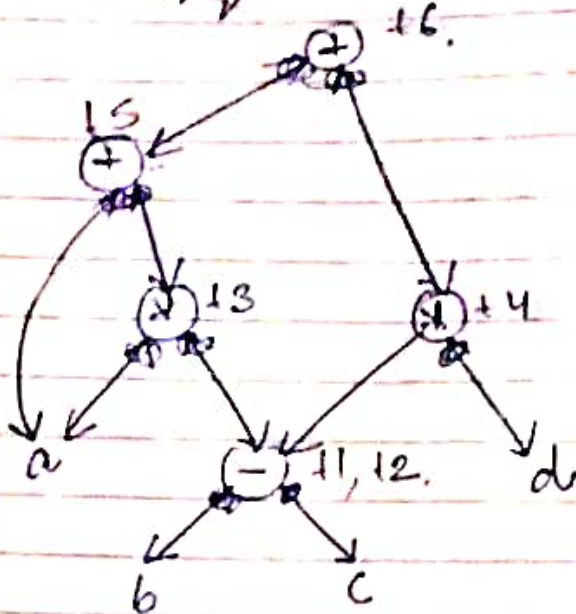
$$t2 = t1 + t1$$

$$t3 = t2 + t2$$



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8 $a + a * (b - c) + (b - c) * d$



~~t1 = b~~

$t1 = b - c$

$t2 = b - c$

$t3 = a * t1$

$t4 = t2 * d$

$t5 = a + t3$

$t6 = t5 + t4$

↓ optimized

$t1 = b - c$

$t2 = a * t1$

$t3 = t1 * d$

$t4 = a + t2$

$t5 = t4 + t3$

