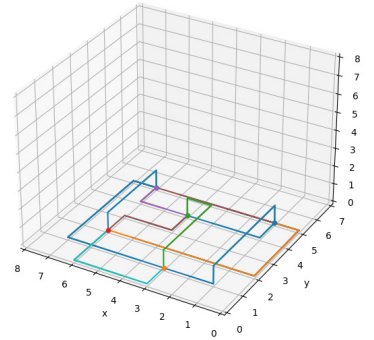
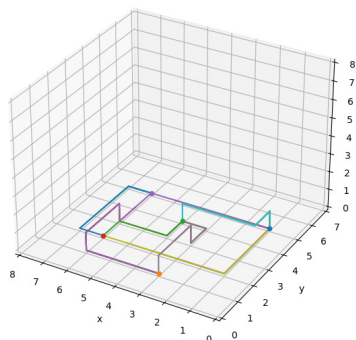
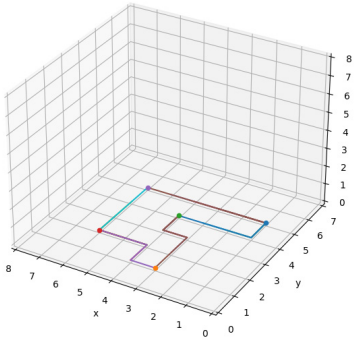
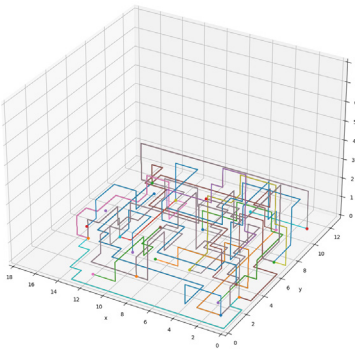


A*

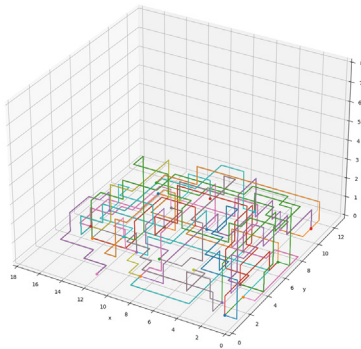
Chip_0



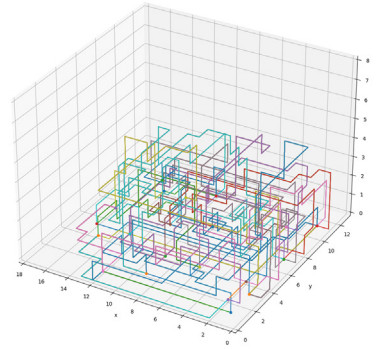
Netlist_1: 20
(0 int)
Chip_1



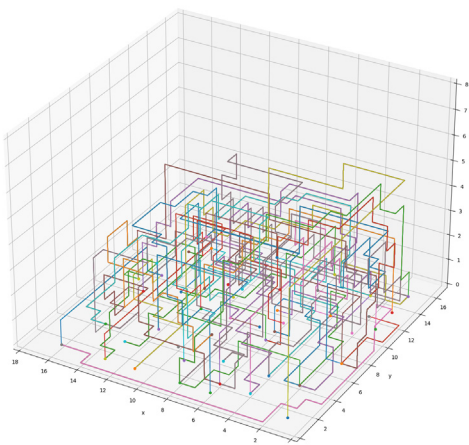
Netlist_2: 343
(1 int)



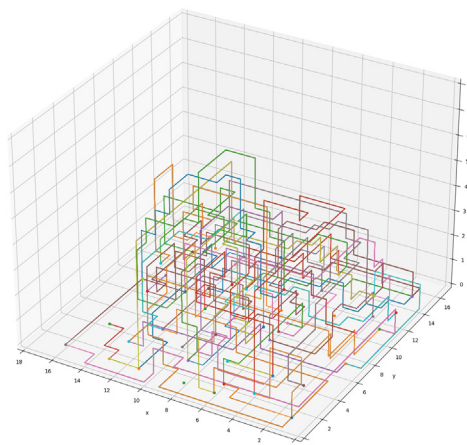
Netlist_3: 360
(1 int)



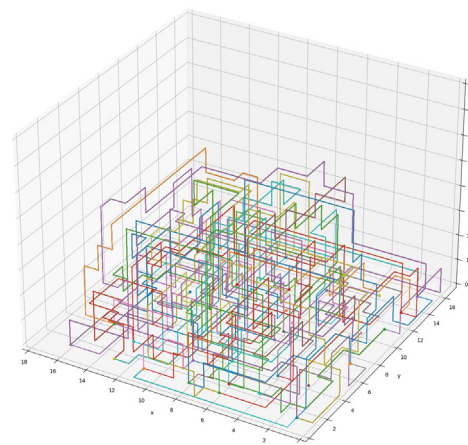
Netlist_4: 4025
(12 int)
Chip_2



Netlist_5: 9793
(31 int)



Netlist_6: 16671
(53 int)



Netlist_7: 16570
(52 int)

Netlist_8: 116733
(53 int)

Netlist_9: 37815
(122 int)