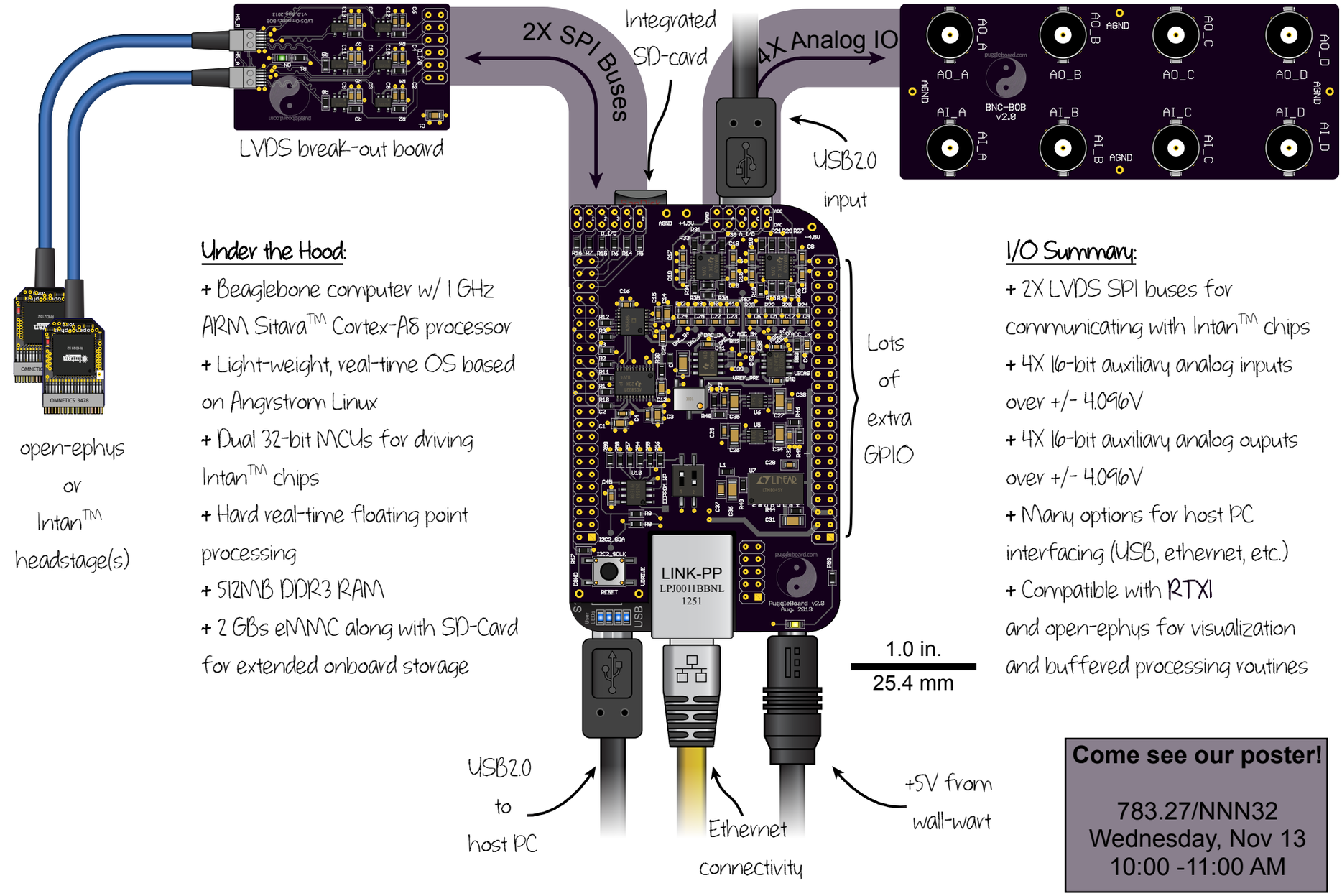
puggle

+ ARM-based, real-time data acquisition and processing tool.
+ Designed to sense, process, and react to input signals in hard real-time.
+ Low cost

$150*

*Approximate materials cost. Does not include Intan™ chips or headstage PCBs

acquisition to output in ~50 µs



open-ephys or Intan™ headstage(s)

Under the Hood:

+ Beaglebone computer w/ 1 GHz ARM Sitara™ Cortex-A8 processor
+ Light-weight, real-time OS based on Angrstrom Linux
+ Dual 32-bit MCUs for driving Intan™ chips
+ Hard real-time floating point processing
+ 512MB DDR3 RAM
+ 2 GBs eMMC along with SD-Card for extended onboard storage

I/O Summary:

+ 2X LVDS SPI buses for communicating with Intan™ chips
+ 4X 16-bit auxiliary analog inputs over +/- 4.096V
+ 4X 16-bit auxiliary analog ouputs over +/- 4.096V
+ Many options for host PC interfacing (USB, ethernet, etc.)
+ Compatible with RTXI and open-ephys for visualization and buffered processing routines

Come see our poster!

783.27/NNN32
Wednesday, Nov 13
10:00 -11:00 AM

RTXI open ephys Georgia Tech