

國立交通大學 97 學年度碩士班考試入學試題

資訊科學與工程, 網路工程, 生醫影像工程

科目: 作業系統(1005)

考試日期: 97 年 3 月 9 日 第 3 節

系所班別: 資訊系所跨組聯招

組別: 資訊聯招

第 1 頁, 共 5 頁

*作答前請先核對試題、答案卷(試卷)與准考證之所組別與考科是否相符!!

共八大題, 請按題號依序作答, 未按題號作答者不予給分。

一、單一選擇題, 共四大題, 每一大題三小題, 每一小題答對給五分, 答錯倒扣兩分

1. Processes, Threads, and CPU Scheduling

1.1 Which of the descriptions about the following C program with fork() is correct?

```

#include <stdio.h>
#include <unistd.h>

void func(int *input)
{
    (*input)++;
}

int main()
{
    pid_t pid;
    int k;
    k = 3;
    pid = fork();
    if(pid < 0) {
        func(&k);
        printf("k=%d ", k);
        exit(-1);
    } else if(pid == 0) {
        func(&k);
        printf("k=%d ", k);
    } else {
        wait(NULL);
        func(&k);
        printf("k=%d ", k);
        exit(0);
    }
}

```

- (a) The output "k" of the parent process is 4.
- (b) The output "k" of the child process is 3.
- (c) The last output of "k" is 5.
- (d) None of the above

1.2 Suppose a non-preemptive scheduler uses variable priorities and time quanta as the following table, where the highest priority is 5.

priority	time quantum	new priority after time quantum is expired	new priority after returning from I/O
5	40	4	5
4	60	3	5
3	100	2	4

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2	200	1	4
1	320	1	3

Which of the following statements is incorrect?

- (a) Response time of interactive threads will increase.
- (b) Priorities of CPU-intensive threads will be lowered.
- (c) Time-quantum of I/O-bound threads will decrease.
- (d) None of the above.

1.3 Consider the descriptions: (α) having separate program counters (β) having shared data (γ) having separate register sets (δ) scheduling by a thread library (ϵ) parallel running on a multi-processor system. Which of the following statements is incorrect?

- (a) (α)(β)(ϵ) can be characteristics of kernel threads of a multi-threaded process.
- (b) (α)(β)(δ) can be characteristics of user threads of a multi-threaded process.
- (c) (α)(β)(γ) can be characteristics of cooperating processes.
- (d) None of the above

2 Process Synchronization and Deadlocks

2.1 We have three processes and four resource classes, which are tape drives, plotters, scanners, and CD-ROM drives. There are 3 tape drives, 5 scanners, 2 plotters, and 2 CD-ROM drives in the system. Process 1 has one scanner. Process 2 has two tape drives and a CD-ROM drive. Process 3 has a plotter and two scanners. Process 1 needs additional 2 tape drives and one CD-ROM drive. Process 2 needs additional 1 tape drive and 1 scanner. Process 3 needs additional 2 tape drives and 1 plotter. Which of the following statements is correct?

- (a) The system will not be in the safe state for the above needs.
- (b) If process 2 request a CD-ROM, two tape drives, and one plotter, the system will be in a safe state.
- (c) If process 3 request a plotter, and 2 scanners, the system will be in a safe state
- (d) None of the above

2.2 Which of the following statements is correct?

- (a) If a system is not in the safe state, it will be in a deadlock state.
- (b) If there is a cycle of resource allocation graph for a system with single instance resource, it will be in a deadlock state.
- (c) If there is a cycle of resource allocation graph for a system with resource of multiple instances, it will be in a deadlock state.

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(d) None of the above

2.3 Which of the following statements is correct?

- (a) If the conditions of mutual exclusion, hold and wait, no preemption, and circular wait, hold simultaneously in a system, a deadlock situation will arise.
- (b) The conditional variable with signal operation can be executed even no process is suspended without influencing the system state.
- (c) A preemptive kernel is free from race conditions on kernel data structures.
- (d) None of the above

3 Memory Management

3.1 A system enables the MMU (memory-management unit) and supports logical addressing and virtual memory management. Which of the following statements is NOT correct?

- (a) To add more physical memory may increase the logical address space of a program.
- (b) To add more physical memory may increase the number of entries in page tables if the inverted page table scheme is employed.
- (c) A system with more swapping spaces in the backing store can execute more programs on the system simultaneously.
- (d) A system with more swapping spaces can grant more memory requests generated by programs.

3.2 Please compare the speed of a memory access under below conditions. (a) A system enables the MMU, the TLB (translation look-aside buffer) and paging. A program accesses a memory address with a TLB miss and a page miss. (b) A system without the TLB enables the MMU and paging. A program accesses a memory address with a page hit. (c) A system without the TLB enables the MMU and paging. A program accesses a memory address with a page miss. (d) A system enables the MMU, the TLB and paging. A program accesses a memory address with a TLB hit. (e) A system enables the MMU, the TLB and paging. A program accesses a memory address with a TLB miss but a page hit. (f) A system does not enable the MMU and paging. A program accesses a memory address. Which of the following statements is correct?

- (a) (c)>(a)
- (b) (b)>(e)
- (c) (d)>(f)
- (d) (d)>(b)

3.3 Which of the following statements is NOT correct?

- (a) If the TLB does not support separate ASIDs (address-space identifiers), the TLB must be flushed in each context switch.
- (b) The TLB entries for kernel codes are usually wired down, meaning that they cannot be removed from the TLB.
- (c) To increase the size of the TLB can always improve the performance of paged-based memory accesses.
- (d) A processor with the TLB can always improve the performance of paged-based memory accesses comparing with a processor without the TLB.

4 File Systems and I/O

4.1 Consider an inode-like disk-space allocation scheme. An inode is of 10 direct pointers, 1 indirect pointer, 1 double indirect pointer, and 1 triple indirect pointer. One pointer references one disk block, which is 512 bytes. Let one pointer be 4 bytes. Which one of the following is the smallest file size that is too large to be managed by this allocation scheme?

- (a) 2^8 bytes (b) 2^{16} bytes (c) 2^{32} bytes (d) none of the above

4.2 Consider a RAID-5 system which is of 5 disks. Suppose that one disk is out of order. Up to how many disks can be involved by a read of one disk block?

- (a) 2 (b) 4 (c) 5 (d) none of the above

4.3 Which one of the following technique *cannot* save precious CPU cycles for processes?

- (a) interrupts (b) DMA (c) polling (d) all of the above

二、問答題，共四大題，每題十分

5. Four processes, with the following sequences of CPU and I/O bursts, will be performed on a single-processor system with multiple and parallel I/O supports.

Process	Arrival Time	CPU and I/O Burst sequence (time unit)
P1	0	5(CPU),6(I/O),4(CPU)
P2	3	15(CPU)
P3	8	3(CPU),10(I/O),9(CPU)
P4	14	8(CPU)

Suppose the round-robin scheduling algorithm (time quanta=5) is applied.

- (a) How many times of context switches occur?
- (b) Please calculate the turnaround time of P1.
- (c) Please calculate the turnaround time of P2.
- (d) Please calculate the waiting time of P3.

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(e) Please calculate the waiting time of P4.

6. Consider a system with five processes P_0 through P_4 and three resource types A, B, C. Resource type A has 11 instances, resource type B has 5 instances, and resource type C has 7 instances. Suppose at time T_0 , the following snapshot of the system has been taken,

	Allocation	Max
	A B C	A B C
P_0	0 1 0	7 5 3
P_1	2 0 0	3 2 2
P_2	3 0 2	9 0 2
P_3	2 1 1	2 2 2
P_4	0 0 2	4 3 3

Please calculate the maximum request (consider type A first, then B, and C last, and all together) which can be granted for process P_0 , P_1 , P_2 , P_3 , and P_4 , respectively, for the system to be in safe states.

7. Page replacement mechanisms are designed to handle page faults. Consider a reference string 1,0,2,1,2,3,0,3,2,4,0,3,0,2,1,0,7 and the system has three frames that are initially empty.

If a FIFO page-replacement algorithm is applied, the number of page faults is (first three references cause page faults) _____.

If the optimal page-replacement algorithm is applied, the number of page faults is (first three references cause page faults) _____.

If the LRU page-replacement algorithm is applied, the number of page faults is (first three references cause page faults) _____.

If the second-change algorithm is applied, the number of page faults is (first three references cause page faults) _____.

If the LFU page-replacement algorithm is applied, the number of page faults is (first three references cause page faults) _____.

8. Consider a 6,000 rpm disk, which is of one single head, 11 tracks, and 110 sectors in total. Let the seek time is linearly proportional to the number of tracks traveled, and to move the head across one track takes 1ms. Suppose that the current head position is at track 0, sector 0. It takes _____ milli-seconds to seek to track 10, sector 5. (note that tracks and sectors are numbered from 0)