

Siemens Test

Prerequisite:

1. A valid license for Chip Creator GUI tool.

Procedure:

1. Start Chip Creator GUI tool from Command line. Enter *CC_PROMPT> start_gui* in the GUI Prompt.

2. Design Menu is open. Import Library or Design of your choice.

Note: *Tech.lef* shall be read first, as GUI tool also supports old project files

OR

2. Enter *read_file* command in the GUI Prompt.
3. Use **Check > Input** to verify all the readings before going to the next step.
4. Use **Run > Create auto outline** to create automatic chip outline.

OR

4. Use **Edit > Draw outline** to manually draw the outline.

OR

4. Enter *CC_PROMPT> create outline xy* in the GUI Prompt.

Note: Distance units are in microns.

5. Place all the cells in the chip using **Run > Place all cells**.

OR

5. Enter *CC_PROMPT> place_cells -all* in the GUI Prompt.

6. Connect all wires using **Run > connect**.

OR

6. Enter *CC_PROMPT> connect* in the GUI Prompt.

Note: Wires use different layers available from *Tech.lef*. If wires do not connect, then re-place the cells again.

7. Use **Verify > All** to check if the design is created accordingly.

OR

7. Enter *CC_PROMPT> verify* in the GUI Prompt.

Result:

After following all the procedures, you shall now be able to create the chips in the Chip Creator GUI tool.