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摘 要： 操作系统管理着计算机的硬件和软件资源，它是向上层应用软件提供服务（接口）的核心系统软件，这些服务包括进程管理，内存管理，文件系统，网络通信，安全机制等。操作系统的设计与实现则是软件工业的基础。为此，在国务院提出的《中国制造 2025》中专门强调了操作系统的开发^[1]。但长期以来，操作系统核心技术都掌握在外国人手中，技术受制，对于我们的软件工业来说很不利。本项目从零开始设计开发个简单的操作系统，包括 boot loader，中断，内存管理，图形接口，多任务，以及在这个系统上的几个小应用等。尽管这个系统很简单，但它为自主开发操作系统做了尝试。

关键词： 操作系统，进程，内存，中断，boot loader

RongOS — A simple OS implementation

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Abstract: Operating system manages the resources of hardware and software, it lies in the core of the system software and provides services (interfaces) to upper applications. These services include process management, memory management, file system, network communication, security mechanism and more. Operating system development is the foundation and core of software industry. Therefore, *Made in China 2025* emphasizes the development of operating system that put forward by The State Council of China. For a long time, however, the OS kernel development technology is mastered by foreigner, due to technical limitations it is detrimental to our software industry. So this project will design and develop a simple operating system, including boot loader, interrupt, memory management, graphic interface, multitasking, and some little applications based on this system. In spite of the simplicity of this system, it's a small trying for autonomous development operating system.

Key words: operating system, boot loader, process, interrupt, memory management

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<code>\cite{hunt2005broad}</code>	1
use citation instead, see <code>thesis.bib</code>	1
debugger?	2
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use <code>\texttt{0x10,ax,al,...}</code> , rather than <code>\$0x10,ax,al,...\$</code>	11
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1 Introduction

1.1 Background

Contemporary software systems are beset by problems that create challenges and opportunities for broad new OS research. There are five areas could improve user experience including dependability, security, system configuration, system extension, and multiprocessor programming.

The products of forty years of OS research are sitting in everyone's desktop computer, cell phone, car, etc., and it is not a pretty picture. Modern software systems are broadly speaking complex, insecure, unpredictable, prone to failure, hard to use, and difficult to maintain. Part of the difficult is that good software is hard to write, but in the past decade, this problem and more specific shortcomings in systems have been greatly exacerbated by increased networking and embedded systems, which placed new demands that existing architectures struggled to meet. These problems will not have simple solutions, but the changes must be pervasive, starting at the bottom of the software stack, in the operating system.

The world needs broad operating system research. Dependability, security, system configuration, system extension, and multi-processor programming illustrate areas where contemporary operating systems have failed to meet the software challenges of the modern computing environment TODO: \cite{hunt2005broad}.

Please reference. TODO: use citation instead, see `thesis.bib`

TODO!
TODO!

1.2 Preliminary Works

1.2.1 Development Environment

OS platform: Debian 9, Linux kernel 4.12.0-1-amd64

Editor: GNU Emacs 25.2.2

Run time VM: QEMU emulator 2.8.1

Assembler: Nask

Compiler: CC1(Based on gcc)

TODO!

Debugger: TODO: debugger?

1.2.2 Tools

Some tools were used to develop RongOS, ~~see tools~~ **TODO: need some descriptions** **TODO!**
about the tools¹. Note that these tools are Windows executables **TODO: (so what?)** ~~exe format~~ **TODO!**
~~based on Windows system~~. Among these tools, the most important ones are:

nask.exe the assembler, a modified version of NASM

cc1 the C compiler

1.2.3 Platform Setup

The development platform (mainly the Debian system) was setup by following the *Debian Installation tutorial*². The main steps were:

1. Installing the base system;
2. Installing necessary software tools, such as emacs, web browser, qemu, wine, etc.;
3. Cloning configuration files by following the tutorial;
4. Some more miscellaneous tweaks to satisfy my personal needs.

~~Debian System: there is a small tutorial. This tutorial is a sample one for set up development environment, the main thing is operating system. ## Debian.~~

Qemu

QEMU is a generic and open source machine emulator and virtualizer. **TODO: need** **TODO!**
citation In this project, QEMU was used as the test bed. ~~to test the running result~~

Installing QEMU for my x86_64 architecture is as easy as the following command:

```
$ sudo apt-get install qemu-system-x86_64
```

¹<https://github.com/Puqiyuan/RongOS/tree/master/Tools>

²http://cs2.swfc.edu.cn/~wx672/lecture_notes/linux/install.html

Wine

Wine (originally an acronym for "Wine Is Not an Emulator") is a compatibility layer capable of running Windows applications on several POSIX-compliant operating systems, such as Linux, macOS, and BSD. **TODO: need citation**

TODO!

Because the tools I used in this project are in Windows exe format, so on Debian system, Wine need to be installed:

```
$ sudo apt-get update
$ sudo apt-get install wine
```

Debian i386 support

On 64-bit systems you need to enable multiarch support for running 32-bit Windows applications (many modern apps are still 32-bit, also for large parts of the Windows subsystem itself), our development tools are 32-bit Windows applications, so add this architecture:

```
$ sudo dpkg --add-architecture i386
$ sudo apt-get update
```

2 Leading Knowledge

2.1 Mouse

2.2 Data Structure

TODO: Data Structure

TODO!

2.3 Programmable Interrupt Controller

TODO: Programmable interrupt controller

TODO!

2.4 C Language Basis

TODO: C language basis

TODO!

2.5 Segment and Descriptor

TODO: segment and

TODO!

2.6 Instruction Set

An instruction set architecture (ISA) is an abstract model of a computer. It is also referred to as architecture or computer architecture. An ISA defines everything a machine language programmer needs to know in order to program a computer.

An ISA may be classified in a number of different ways. A common classification is by architectural complexity. A complex instruction set computer (CISC) has many specialized instructions, some of which may only be rarely used in practical programs. A reduced instruction set computer (RISC) simplifies the processor by efficiently implementing only

the instructions that are frequently used in programs, while the less common operations are implemented as subroutines, having their resulting additional processor execution time offset by infrequent use.

On traditional architectures, an instruction includes an opcode that specifies the operation to perform, such as add contents of memory to register—and zero or more operand specifiers, which may specify registers, memory locations, or literal data TODO: \cite{...}. TODO!

~~Please reference~~ TODO: use citation rather than footnote TODO!

The simple RongOS is based on x86 architecture, the following instructions are common for RongOS:

db: the abbreviation of define byte, write a byte, also 8 bits to file.

resb: the abbreviation of reserve byte, reserved bytes and filling 0x00 in these reserved space.

dw: the abbreviation of define word, write two bytes, also 16 bits to file.

dd: the abbreviation of define double-word, write four bytes, also 32 bits to file.

org: load the program to specified address.

jmp: jump to another instruction.

mov: assign the right value to left variable.

jc: the abbreviation of jump if carry, it means if carry flag is 1, jump.

jnc: jump if not carry.

jae: jump if above or equal.

jbe: jump if below or equal.

jb: jump if below.

equ: equ is the abbreviation of equal.

ret: end of function, return.

in: get signal from device.

out: send signal to device.

cli: clear interrupt flag, set it to 0.

sti: set interrupt flag, set it to 1.

pushfd: push flags double-word

popfd: pop flags double-word

lgdt: load content from specified memory to initialize GDT(global descriptor table) register.

lidt: load content from specified memory to initialize IDT(interrupt descriptor table) register.

2.7 x86 Registers

In computer architecture, a processor register is a quick accessible location available to a computer's central processing unit (CPU). Registers usually consist of a small amount of fast storage, although some registers have specific hardware functions, and may be read-only or write-only. Almost all computers, whether load/store architecture or not, load data from a larger memory into registers where it is used for arithmetic operations and is manipulated or tested by machine instructions. Manipulated data is then often stored back to main memory, either by the same instruction or by a subsequent one. Modern processors use either static or dynamic RAM as main memory, with the latter usually accessed via one or more cache levels. **TODO: need citation**

TODO!

Processor registers are normally at the top of the memory hierarchy, and provide the fastest way to access data. The term normally refers only to the group of registers that are directly encoded as part of an instruction, as defined by the instruction set. Registers are normally measured by the number of bits they can hold, for example, an “8-bit register” or a “32-bit register”. For x86 architecture, the following registers exist:

ax: accumulator	dl: data low	si: source index
bx: base	bh: base high	di: destination index
cx: counter	ah: accumulator high	es: extra segment
dx: data	ch: counter high	cs: code segment
bl: base low	dh: data high	ss: stack segment
al: accumulator low	sp: stack pointer	ds: data segment
cl: counter low	bp: base pointer	fs: no name

Among these registers, **bx, bp, si** **TODO: better be \texttt{bx, bp, si}** and **di** can be used **TODO!** to specify the address of memory. But **ax, cx, dx** and **sp** can not. When **mov** instruction is used **using**, the number of bits of operation number should be the same **TODO: don't under-** **TODO!**

stand. 16-bit registers: ax, cx, dx, bx, sp, bp, si, di, es, cs, ss, ds and fs. 8-bit registers: al, cl, dl, bl, ah, ch, dh and bh. Actually, all these 8-bit registers are part of corresponding 16-bit registers, low 8 bits or high 8 bits. **TODO: perhaps you can add a picture to illustrate your understanding.** **TODO!**

2.8 Interrupt Call

BIOS interrupt calls perform hardware control or I/O functions requested by a program, return system information to the program, or do both **TODO: need citation.** A key element of **TODO!** the purpose of BIOS calls is abstraction. The BIOS calls perform generally defined functions, and the specific details of how those functions are executed on the particular hardware of the system are encapsulated in the BIOS and hidden from the program. The interrupt calls are commonly used in RongOS are listed in Table 2-1.

Interrupt Number	Register Parameter	Return Value	Function
0x10	ah=0x0e(write character in tty mode) al=character code bh=0, bl=colorcolor	null	video services
0x13	ah=0x02(read sectors) ah=0x03(write sectors) ah=0x04(verify sectors) ah=0x0c(seek to specified track) al=number of sectors processing ch=cylinder & 0xff cl=sector number dh=header number dl=driver number es:bx=buffer address	FLACS.CF=0 no error, ah = 0 FLAGS.CF=1 error, ah=error number	disk services

表 2-1 RongOS interrupt calls

2.9 Memory Map

In the boot process, a memory map is passed on from the firmware in order to instruct an operating system kernel about memory layout **TODO: need citation**. It contains the in-**TODO!**formation regarding the size of total memory, any reserved regions and may also provide other details specific to the architecture. For loading RongOS to memory, the memory layout should be clarified as in Table 2-2.

Range(in hexadecimal)	Range(in decimal)	Size(in bytes)	Usage
0000 — 03ff	0000 — 1023	1024	interrupt vector table
0400 — 04ff	1024 — 1279	256	BIOS data area
0500 — 051f	1280 — 1311	32	Reserved
0520 — 7bff	1312 — 31743	30432	conventional memory
7c00 — 7dff	31744 — 32255	512	master boot record
7e00 — 9fff	32256 — 655359	623104	conventional memory
a0000 — affff	655360 — 720895	64K	VGA graphics RAM
b0000 — b7fff	720896 — 753663	32K	monochrome text mode
b8000 — bffff	753664 — 786431	32K	color text mode
c0000 — c7fff	786432 — 819199	32K	VGA video ROM
c8000 — cbfff	819200 — 835583	16K	IDE hard drive
cc000 — cffff	835584 — 851967	16K	optional adapter

表 2-2 RongOS Memory Layout

2.10 Floppy Disk

There are many ways to boot an operating system, from hard disk, USB, floppy disk, etc. The structure of floppy disk is simple and for this simple operating system, it's enough.

Fig. 2-1 shows the inside of a floppy disk:

A floppy disk, also called a floppy, diskette, or just disk, is a type of disk storage composed of a disk of thin and flexible magnetic storage medium, sealed in a rectangular plastic

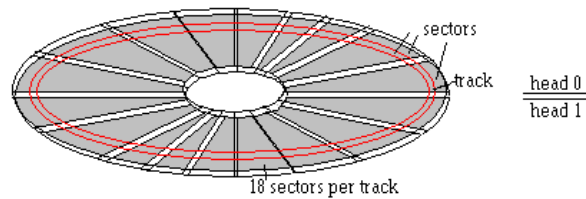


图 2-1 Floppy Disk Structure

enclosure lined with fabric that removes dust particles. Floppy disks are read and written by a floppy disk drive (FDD). **TODO: need citation**

TODO!

For 3.5 inch HD floppy, There are 80 cylinders from the outermost to the core on each side, numbering 0, 1, ..., 79. The head can assign be 0 or 1, representing two sides of floppy. When specify head number and cylinder number, forming a ring, named track in jargon. The track is large so we divide it to 18 small parts, named sector. A sector can store 512 byte. So the capacity of a floppy is:

$$18 \times 80 \times 2 \times 512 = 1474560 \text{ Byte} = 1440 \text{ KB}$$

3 Design

3.1 Top Level Design

3.1.1 32-bit Mode and Import C Codes

4 Implementation

4.1 Boot Loader

4.1.1 Flowchart of Boot Loader

Fig. 4-1 shows how the boot loader works.

TODO: need a better chart

图 4-1 Flowchart of Boot Loader

The boot loader is implemented in Intel assembly. It works as following:

TODO!

TODO: use `\texttt{0x10,ax,al,...}`, rather than `$0x10,ax,al,...$`

1. **Display boot information:** Firstly, the boot sector display some boot information, when *al* = 0, the null character of boot information hit. Interrupt *0x10* is used for show a character. Appendix A.1.1 is the code to perform this function.
2. **Read the second sector:** Then jump to load C0-H0-S2, *ax* register saved the address where beginning puts the sectors from floppy. And preparing parameters for interrupt *0x13* in registers. The *0x13* interrupt used for read sector from floppy to memory. Appendix A.1.2 is the code to perform this function.
3. **Read two sides of a track:** If there is a carry, representing some thing wrong when read floppy, so reset the registers and try again read floppy, until five times trying. Register *si* is a counter. If no carry, jump to next segmentation, as one sector read to memory already, the address space should increase 512 byte. Then sector number(*cl* register) added 1 and compare it to 18, if it's smaller than 18, jump to *readloop*, read the next sector. If the value of *cl* register bigger or equal to than 18, meaning that one track 18 sector in this side of floppy read already, then reversed the head, add 1 to *dh* register. If the value of *dh* register after adding larger than or equal to 2, it's saying the original head is 1, one track of two sides read already. Otherwise the value of *dh* register smaller than 2, read this side indicating by *dh* register, jump to *readloop*

segmentation. Appendix A.1.3 is the code to perform this function.

4. **The next cylinder:** So the next step is moving a cylinder, add 1 to register *ch*. Otherwise the value of *dh* register smaller than 2, read this side indicating by *dh* register, jump to *readloop* segmentation. After *ch* register add 1, if it's smaller than 10, jump to *readloop*, otherwise end loading floppy to memory process, for we only load ten cylinders of floppy. Appendix A.1.4 is the code to perform this function.

4.1.2 Running Result

Fig. 4-2 shows the running results of boot loader. From this picture we see that the boot loader loaded 10 cylinders from floppy successfully.

TODO: need a better pic.

图 4-2 Running Result of Boot Loader

4.2 32-bit Mode and Import C Codes

4.3 Screen Display and Text

4.4 Control Mouse

4.5 Memory Management

4.6 Making Window

4.7 Timer

4.8 Multitasking

4.9 Command Line Window

4.10 API

4.11 OS Protection

4.12 Graphics Processing

4.13 Window Operation

4.14 Application Protection

4.15 File Operation

4.16 Some Applications

5 Prospects And Shortages

参考文献

- [1] 国务院, 中国制造 2025, **2015-05**.

指导教师简介

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首先我想感谢我的老师，王晓林。大学期间，他给了我很多指导，包括专业方面和上大学的意义等。很多时候，他对学生的要求看起来都是不近情理的，但正是通过这个“痛苦”的过程，我锻炼了坚强的意志，和战胜困难的信心。谢谢你，王老师。我最想感谢的是我的女友，她容忍我在完成这个设计时的很多个夜晚不陪她，给我支持，鼓励我，不抱怨。所以我愿意把这个简单操作系统命名为 RongOS, 蓉便是她名字的最后两个字。谢谢你，我最亲爱的。

附录 A Main Program Code

A.1 Boot loader

A.1.1 Display boot information

```
55  init:
56      mov al, [si]
57      add si, 1 ; increment by 1.
58      cmp al, 0
59      je load ; if al == 0, jmp to load, the msg_init info
           ↪ displayed.
60 ; the lastest character is null character, coding in 0.
61
62      mov ah, 0x0e ; write a character in TTY mode.
63      mov bx, 15 ; specify the color of the character.
64      int 0x10 ; call BIOS function, video card is number 10.
65      jmp init
```

A.1.2 Read the second sector

```
87  load:
88      mov ax, 0
89      mov ax, 0x0820 ; load C0-H0-S2 to memory begin with
           ↪ 0x0820.
90      mov es, ax
91      mov ch, 0 ; cylinder 0.
92      mov dh, 0 ; head 0.
93      mov cl, 2 ; sector 2.
94
```

```
95
96 readloop:
97     mov si, 0 ; si register is a counter, try read a sector
98     ; five times.
99
100
101 retry:
102     mov ah, 0x02 ; parameter 0x02 to ah, read disk.
103     mov al, 1 ; parameter 1 to al, read disk.
104     mov bx, 0
105     mov dl, 0x00 ; the number of driver number.
106     int 0x13 ; after prepared parameters, call 0x13
        ↪ interrupted.
```

A.1.3 Read two sides of a track

```
108     jnc next ; if no carry read next sector.
109     add si, 1 ; tring again read sector, counter add 1.
110     cmp si, 5 ; until five times
111     jae error ; if tring times large than five, failed.
112
113     ; reset the status of floppy and read again.
114     mov ah, 0x00
115     mov dl, 0x00
116     int 0x13
117     jmp retry
118
119
120 next:
121     mov ax, es
122     ; we can not directly add to es register.
123     add ax, 0x0020 ; add 0x0020 to ax
```

```
124      mov es, ax ; the memory increase 0x0020 * 16 = 512 byte.  
125      ; size of a sector.  
126      add cl, 1 ; sector number add 1.  
127      cmp cl, 18 ; one track have 18 sector.  
128      jbe readloop ; jump if below or equal 18, read the next  
           ↪ sector.  
129      mov cl, 1 ; cl number reset to 1, ready to read the other  
           ↪ side.  
130      add dh, 1 ; the other side of floppy.  
131      cmp dh, 2 ; only two sides of floppy.  
132      jb readloop ; if dh < 2, read 18 sectors of the other  
           ↪ sides
```

A.1.4 The next cylinder

```
134      mov dh, 0 ; after finished read the other side, reset head to 0.  
135      add ch, 1 ; two sides of a cylinder readed, add 1 to ch.  
136      cmp ch, CYLS ; read 10 cylinders.  
137      jb readloop
```