西南林业大学 本科毕业(设计)论文

(二〇一八届)

题	目:	RongOS — 一个简单操作系统的
		设计与实现
/ \ 18≥-	₹ 20 7	
分院	系部:_	大数据与智能工程学院
专	业: _	计算机科学与技术专业
姓	名:_	蒲启元
导师	姓名:_	王晓林
导师	职称:	讲师

RongOS — 一个简单操作系统的设计与实现

蒲启元

(西南林业大学 大数据与智能工程学院,云南昆明 650224)

摘 要:操作系统管理着计算机的硬件和软件资源,它是向上层应用软件提供服务(接口)的核心系统软件,这些服务包括进程管理,内存管理,文件系统,网络通信,安全机制等。操作系统的设计与实现则是软件工业的基础。为此,在国务院提出的《中国制造2025》中专门强调了操作系统的开发[1]。但长期以来,操作系统核心开发技术都掌握在外国人手中,技术受制,对于我们的软件工业来说很不利。本项目从零开始设计开发一个简单的操作系统,包括 boot loader,中断,内存管理,图形接口,多任务等功能模块,以及能运行在这个系统之上的几个小应用程序。尽管这个系统很简单,但它是自主开发操作系统的一次尝试。

关键词:操作系统,进程,内存,中断,boot loader

RongOS — A simple OS implementation

Qiyuan PU

School of Big Data and Intelligence Engineering Southwest Forestry University Kunming 650224, Yunnan, China

Abstract: Operating system manages the hardware and software resources in a running computer system. It is the core of any modern software system and provides services (interfaces) to upper layer applications. The services it provides include process management, memory management, file system, network communication, security mechanism and more. Operating system development is the foundation and core of software industry. Therefore, *Made in China 2025* emphasizes the development of operating system that put forward by The State Council of China. For long time, however, the OS kernel development technology is dominated by foreigners. This technical limitation is detrimental to the development of our software industry. In this project, we presents a simple operating system which includes a boot loader, interrupt services, memory management functions, a graphic interface, and multi-process management functions. Also, some trivial user-level applications are provided for system testing purpose. This simple toy OS is an experimental trial for developing an operating system from scratch.

Key words: operating system, boot loader, interrupt, process management, memory management

Contents

1	Intr	oductio	n	1			
	1.1	.1 Background					
1.2 Preliminary Works			inary Works	1			
		1.2.1	Development Environment	1			
		1.2.2	Tools	2			
		1.2.3	Platform Setup	2			
2	Desi	gn		4			
	2.1	Top Le	evel Design	4			
	2.2	Detaile	ed Design	4			
		2.2.1	Boot Up	4			
		2.2.2	Kernel	6			
		2.2.3	API	15			
		2.2.4	APPs	18			
3	Imp	lementa	ntion				
	3.1	Boot U	Jp	19			
	3.2	Kernel		22			
		3.2.1	Memory Management	22			
	3.3	API .		22			
	3.4	APPs		22			
4	Con	clusions	5	25			
参	考文的	献		26			
Su	Supervisor 2						

CONTENTS

Ac	Acknowledgments					
A	A Main Program Code					
	A.1	Boot lo	oader	29		
		A.1.1	Display boot information	29		
		A.1.2	Read the second sector	29		
		A.1.3	Read two sides of a track	29		
		A.1.4	The next cylinder	30		

List of Figures

2-1	Top-level design	5
2-2	Context switch	8
3-1	the working flowchart of boot loader	21
3-2	memory check	23
3-3	subprogram of memory check	24

List of Tables

List of Corrections

do you have VFS?	4
say some more	6
say some more. you can get some ideas from wikipedia	6
???	6
see wikipedia	8
What's a layer/level?	8
not clear	9
???	11
bad	11
???	13
see wikipedia	13
wikipedia	13
wikipedia	14
Not clear. You have to explain what is al/ah for. For example, as shown in table	
al is for, ah is for int 0x10 is for	19

Introduction 1

This section will introduce the purpose and current status of the operating system re-

search. The setup of the development environment will also be presented here.

Background 1.1

Contemporary software systems are beset by problems that create challenges and op-

portunities for broad new OS research. There are five areas could improve user experience

including dependability, security, system configuration, system extension, and multiproces-

sor programming.

The products of forty years of OS research are sitting in everyone's desktop computer,

cell phone, car, etc., and it is not a pretty picture. Modern software systems are broadly speak-

ing complex, insecure, unpredictable, prone to failure, hard to use, and difficult to maintain.

Part of the difficult is that good software is hard to write, but in the past decade, this prob-

lem and more specific shortcomings in systems have been greatly exacerbated by increased

networking and embedded systems, which placed new demands that existing architectures

struggled to meet. These problems will not have simple solutions, but the changes must be

pervasive, starting at the bottom of the software stack, in the operating system.

The world needs broad operating system research. Dependability, security, system con-

figuration, system extension, and multi-processor programming illustrate areas were contem-

porary operating systems have failed to meet the software challenges of the modern comput-

ing environment^[2].

1.2 **Preliminary Works**

1.2.1 **Development Environment**

OS platform: Debian 9, Linux kernel 4.12.0-1-amd64

-1-

1 Introduction

Editor: GNU Emacs 25.2.2

Run time VM: QEMU emulator 2.8.1

Assembler: Nask

Compiler: CC1(Based on gcc)

Debugger: GNU gdb 7.12

Version Control: git 2.15

1.2.2 Tools

Some tools were used to develop RongOS, See *tools*¹. Note that these tools are Windows executable. Please install wine if you want to run these tools on Linux. In these tools, the most important ones are:

nask.exe: the assembler, a modified version of NASM^[3]

cc1: the C compiler

1.2.3 Platform Setup

The development platform (mainly the Debian system) was set up by following the *Debian Installation tutorial*². The main steps include:

- 1. Installing the base Debian system;
- 2. Installing necessary software tools, such as emacs, web browser, qemu, wine, etc.;
- 3. Cloning configuration files by following the tutorial mentioned above;
- 4. Some more fine tweaks to satisfy my personal needs.

1.2.3.1 Qemu

QEMU is a generic and open source machine emulator and virtualizer^[4]. In this project, QEMU was used as the test bed.

Installing QEMU for my x86_64 architecture can be easily done by executing the following command:

\$ sudo apt-get install qemu-system-x86_64

1https://github.com/Puqiyuan/RongOS/tree/master/z_tools

2http://cs2.swfc.edu.cn/~wx672/lecture_notes/linux/install.html

1.2.3.2 Wine

Wine (originally an acronym for "Wine Is Not an Emulator") is a compatibility layer capable of running Windows applications on several POSIX-compliant operating systems, such as Linux, macOS, and BSD^[5].

Because the tools I used in this project are in Windows executable format, so on Debian system, Wine is needed to be installed:

```
$ sudo apt-get update
$ sudo apt-get install wine
```

1.2.3.3 Debian i386 support

On 64-bit systems you need to enable multi-arch support for running 32-bit Windows applications (many modern apps are still 32-bit, also for large parts of the Windows subsystem itself). Our development tools were 32-bit Windows applications, so we needed to have i386 support for our 64-bit Linux system.

```
$ sudo dpkg --add-architecture i386
$ sudo apt-get update
```

2 Design

This chapter describes the design issues in the entire software system, including system startup, the kernel, APIs, and some applications.

2.1 Top Level Design

As shown in Fig. 2-1 shown, all applications require services from the operating system through a set of function calls. This set of functions provided by the OS kernel is usually called *system calls*. However, the user applications do not invoke these system calls directly. Instead, they call the API library functions. The process in which the API invokes a system call and hands over processing to the kernel is called *trapping*.

Within the kernel, there are some important software modules working to keep the system running well. The process control subsystem provides graphics processing, CPU scheduling, and memory management functions for processes. Fixme: do you have VFS? Fixme! The file subsystem provides a friendly way to the user processes for accessing disk data. For example, to launch an application program stored on the disk, the function in the file system must be invoked to find the specific application. All of these subsystems may interact with the driver or hardware control module so as to operate on the system hardwares.

2.2 Detailed Design

This section discusses the system design in details, including the function of each module, data structures used in boot loader, kernel, API, and APPs.

2.2.1 **Boot Up**

At this stage the boot loader loads the operating system kernel into memory. This is done in the following four steps:

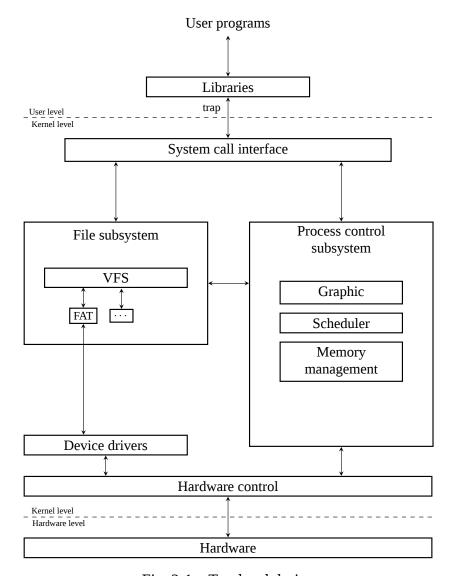


Fig. 2-1 Top-level design

- 1. Display boot information;
- 2. Read the second sector;
- 3. Read two sides of a track;
- 4. Read the next cylinder until all twenty cylinders have been read.

At this stage, it is also necessary to complete the 32-bit protection mode switch and jump to the entry point of the operating system.

2.2.2 Kernel

The kernel receives the API calls from user processes, and operates the hardwares through the device drivers. Fixme: say some more.

2.2.2.1 Memory Management

The process may request memory space while running. When it is finished running, the operating system kernel shall reclaim its memory space. Fixme: say some more. you can get some ideas from wikipedia.

2.2.2.1.1 Data Structures used in MM

```
struct FREEINFO
{
    unsigned int addr; /* start address of free space */
    unsigned int size; /* size of free mem in bytes*/
};
```

• is used to record the size in bytes of free memory while the system is running.

```
struct MEMMAN
{
   int frees;  /* free memory blocks */
   int maxfrees; /* max of frees */
   int lostsize; /* release the sum of the failed memory size ????????*/
   int losts;  /* number of failures */
   struct FREEINFO free[MEMMAN_FREES]; /* free memory block info */
   };
```

• is used to record the entire memory usage, such as the total remaining free memory space and entries Fixme: ???.

Fixme!

2.2.2.1.2 Memory Management Functions

```
void memman_init(struct MEMMAN* man);
```

• The memory space is initialized and the man variable is used to record the memory space usage and free information.

```
unsigned int memman_total(struct MEMMAN* man);
```

• Report the sum of all empty space. Return the size of free memory.

```
unsigned int memman_alloc(struct MEMMAN *man, unsigned int size);
```

• Allocate memory to the application, where size is the space requested by the application. Success returns the available starting address, otherwise 0.

```
int memman_free(struct MEMMAN *man, unsigned int addr, unsigned int size);
```

• Releases memory, where addr is the starting address variable and size is the size of the release variable. Returns 0 if successful, otherwise -1.

```
unsigned int memman_alloc_4k(struct MEMMAN *man, unsigned int size);
```

Memory is allocated in 4k memory units and the starting address of the allocated memory is returned. size is the size of requested space.

• Memory space is freed in units of 4k, addr is the starting point variable, and size is the release size.

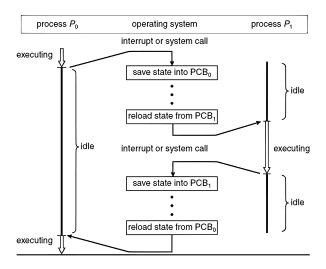


Fig. 2-2 Context switch

2.2.2.2 Task Management (Scheduler)

The general operating systems need to be able to support multitasking. Simply saying that multi-tasking is running multiple programs at the same time. But this only gives the user an illusion. For a single CPU, it cannot handle multiple programs at the same time. It merely divides the CPU time into many small pieces for different programs to run. The operating system should be able to do task switching, that is to pass the CPU from one process to another. And at a later time to switch it back, as shown in Fig. 2-2. Therefore, the register values should be saved when the task is switched. The task switching itself also takes time. The operating system should try to shorten this time. Doing this on the one hand provides the user with a good experience and does not leave the user with a delayed impression. On the other hand, reducing this time can improve CPU utilization. Because this time can not be used for program operation. Fixme: see wikipedia

2.2.2.2.1 Data Structures For Task Management

```
struct TASKLEVEL

int running; /* how many tasks are running */
int now; /* which task is currently running */
struct TASK* tasks[MAX_TASKS_LV]; /* all tasks in one level */
};
```

• is used to record the status of each task in a layer Fixme: What's a layer/level?.

```
struct TSS32
     int esp0, esp1, esp2; /* stack pointer register */
     int ss0, ss1, ss2;
                          /* stack segment register */
4
     int cr3;
                /* control register */
     int eip;
                /* instruct pointer register */
     int eflags; /* registers flag */
     int eax;
                /* accumulator register */
8
                /* counter register */
     int ecx;
9
                /* data register */
     int edx;
     int ebx;
                /* base register */
               /* stack pointer register */
     int esp;
                /* base pointer register*/
     int ebp;
                /* source index register */
     int esi;
                /* destination index register */
     int edi;
     int es;
                /* extra segment register */
                /* code segment register */
     int cs;
                /* stack segment register */
     int ss;
                /* data segment register */
    int ds;
                /* segment part 2 */
     int fs;
     int gs;
                /* segment part 3 */
     int ldtr;
                /* LDT segment selector */
     int iomap;
                /* I/O map base address */
  };
```

holds information about task status segments, which are based on CPU specifications^[6].

• is used to control all tasks in the system. Fixme: not clear.

Fixme!

```
struct TASK
3
     int sel;
                          /* the number of GDT */
     int flags;
                         /* the state of task */
4
     int level;
                          /* the level of task */
                         /* the priority of task */
     int priority;
     struct FIF032 fifo; /* a fifo buffer */
7
     TSS32 tss;
                          /* TSS segment for a task */
8
     struct CONSOLE* cons; /* the console window address of task */
9
                          /* data segment address of APPs */
     int ds_base;
                           /* the stack address of APPs */
     int cons_stack;
     struct SEGMENT_DESCRIPTOR ldt[2]; /* tow LDT segments of task */
     struct FILEHANDLE* fhandle; /* file handles for manipulating files */
     int* fat; /* file allocation table */
14
     char* cmdline; /* store the command line context */
     unsigned char langmode; /* which font to use */
     unsigned char langbyte1; /* store the first byte of the full-width

→ character */

18
   };
```

• is used to manage variables for a task. Record the task's sections, permissions, stacks, etc.

2.2.2.2. Task Management Functions

```
struct TASK *task_now(void);
```

• Returns which level of which task is currently running.

```
struct TASK *task_init(struct MEMMAN *memman);
```

• Initialize a task.

```
struct TASK *task_alloc(void);
```

• Initialize the structure of a task.

```
void task_run(struct TASK *task, int level, int priority);
```

· Add a task to list.

```
void task_switch(void);
```

• Switch to the next task.

```
void task_sleep(struct TASK *task);
```

• Put a task to sleep.

2.2.2.3 Graphic Management

The patterns on the screen belong to one layer. The movement of the window is achieved through layers Fixme: ???. The height of the layer affects the layout of the screen. Layers to Fixme! consider how to cover when moving, which one is on and below. Fixme: bad

2.2.2.3.1 Graphic Management Data Structures

```
struct SHEET
    char* buf; /* address of the graphic content depicted */
3
    int bxszie; /* size of x coordinate of sheet */
4
    int bysize; /* size of y coordinate of sheet */
5
    int vx0;
               /* x coordinate of sheet */
                 /* y coordinate of sheet */
    int vy0;
    int col_inv; /* number of invisible color */
    int height; /* height of sheet */
9
    int flags;
                 /* states of sheet, using or not */
  };
```

• is used to record layer-related information, including the layer's size and position.

• is used to manage the structure of multiple layer information, including how many layers there are in total, the size and height of each layer.

2.2.2.3.2 Graphic Management Functions

• Initialize a sheet control structure, vram is the address of video RAM. xsize and ysize is the size of sheet.

```
struct SHEET *sheet_alloc(struct SHTCTL *ctl);
```

• Return a SHEET structure if success, otherwise 0.

• Set the properties of the layer buf.

```
void sheet_updown(struct SHEET *sht, int height);
```

Set the height of layer sht.

```
void sheet_refresh(struct SHEET *sht, int bx0, int by0, int bx1, int by1);
```

• Refreshes the screen range specified by bx0, by0, bx1 and by1.

```
void sheet_free(struct SHEET *sht);
```

· Release layer.

• Make a window in SHEET buf.

```
void putfonts8_asc_sht(struct SHEET *sht, int x, int y, int c, int b, char

     *s, int 1);
```

• Paint the background color and write the characters and finish the refresh.

2.2.2.4 System Calls

The system calls encapsulate each module in the kernel Fixme: ???. These system Fixme! calls are part of the kernel. In this system, system calls are called by the API instead of the application. Fixme: see wikipedia

2.2.2.4.1 System Calls Prototype

• Start an application based on the input from the command line cmdline;

int cmd_app(struct CONSOLE *cons, int *fat, char *cmdline);

2.2.2.5 **Driver**

The device drivers are used to control and manipulate the hardware. The driver blocked Fixme! the hardware information Fixme: wikipedia. It is the dividing line between hardware and software. A driver provides a software interface to hardware devices, enabling operating systems and other computer programs to access hardware functions without needing to know precise details of the hardware being used.

2.2.2.5.1 Driver Functions

2.2.2.6 FAT File System

· activate mouse.

Fixme!

The file system defines how data is stored and accessed. The file system divides the entire storage space according to a certain form. This facilitates the storage of files while increasing the utilization of storage media. The small blocks thus divided are called sectors. The FAT records which sectors all files are stored on. Fixme: wikipedia

2.2.2.6.1 FAT Data Structure

```
struct FILEINFO
    unsigned char name[8]; /* file name */
3
    unsigned char ext[3];
                           /* extend name of file */
4
    unsigned char type;
                           /* file attributes */
5
                           /* reserve byte */
    char reserve[10];
    unsigned short time; /* the time for storing file */
    unsigned short date;
                           /* the date for storing file */
    unsigned short clustno; /* the file from which sector on the disk is

    stored */

  };
```

• is used to record file-related information, such as file name, size, etc.

2.2.2.6.2 FAT Functions

```
void file_readfat(int *fat, unsigned char *img);
```

read file allocation table

```
void file_loadfile(int clustno, int size, char *buf, int *fat, char *img);
```

read file contents into memory

```
struct FILEINFO *file_search(char *name, struct FILEINFO *finfo, int max);
```

• search for files based on provided name.

2.2.3 API

In computer programming, APIs are subroutines that are used to develop applications. Usually, it communicates all parts of the software. A good API makes application development easier. By abstracting the underlying implementation and only exposing objects or actions the developer needs, an API simplifies programming.

2.2.3.1 All APIs

```
void api_putchar(int c);
```

• output a character c on the console window.

```
void api_putstr0(char *s);
```

• ouput a string s on the console window.

```
void api_putstr1(char *s, int 1);
```

• output a string s on console window, 1 is the length of this string.

```
void api_end(void);
   • end the application's running.
int api_openwin(char *buf, int xsiz, int ysiz, int col_inv, char *title);
   • open a window, return the hanlder of window.
void api_putstrwin(int win, int x, int y, int col, int len, char *str);
   • display string str on window.
void api_boxfilwin(int win, int x0, int y0, int x1, int y1, int col);
   • draw a box on the window
void api_initmalloc(void);
   • initialize the structure of the management memory
char *api_malloc(int size);
   • allocating memory for the application.
void api_free(char *addr, int size);
   • free up memory used by the application.
void api_point(int win, int x, int y, int col);
   • draw a pint on win window.
void api_refreshwin(int win, int x0, int y0, int x1, int y1);
```

• refresh the window. void api_linewin(int win, int x0, int y0, int x1, int y1, int col); • draw a straight line. void api_closewin(int win); · close window. int api_getkey(int mode); • accept keyboard input. int api_alloctimer(void); • get timer. void api_inittimer(int timer, int data); • set the data of timer sending. void api_settimer(int timer, int time); • set the time of timer. void api_freetimer(int timer); • release timer. void api_beep(int tone); · make a buzzer sound

```
int api_fopen(char *fname);
   • open file, return file hanlder
void api_fclose(int fhandle);
   · close file
void api_fseek(int fhandle, int offset, int mode);
   · locate a file
int api_fsize(int fhandle, int mode);
   • return the size of fhandle file.
int api_fread(char *buf, int maxsize, int fhandle);
   • read file, return read bytes.
int api_cmdline(char *buf, int maxsize);
   • return the context of command line.
int api_getlang(void);
   • return the language mode.
```

2.2.4 APPs

Since this design is only about the operating system itself, only three representative applications are introduced. The first is the application of the display color: color. The second is the application for timing: sundial. The third application is to imitate the application of stars in the sky: stars2.

3 Implementation

3.1 Boot Up

The boot loader is implemented in Intel assembly. It works as follows:

- 1. Display boot information: Firstly, the code in boot sector (See Appendix A.1.1) outputs some boot information. When al=0, the null character of boot information hit. Fixme! Interrupt 0x10 is used for showing a character on the screen. Fixme: Not clear. You have to explain what is al/ah for. For example, as shown in table... al is for..., ah is for... int 0x10 is for...
- Read the second sector: Then jump to load C0-H0-S2, ax register saved the address
 where beginning puts the sectors from floppy. And preparing parameters for interrupt
 0x13 in registers. The 0x13 interrupt used for read sector from floppy to memory. (See
 Appendix A.1.2).

3. Read two sides of a track:

If there is a carry indicating some thing went wrong while reading the floppy disk, reset the registers and try reading it again. The read process aborts after five unsuccessful read.

Register si is a counter. If no carry (success), jump to next segment, as one sector has been read into memory already. The address should increase 512 byte. Then sector number (cl register) is added by 1 and compare it to 18, if it's smaller than 18, jump to readloop, read the next sector.

If the value of cl register is bigger or equal to 18, meaning that one track 18 sector in this side of floppy read already, then reversed the head, add 1 to dh register.

If the value of dh register after adding larger than or equal to 2, it's saying the original head is 1, one track of two sides read already. Otherwise the value of dh register smaller than 2, read this side indicating by dh register, jump to readloop segmentation. Appendix A.1.3 is the code performing this function.

There is a pseudo code about this process:

```
Result: Read two sides of one track
 1 ENTRANCE: call readloop();
2 Procedure readloop()
      clear the times of failed to 0, si \leftarrow 0;
      call retry();
5 Procedure retry()
      register parameter preparing;
      read a sector;
      if no carry then
 8
         call next();
 9
      else
10
          add 1 to si, si \leftarrow si + 1;
11
12
          compare si with 5;
          if si >= 5 then
13
             goto error, FINISHED;
14
          else
15
16
           reset registers and call retry() to read again;
17
18
      \mathbf{end}
19 Procedure next()
20
      memory address moved back 0x200;
      add 1 to cl, preparing for reading the next sector, cl \leftarrow cl + 1;
21
      if cl \ll 18 then
22
         call readloop() to read this sector;
23
      else
24
          cl > 18, it means that one side of this track is read already;
25
26
          add 1 to dh, dh \leftarrow dh + 1, reverse the head pointer;
          if dh < 2 then
27
             it means the 1 side has not read yet, call readloop();
28
          else
29
             both sides have finished reading, FINSHED;
30
          end
31
      end
32
```

Algorithm 1: read two sides of one track

4. **The next cylinder:** So the next step is moving a cylinder, add 1 to register ch. Otherwise the value of dh register smaller than 2, read this side indicating by dh register, jump to readloop segmentation. After ch register add 1, if it's smaller than 10, jump to readloop, otherwise end loading floppy to memory process, for we only load ten cylinders of floppy. Appendix A.1.4 is the code to perform this function.

The above four steps can be intuitively reflected in the Fig. 3-1.

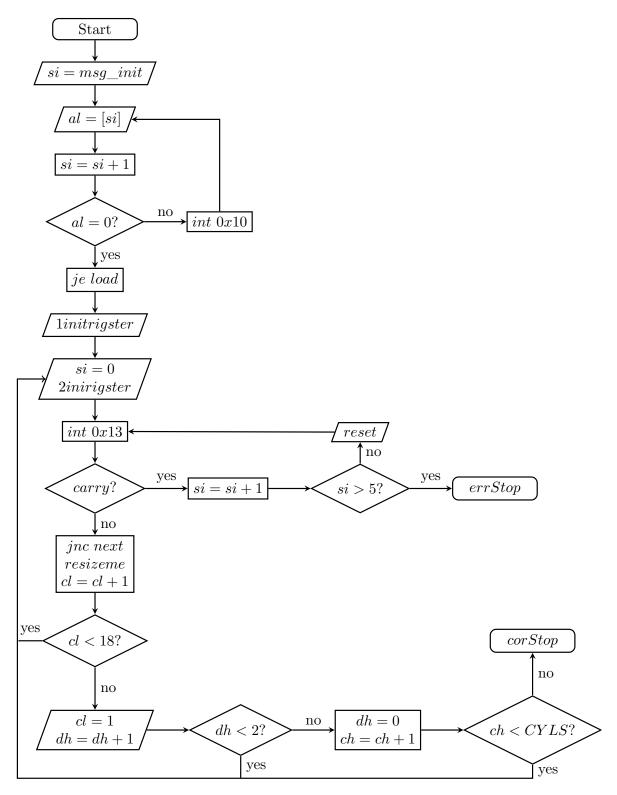


Fig. 3-1 the working flowchart of boot loader

3.2 Kernel

3.2.1 Memory Management

3.2.1.1 Memory Check

For memory management, memory must be checked to see how large the memory is. After i486, the CPU turned on the cache function. In order to avoid checking the cache, the CPU's cache function must be turned off. So first check the version of the CPU. Then check the memory capacity. When checking the memory capacity, first write a value to the same address and immediately read out whether the observation is equal to the previously written value. The entire inspection process can be shown by 3-2 and 3-3.

3.3 **API**

3.4 APPs

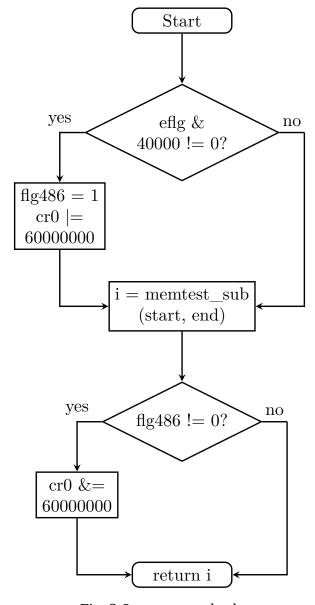


Fig. 3-2 memory check

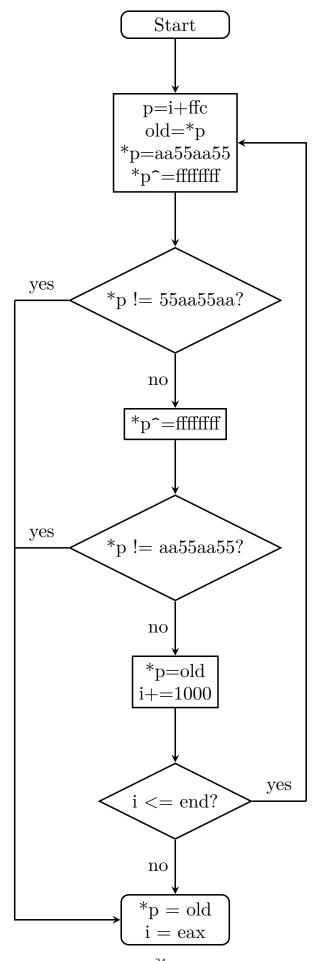


Fig. 3-3 subprogram of memory check

4 Conclusions

4.0.0.0.1 What goes in your "Conclusions" chapter? The purpose of this chapter is to provide a summary of the whole thesis or report. In this context, it is similar to the Abstract, except that the Abstract puts roughly equal weight on all thesis/report chapters, whereas the Conclusions chapter focuses primarily on the findings, conclusions and/or recommendations of the project.

There are a couple of rules -one rigid, one common sense, for this chapter:

- All material presented in this chapter must have appeared already in the report; no new material can be introduced in this chapter. (rigid rule of technical writing)
- Usually, you would not present any new figures or tables in this chapter.
 (rule of thumb)

Generally, for most technical reports and Masters theses, the Conclusions chapter would be 3 to 5 pages long (double spaced). It would generally be longer in a large PhD thesis. Typically you would have a paragraph or two for each chapter or major subsection. Aim to include the following (typical) content.

- 1. Re—introduce the project and the need for the work—though more briefly than in the intro:
- 2. Re-iterate the purpose and specific objectives of your project.
- 3. Re-cap the approach taken -similar to the road map in the intro; however, in this case, you are re-capping the data, methodology and results as you go.
- 4. Summarize the major findings and recommendations of your work.
- 5. Make recommendations for future research.

Ohttps://thesistips.wordpress.com/2012/03/25/how-to-write-your-introduction-abstract-and-summary/

参考文献

- [1] 国务院,中国制造 2025, 2015-05.
- [2] G. C. Hunt, J. R. Larus, D. Tarditi, T. Wobber in HotOS, 2005.
- [3] 川合秀实, 30 天自制操作系统, 1st ed., 人民邮电出版社, 2012-08.
- [4] W. contributors, QEMU Wikipedia, The Free Encyclopedia, [Online; accessed 12-January-2018], **2017**.
- [5] W. contributors, Wine (software) Wikipedia, The Free Encyclopedia, [Online; accessed 12-January-2018], **2017**.
- [6] Intel, Intel® 64 and IA-32 Architectures Software Developer's Manual, Volume 3A, 1st ed., **2006-10**.

Supervisor

Xiaolin WANG (Mr.), 49 years old, got his MSc degree at University of Greenwich in UK. Currently he's been working as a lecturer at the School of Big Data and Intelligence Engineering, Southwest Forestry University in China, teaching Linux, Operating Systems, and Computer Networking.

Acknowledgments

I would like to thank my supervisor Mr. WANG Xiaolin for his continuous support of my four years undergraduate study. I am extremly thankful to him for sharing expertise, and sincere and valuable guidance and encouragement extended to me.

What I most want to thank is my girlfriend. She tolerated me when I finished this graduation project many nights did not accompany her, gave me support, encouraged me, and did not complain. So I would like to name this simple operating system as RongOS. Rong is the last word of her name. Thank you, my dearest.

My special thanks to a great company - Google, I think I need to thank you in this very formal place in my graduation thesis. Every time you gave me a lot of help, the knowledge and other abilities I learned from you will have a profound impact on my future life. I am grateful for every search, because I know you will give me the results I want. Without you, this paper cannot be completed. Thank you.

A Main Program Code

A.1 Boot loader

A.1.1 Display boot information

```
init:
    mov al, [si]
    add si, 1 ; increment by 1.
    cmp al, 0
    je load ; if al == 0, jmp to load, the msg_init info displayed.
    ; the lastest character is null character, coding in 0.

mov ah, 0x0e ; write a character in TTY mode.
    mov bx, 15 ; specify the color of the character.
    int 0x10 ; call BIOS function, video card is number 10.
    jmp init
```

A.1.2 Read the second sector

```
load:
            mov ax, 0
            mov ax, 0x0820; load CO-HO-S2 to memory begin with 0x0820.
            mov es, ax
            mov ch, 0 ; cylinder 0.
            mov dh, 0; head 0.
            mov cl, 2; sector 2.
    readloop:
            mov si, 0 ; si register is a counter, try read a sector
98
    ; five times.
    retry:
            mov ah, 0x02; parameter 0x02 to ah, read disk.
            mov al, 1; parameter 1 to al, read disk.
            mov bx, 0
            mov dl, 0x00; the number of driver number.
            int 0x13; after prepared parameters, call 0x13 interrupted.
106
```

A.1.3 Read two sides of a track

```
jnc next; if no carry read next sector.

add si, 1; tring again read sector, counter add 1.

cmp si, 5; until five times

jae error; if tring times large than five, failed.
```

```
; reset the status of floppy and read again.
        mov ah, 0x00
        mov dl, 0x00
        int 0x13
        jmp retry
next:
        mov ax, es
        ; we can not directly add to es register.
        add ax, 0x0020 ; add 0x0020 to ax
        mov es, ax; the memory increase 0x0020 * 16 = 512 byte.
        ; size of a sector.
        add cl, 1; sector number add 1.
        cmp cl, 18; one track have 18 sector.
        jbe readloop; jump if below or equal 18, read the next sector.
        mov cl, 1; cl number reset to 1, ready to read the other side.
        add dh, 1; the other side of floppy.
        cmp dh, 2; only two sides of floppy.
        jb readloop; if dh < 2, read 18 sectors of the other sides
```

A.1.4 The next cylinder

```
mov dh, 0; after finished read the other side, reset head to 0.
add ch, 1; two sides of a cylinder readed, add 1 to ch.
cmp ch, CYLS; read 10 cylinders.
jb readloop
```