[2][1][3]

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题　　目： RongOS — 一个简单操作系统的实现

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RongOS — 一个简单操作系统的实现

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摘　要： 操作系统管理着计算机的硬件和软件资源，它是向上层应用软件提供服务（接口）的核心系统软件，这些服务包括进程管理，内存管理，文件系统，网络通信，安全机制等。操作系统的设计与实现则是软件工业的基础与内核。为此，在国务院提出的《中国制造 2025》中专门强调了操作系统的开发。但长期以来，操作系统核心开发技术都掌握在外国人手中，技术受制，对于我们的软件工业来说很不利。本文拟从零开始设计开发一个简单的操作系统，包括 boot loader，中断，内存管理，图形接口，多任务，以及在这个系统上的几个小应用等。尽管这个系统很简单，但它为自主开发操作系统做了一个小小的尝试。

关键词： 操作系统，开发，自主

The implement of a simple OS — RongOS

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Abstract: Operating system manages the sources of hardware and software, it lie in the core of the system software and provide service(interface) to upper application. These service including process management, memory management, file system, network communication, security mechanism etc. The design and implement of operating system is the foundation and core of software industry. Therefore, «Made in China 2025» emphasize the development of operating system that put forward by The State Council. For a long time, however, the kernel development technology grasped in the hand of foreigner, it's bad for our software industry cause of limited technology. So this article will design and develop a simple operating system, including boot loader, interrupt, memory management, graphic interface, multitasking, and some little application depend on this system. In spite of the simple of this system, it's a small trying for autonomous development operating system.

Key words: operating system, development, autonomous

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1 Introduction

1.1 Background

1.2 Preliminary Works

1.2.1 Development Environment

Operating System: Debian 4.11.0-1-amd64

Debug System: QEMU emulator version 2.8.1(Debian 1:2.8+dfsg-7)

Emacs version: GNU Emacs 25.2.2

1.2.2 Tools

Some tools used to develop RongOS, see tools.[1].

1.2.3 Install

Debian System: there is a small tutorial.[2]

QEMU, for my x86_64 architecture:

```
$ sudo apt-get install qemu-system-x86_64
```

Note that the tools is exe formate, so on Debian system, you need to install wine:

```
$ sudo apt-get update
$ sudo apt-get install wine
```

Maybe you also need to add i386 architecture cause of AMD64 on your machine to use these tools:

```
$ sudo dpkg --add-architecture i386
```

[1] https://github.com/Puqiyuan/RongOS/tree/master/Tools
[2] http://cs2.swfc.edu.cn/~wx672/lecture_notes/linux/install.html

```
$ sudo apt-get update
```

2 Design

2.1 Top Level Design

2.2 Detailed Design

2.2.1 Boot Loader

This is working flow of boot loader:

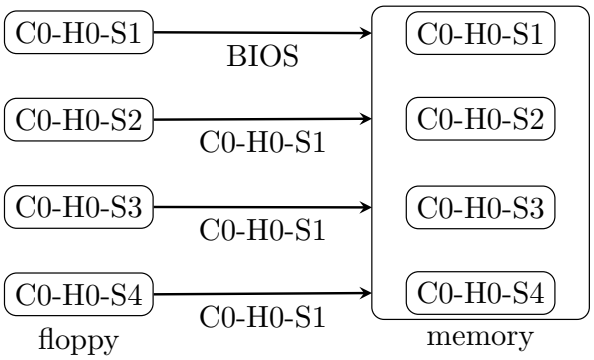

图 2-1 Working Flow of Boot Loader

The instructions of boot loader saved in C0-H0-S1 of floppy, the first cylinder, head 0, the first sector, total 512 byte. These instructions end with 0x55 0xaa, so BIOS will load C0-H0-S1 to memory, then the instructions in C0-H0-S1 will load C0-H0-S2 — C9-H1-S18, total $10 * 2 * 18 * 512 = 184320byte = 180KB$(including boot sector, C0-H0-S1) to main memory.

3 Implementation

3.1 Boot Loader

3.1.1 Chose Disk

There are many ways to boot a operating system, from hard disk, USB, floppy disk etc. I chose floppy disk, although it is out of date. For my purpose is that develop a simple operating system, pay my attention on how to development. The structure of floppy disk is simple and for my simple operating system it's enough.

3.1.2 The Structure of Floppy Disk

This picture show the inside of floppy disk:

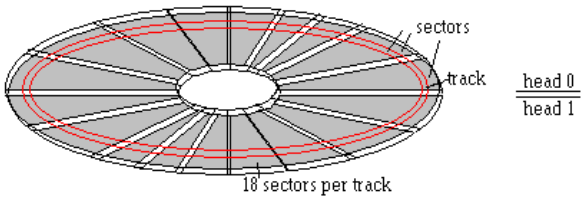

图 3-1 Floppy Disk Structure

The floppy store information in two sides. There are 80 cylinders from the outermost to the core in each side, numbering 0, 1, ..., 79. The head can assign be 0 or 1, representing two sides of floppy. When specify head number and cylinder number, forming a ring, named track in jargon. The track is large so we divide it to 18 small parts, named sector. A sector can store 512 byte. So the capacity of a floppy is:

$$18 * 80 * 2 * 512 = 1474560Byte = 1440KB.$$

The IPL(Initial Program Loader) in C0-H0-S1(cylinder 0, head 0, sector 2), and the next sector is C0-H0-S2.

3.1.3 Flowchart of Boot Loader

The following is the flowchart of boot loader:

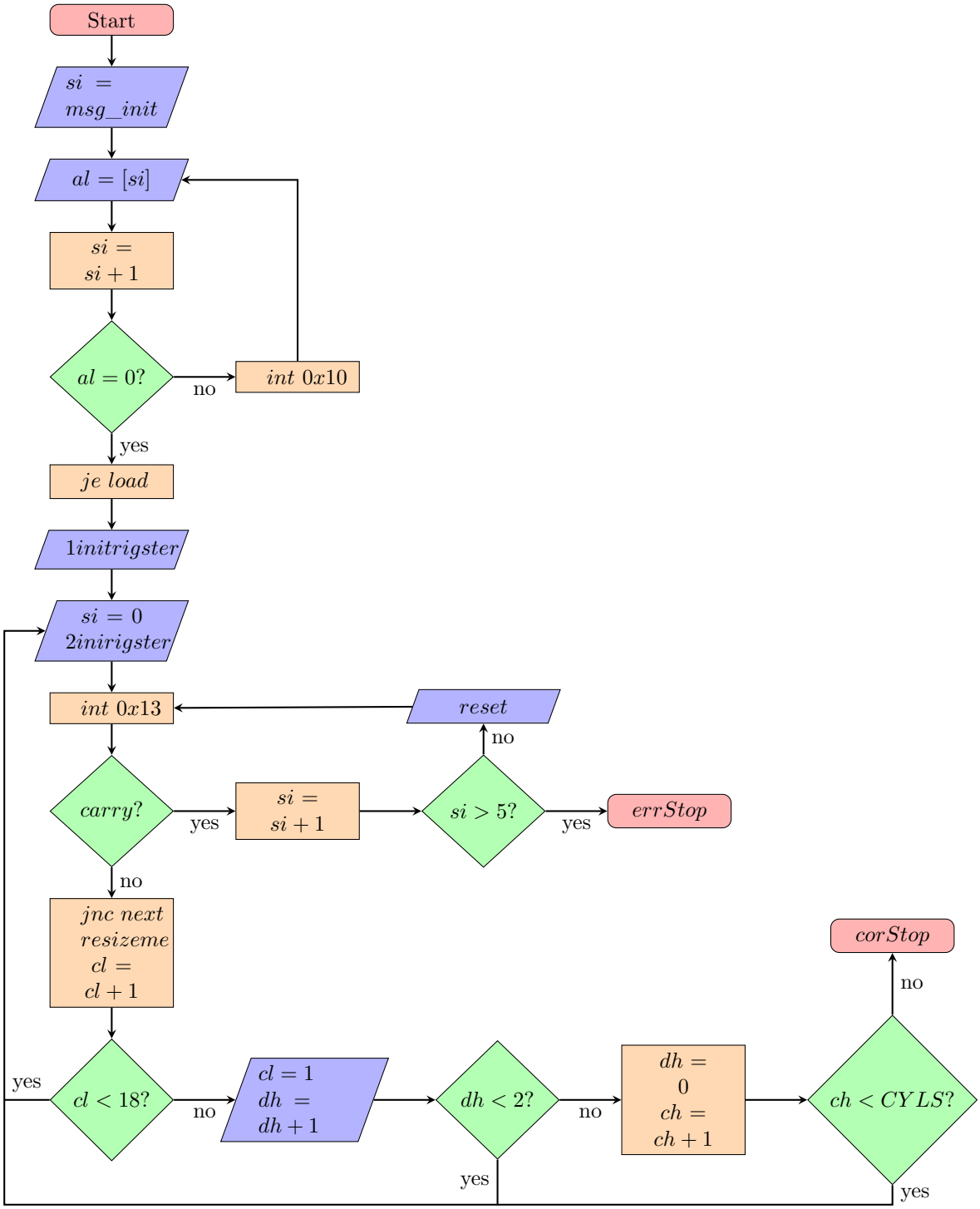

图 3-2 Flowchart of Boot Loader

Firstly, the boot sector display some boot information, when $al = 0$, the null character of boot information hit. Interrupt $0x10$ is used for show a character.

Then jump to load C0-H0-S2, ax register saved the address where beginning puts the sectors from floppy. And preparing parameters for interrupt $0x13$ in registers. The $0x13$ interrupt used for read sector from floppy to memory.

If there is a carry, representing some thing wrong when read floppy, so reset the registers and try again read floppy, until five times trying. Register si is a counter. If no carry, jump to next segmentation, as one sector read to memory already, the address space should increase 512 byte. Then sector number(cl register) added 1 and compare it to 18, if it's smaller than 18, jump to $readloop$, read the next sector. If the value of cl register bigger or equal to than 18, meaning that one track 18 sector in this side of floppy read already, then reversed the head, add 1 to dh register. If the value of dh register after adding larger than or equal to 2, it's saying the original head is 1, one track of two sides read already. Otherwise the value of dh register smaller than 2, read this side indicating by dh register, jump to $readloop$ segmentation.

So the next step is moving a cylinder, add 1 to register ch. Otherwise the value of dh register smaller than 2, read this side indicating by dh register, jump to $readloop$ segmentation. After ch register add 1, if it's smaller than 10, jump to $readloop$, otherwise end loading floppy to memory process, for we only load ten cylinders of floppy.

3.1.4 Codes and Comments of Boot Loader

```
1  ; read ten cylinders to memory begin with 0x8200.
2  ; read order:
3          ; C0-H0-S1 --- C0-H0-S18
4          ; C0-H1-S1 --- C0-H0-S18
5          ; C1-H0-S1 --- C1-H0-S18
6          ; C1-H1-S1 --- C1-H1-S18
7          ;          ...
8          ; C9-H1-S1 --- C9-H1-S18
9          ; C is cylinder, H is head, S is sector.
10         ; total 10 * 2 * 18 * 512 = 184320Byte = 180KB.
11         ; begin with 0x8200, end with 0x34fff in memory.
12
```

```
13
14          CYLS equ 10 ; read 10 cylinders,
15
16  org 0x7c00 ; load the program to address 0x7c00.
17          jmp entry
18          ; The next codes specify the format of standard FAT12 floppy disk.
19  db 0x90 ;db is the abbreation of "define byte", it literally places that byte
20          ; right there in the executable.
21  db "RONGBOOT" ;The name of boot sector, must be 8 byte.
22  dw 512 ; the size of every sector, must be 512 byte.
23  db 1 ; the size of cluster, must be 1.
24  dw 1 ; the start point of FAT, 1 general case.
25  db 2 ; the number of FAT, must be 2.
26  dw 224 ; the size of root directory, 224 in general.
27  dw 2880 ; the size of this floppy disk, must be 2880.
28  db 0xf0 ; the kind of disk.
29  dw 9 ; the length of FAT.
30  dw 18 ; how many sectors in one track, must be 18.
31  dw 2 ; the number of head, must be 2.
32  dd 0 ; no partion, must be 0.
33  dd 2880 ; the size if re-writer one time.
34  db 0,0,0x29 ; just fixed, no meaning.
35  dd 0xffffffff
36  db "RONGBOOTOS " ; the name of disk.
37  db "FAT12   " ; the name of disk formate.
38  resb 18 ; reserved 18 byte.
39          ; end FAT12 formate.
40
41  entry:
42          mov ax, 0 ; init the registers.
43          mov ss, ax ; can not directly write ss segment register.
```

```
44          mov sp, 0x7c00 ; the instructions of this program
45          ; loaded to 0x7c00 in memory, so sp=0x7c00, from here
46          ; to execute.
47
48          mov ds, ax
49
50          mov si, msg_init ; show some init message.
51          jmp init
52
53
54  init:
55          mov al, [si]
56          add si, 1 ; increment by 1.
57          cmp al, 0
58          je load ; if al == 0, jmp to load, the msg_init info displayed.
59  ; the lastest character is null character, coding in 0.
60
61          mov ah, 0x0e ; write a character in TTY mode.
62          mov bx, 15   ; specify the color of the character.
63          int 0x10 ; call BIOS function, video card is number 10.
64          jmp init
65
66  ;show some init messages.
67  msg_init:
68  db 0x0a ; new line
69  db 0x0d
70  db "Copyright: GPL"
71  db 0x0a
72  db 0x0d
73  db "Author: Qiyuan Pu"
74  db 0x0a
```

```
75  db 0x0d
76  db "https://github.com/Puqiyuan/RongOS"
77  db 0x0a
78  db 0x0d
79  db "IPL is loading, please waiting..."
80  db 0x0a
81  db 0x0d
82  db "......"
83
84
85  load:
86
87          mov ax, 0
88
89          mov ax, 0x0820 ; load C0-H0-S2 to memory begin with 0x0820.
90          mov es, ax
91          mov ch, 0 ; cylinder 0.
92          mov dh, 0 ; head 0.
93          mov cl, 2 ; sector 2.
94
95  readloop:
96          mov si, 0 ; si register is a counter, try read a sector
97          ; five times.
98
99  retry:
100         mov ah, 0x02 ; parameter 0x02 to ah, read disk.
101         mov al, 1 ; parameter 1 to al, read disk.
102         mov bx, 0
103         mov dl, 0x00 ; the number of driver number.
104         int 0x13 ; after prepared parameters, call 0x13 interrupted.
105
```

```
106         jnc next ; if no carry read next sector.
107         add si, 1 ; tring again read sector, counter add 1.
108         cmp si, 5 ; until five times
109         jae error ; if tring times large than five, failed.
110
111         ; reset the status of floppy and read again.
112         mov ah, 0x00
113         mov dl, 0x00
114         int 0x13
115         jmp retry
116
117 next:
118         mov ax, es
119         ; we can not directly add to es register.
120         add ax, 0x0020 ; add 0x0020 to ax
121         mov es, ax ; the memory increase 0x0020 * 16 = 512 byte.
122         ; size of a sector.
123         add cl, 1 ; sector number add 1.
124         cmp cl, 18 ; one track have 18 sector.
125         jbe readloop ; jump if below or equal 18, read the next sector.
126         mov cl, 1 ; cl number reset to 1, ready to read the other side.
127         add dh, 1 ; the other side of floppy.
128         cmp dh, 2 ; only two sides of floppy.
129         jb readloop ; if dh < 2, read 18 sectors of the other sides
130         ; of floppy.
131         mov dh, 0 ; after finished read the other side, reset head to 0.
132         add ch, 1 ; two sides of a cylinder readed, add 1 to ch.
133         cmp ch, CYLS ; read 10 cylinders.
134         jb readloop
135         jmp correct ; if 10 cylinders readed, show correct message.
136
```

```
137
138 fin:
139         hlt ; halt the cpu.
140         jmp fin
141
142
143 error:
144         mov si, msg
145
146
147 correct:
148         mov si, msg_corr
149
150
151 putloop:
152         mov al, [si]
153         add si, 1
154         cmp al, 0
155         mov [0x0ff0], ch
156         je 0xc200
157         mov ah, 0x0e
158         mov bx, 15
159         int 0x10
160         jmp putloop
161
162
163 msg_corr:
164 db 0x0a
165 db 0x0d
166 db 0x0a
167 db 0x0d
```

```
168 db "OK: IPL loaded"
169 db 0x0a
170 db 0x0d
171 db 0
172
173
174 msg:
175 db 0x0a
176 db "IPL load error"
177 db 0x0a
178 db 0
179 resb 0x7dfe-$
180
181
182 db 0x55, 0xaa ; the sector end with 0x55 0xaa, the sector is
183         ;boot sector.
```

3.2 32-bit Mode and Import C Codes

3.3 Screen Display and Text

3.4 Control Mouse

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3.16 Some Applications

3.17 Prospects and Shortages

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