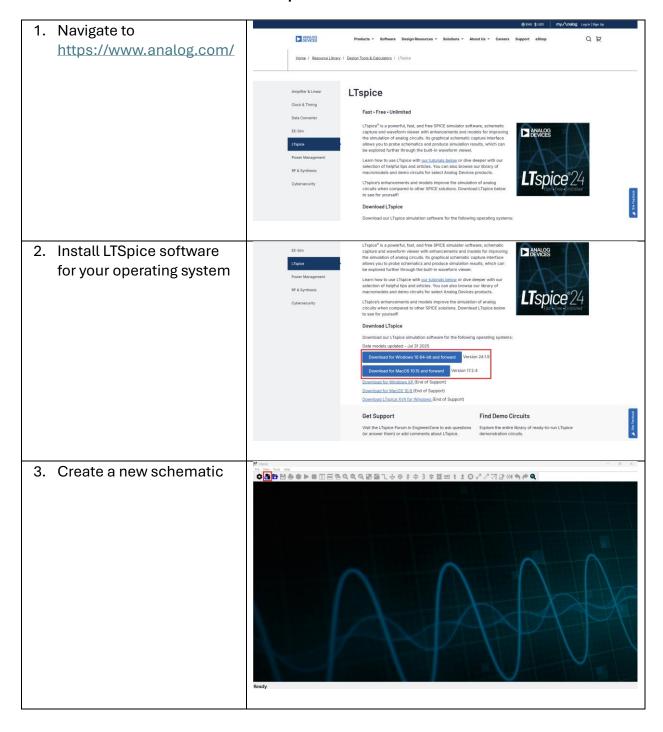
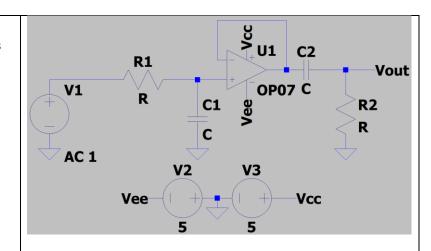
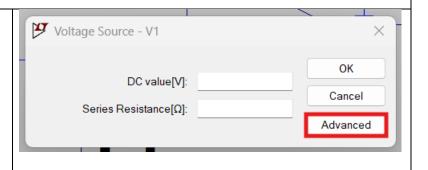
LTSpice Tutorial



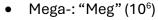
4. Construct the following circuit using the controls listed below



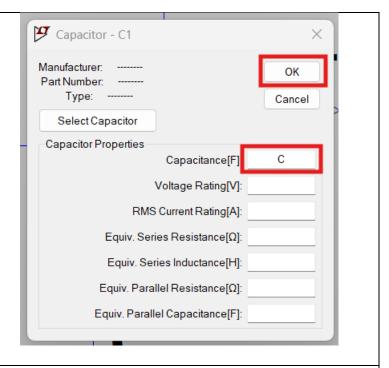
- "v" creates a voltage source
- "r" creates a resistor
- "c" creates a capacitor
- "g" creates a ground reference point
- "n" creates a net which can connect parts of your circuit with labels rather than wires
- "w" creates a wire
- "Ctrl+r" will rotate a selected component
- "esc" will stop whatever operation you are preforming
- "del" activates the deletion tool
- "Ctrl+c" activates the duplicate tool
- "p" opens the components menu
 - o Use the component labeled "OP07"
- "s" activates the selection tool
- Scrolling controls zoom
- Left click dragging moves the screen
- 5. Right click on the voltage source V1 (as depicted in the schematic in step 4) and click Advanced



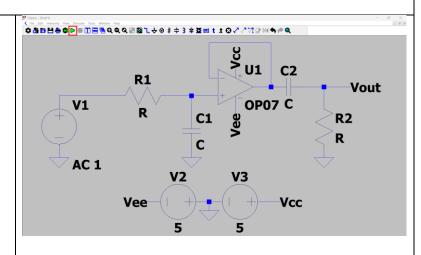
6. In the "Small Signal AC Independent Voltage Source - V1 Functions DC Value Analysis" section, set the (none) DC value AC Amplitude to "1" and OPULSE(V1 V2 Tdelay Trise Tfall Ton Period Ncycles) Make this information visible on schematic: SINE(Voffset Vamp Freg Td Theta Phi Novoles) then click Ok Small signal AC analysis(.AC) OEXP(V1 V2 Td1 Tau1 Td2 Tau2) AC Amplitude: ○ SFFM(Voff Vamp Fcar MDI Fsig) AC Phase: OPWL(t1 v1 t2 v2...) Make this information visible on schematic: OPWL FILE: Parasitic Properties Series Resistance[Ω] Parallel Capacitance[F]: Make this information visible on schematic: Additional PWI Points Make this information visible on schematic: 7. Set the other 2 voltage Voltage Source - V2 sources at 5V and click Ok OK DC value[V]: Cancel Series Resistance[Ω]: Advanced The cutoff frequency for a high and low pass filter is defined in the following equation. Use it to determine appropriate capacitor and resistor values for the 5kHz cutoff $f_c = \frac{1}{2\pi RC}$ 9. Enter these resistor and Resistor - R1 capacitor values into R1 and C1 from the Manufacturer: OK schematic in step (4) by Part Number: Cancel right clicking on the Select Resistor components, entering the Resistor Properties value into the appropriate Resistance[Ω]: box and clicking Ok Tolerance[%]: Note: You can use the Power Rating[W]: following postfixes for your numbers according to their order of magnitude



- Kilo-: "k" (10³)
- Milli-: "m" (10⁻³)
- Micro-: "u" (10⁻⁶)
- Nano-: "n" (10⁻⁹)
- Femto-: "f" (10⁻¹²)

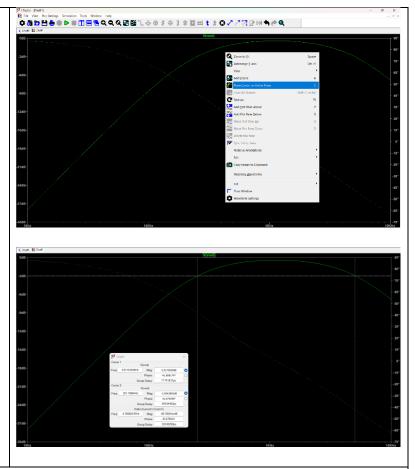


- 10. Use the equation from step (7) to determine appropriate capacitor and resistor values for a 250Hz cutoff
- 11. Enter the values calculated in step (9) into components R2 and C2 from the schematic in step (4)
- 12. Click the simulate button in the toolbar



13. Click on "AC Analysis" and Configure Analysis adjust the simulation Transient AC Analysis DC sweep Noise DC Transfer DC op pnt Transient Frequency Response parameters to match the ones depicted in the Compute the small signal AC behavior of the circuit linearized about its DC operating point. image on the right. Type of sweep: Decade Number of points per decade: Start frequency: Stop frequency: Syntax: .ac <oct, dec, lin> <Npoints> <StartFreq> <EndFreq> .ac dec 10 10 10k Cancel 14. After clicking Ok you should get the following graph. You may need to .ac dec 10 10 10K C2 click on the "Vout" net in R1 Vout the schematic. **OP07** C ۷1 R2 AC 1 **V3** Vee Vcc 5

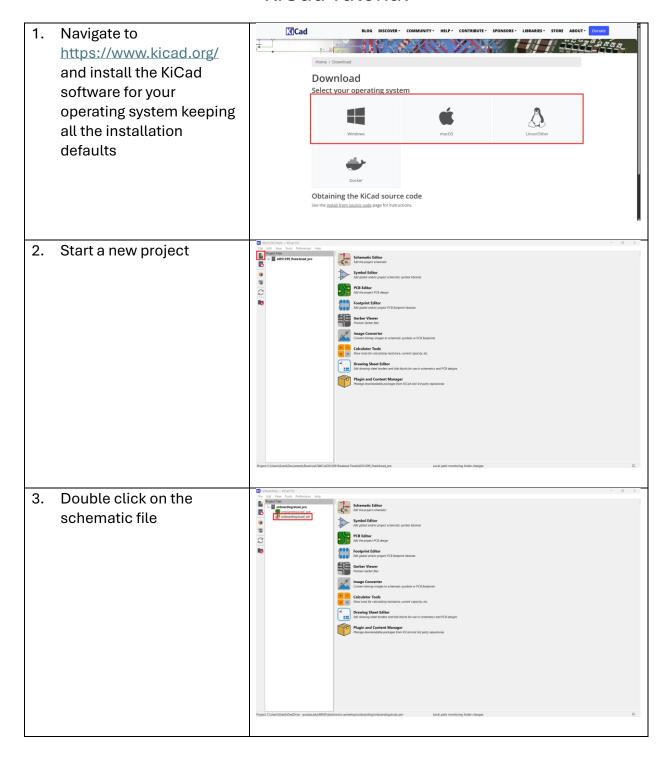
15. Add 2 cursors by right clicking on the graph and selecting <u>Place Cursor on Active Trace</u>. You can adjust the x-position of the cursors using the arrow keys



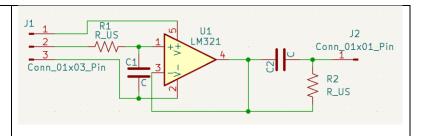
16. Ensure that the graph shows a -3db attenuation at 250Hz and 5kHz. If this is not the case, recheck your calculations for the resistor and capacitor values. If you still can't figure out the issue, reference the LTSpice video tutorial.

LTSpice Tutorial Video

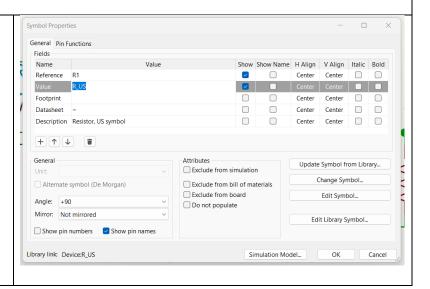
KiCad Tutorial

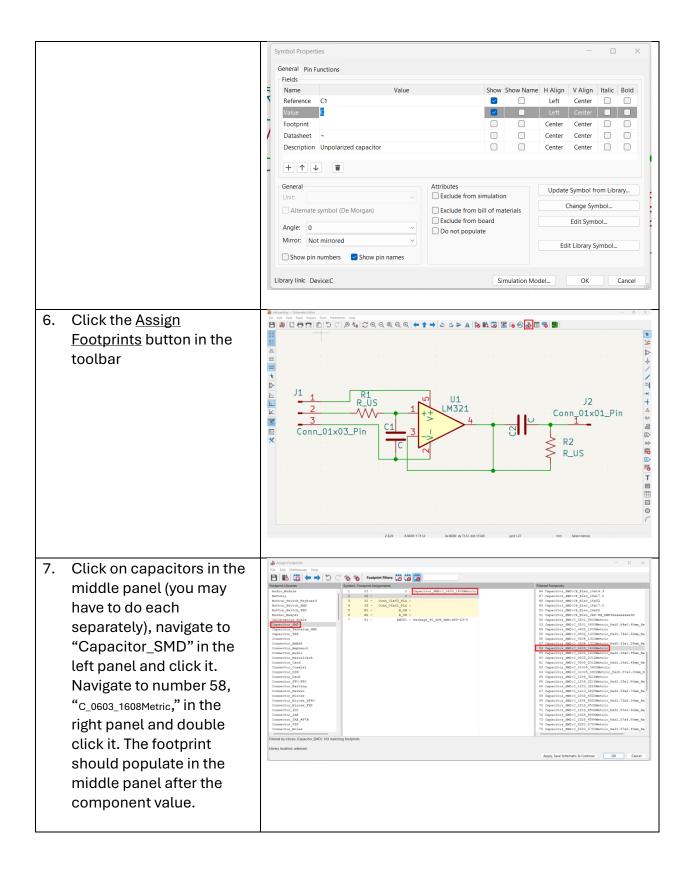


4. Construct the following circuit using the controls listed below

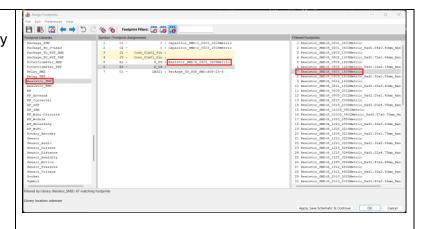


- "w" will create a wire, the terminals of the components can also be clicked to connect components
- "r" will rotate the selected component
- "Ctrl+d" will duplicate a selected component
- Right click dragging will move the screen
- "esc" will stop whatever operation you are preforming
- "a" will open a menu with all the parts
- Clicking on a component/wire and pressing "del" will delete the component/wire
- For resistors use the symbol "R_US"
- For capacitors use the symbol "C"
- For the 1x3 connector use the symbol "Conn_01x03_Pin"
- For the 1x1 connector use the symbol "Conn 01x01 Pin"
- For the op amp use the symbol "LM321"
- 5. Enter the resistor and capacitor values you found in the LTSpice tutorial, into the KiCad schematic by double clicking on the components





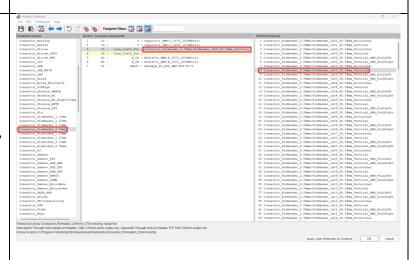
8. Click on the resistors in the middle panel (you may have to do each separately), navigate to "Resistors_SMD" in the left panel and click it. Navigate to number 7, "R_0603_1608Metric," in the right panel and double click it. The footprint should populate in the middle panel after the component value.



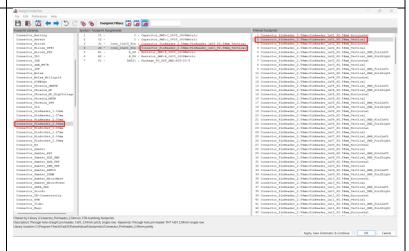
9. Click on the 1x3
connector in the middle
panel, navigate to

"Connector_PinHeader_2.54mm," in
the left panel and click it.
Navigate to the number 8,

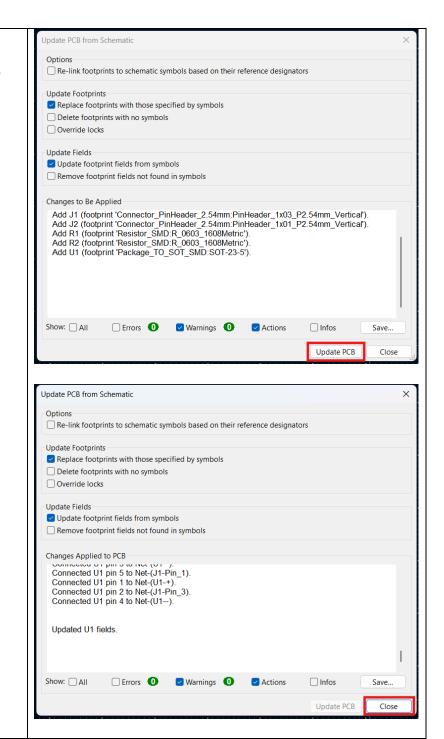
"PinHeader_1x03_P2.54mm_Vertical,"
in the right panel and
double click it. The
footprint should populate
in the middle panel after
the component name.



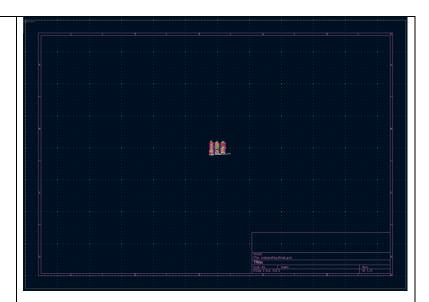
10. Click on the 1x3
connector in the middle
panel, navigate to
"Connector_PinHeader_2.54mm," in
the left panel and click it.
Navigate to the number 8,
"PinHeader_1x01_P2.54mm_Vertical,"
in the right panel and
double click it. The
footprint should populate
in the middle panel after
the component name.
Press Ok when finished.



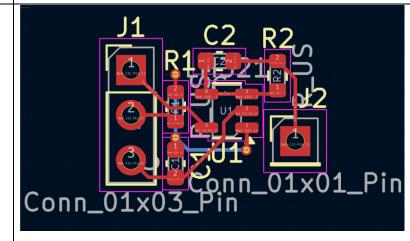
11. Press F8 to create your PCB and click "Update PCB" then click "Close"



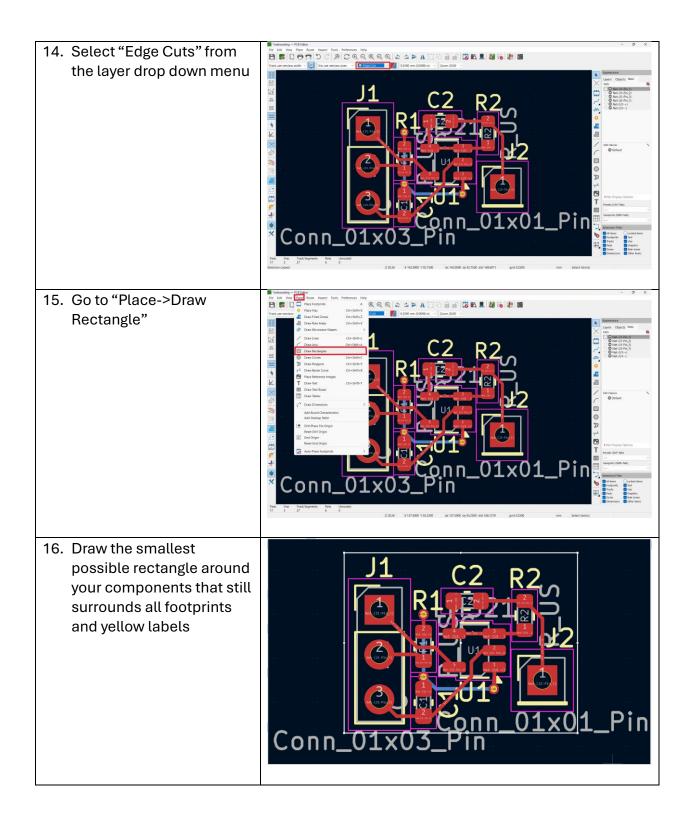
12. Left click to place the group of components in the middle of the screen.

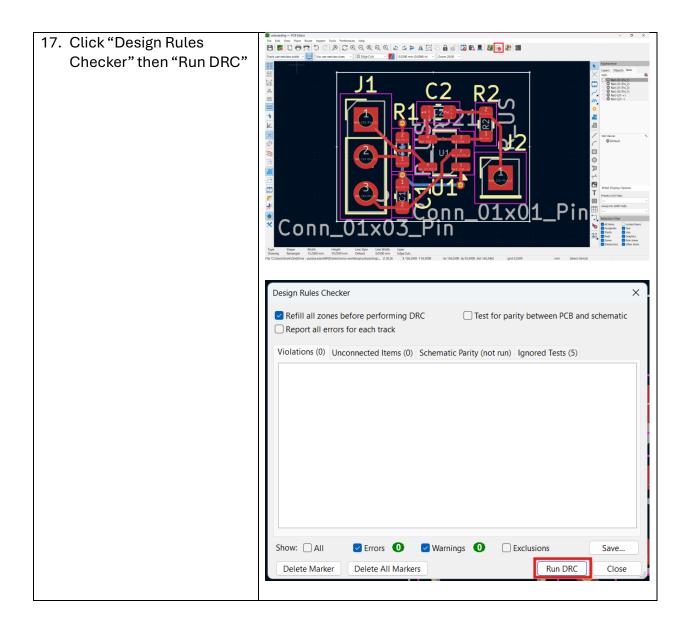


13. Arrange the components in the compact way possible using the following controls. One arrangement example is depicted in the picture on the right



- "x" will create traces (wires) between components
- "v" will create a via which switches the side the trace is on, allowing them to cross
- "r" will rotate the selected component
- Right click dragging will move the screen
- "esc" will stop whatever operation you are preforming
- Clicking on a /trace and pressing "del" will delete the trace
- Do not delete components
- Traces of the same color/level can't cross
- The pink borders of different components can't cross





18. Verify that you get 0 errors Design Rules Checker and 0 warnings. If not, ☐ Test for parity between PCB and schematic Refill all zones before performing DRC recheck your schematic Report all errors for each track and PCB layout. If it still Violations (0) Unconnected Items (0) Schematic Parity (not run) Ignored Tests (0) isn't working, reference the video tutorial. https://youtu.be/eTiLhKjf7fY Errors 0 Show: All Exclusions Run DRC Delete Marker Delete All Markers Close 19. Once there are no errors Design Rules Checker or warnings, click "Close" Refill all zones before performing DRC ☐ Test for parity between PCB and schematic Report all errors for each track Violations (0) Unconnected Items (0) Schematic Parity (not run) Ignored Tests (0) Show: All Exclusions Save... Run DRC Delete Marker Delete All Markers Close 20. Press "Alt+3" to see a 3D model of your PCB