

Roll No.

Total No. of Questions : 09]

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B. Tech. (Sem. - 5th)

COMPUTER PERIPHERALS AND INTERFACES

SUBJECT CODE : CS - 311

Paper ID : [A0469]

[Note : Please fill subject code and paper ID on OMR]

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Time : 03 Hours

Maximum Marks : 60

Instruction to Candidates:

- 1) Section - A is **Compulsory**.
- 2) Attempt any **Four** questions from Section - B.
- 3) Attempt any **Two** questions from Section - C.

Section - A

Q1)

(10 × 2 = 20)

- a) Compare fire wire with USB.
- b) What is scan code for keyboard?
- c) What is graphics accelerator and where it is used?
- d) What is EISA and what is its function?
- e) What is termination?
- f) Differentiate between SCSI-I and SCSI-II
- g) What is bus mastering? How it differs from DMA
- h) Write main components of the video subsystem PC
- i) What is bus arbitration?
- j) Comment on 'Monochrome versus colour' monitors?

Section - B

(4 × 5 = 20)

- Q2)** Explain what are the steps to be taken up to reduce the cost while designing the system.
- Q3)** How many devices can be interfaced to IDE and EIDE standard interface? Compare the characteristics.
- Q4)** Differentiate between (a) VGA card and SVGA card (b) CGA and MCGA.
- Q5)** Describe the problem that occurs when you attempt to connect together two RS-232 –C devices that are both configured as DTE. Draw a diagram which shows how this problem can be solved.
- Q6)** What is an interrupt? Which device is used to handle the interrupt in a computer system? Write down the IRQ assignment in the PC/XT and PC/AT.

Section - C

(2 × 10 = 20)

- Q7)** (a) Explain features of UNIX device driver.
(b) Describe the function & direction of the following signals in centronics parallel interface.
1) STROBE 2) BUSY 3) ACKNLG 4) INIT 5) AUTO FEED XT.
- Q8)** What are the steps involved in design and integration of peripheral devices to computer system? Take many peripheral devices as an example to illustrate the concept.
- Q9)** Write short note on followings:
(a) Programmable I/O Ports .
(b) Programmable interrupt controller.
(c) Programmable peripheral interface .
(d) Programmable DMA controller.

