

TEST CASE [HIGH LEVEL]:

ID	Description	Input	Output	Status
H_01	Generate	C file	Hex File	Implemented
H_02	Assemble Components	Search	Present in simulIDE	Implemented
H_03	Make circuit	Components	circuit	Implemented
H_04	Show time	Run	Displayed	Implemented

TEST CASE [LOW LEVEL];

ID	Description	Input	Output	Status
L_01	Change temperature	Change voltage	As needed	Implemented

OUT PUT:



