

ANALOG LAB 2022

EE2401

Experiment 6: PCB design

1. Design a PCB for the voltage-controlled oscillator implemented in Experiment 5.
 - Two layer PCB
 - 2-pin connectors for supply, input and output
 - Use only one TL074 (same symbol and footprint as LF347) IC with four opamps
 - Through-hole package for all the components
 - DRC rule: Minimum track width and spacing - 10 mils, minimum via hole - 10 mils

CAD info:

- Getting started with KiCAD: https://docs.kicad.org/5.1/en/getting_started_in_kicad/getting_started_in_kicad.html
- Footprints: 2 pin banana jack for connectors, DIP14 (W=7.62mm) for LF347
- Capacitor types: https://www.electronics-tutorials.ws/capacitor/cap_2.html
- Resistor types: https://www.electronics-tutorials.ws/resistor/res_1.html

Submit the following:

- Schematic and layout (both layers individually) snapshots
- DRC report
- Snapshot of 3D view