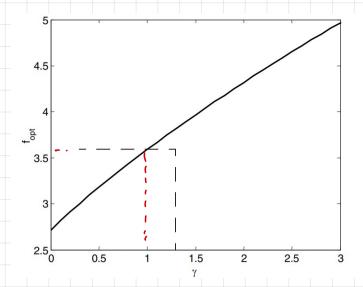
VZSI Design - Lecture 10

29 Ang 2022

$$tp = P t_{po} \left(1 + \frac{1}{2} \right) \qquad C_{j} = \sqrt{C_{j-1}} C_{j+1}$$

Roth can be suigned.

$$F = \frac{C_{L}}{C_{1}} = \int_{0}^{N} N = \frac{1}{2} \int_{0}^{1} \frac{1}{2} \int_{$$



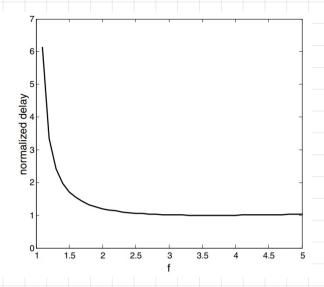
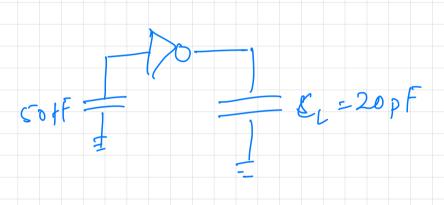
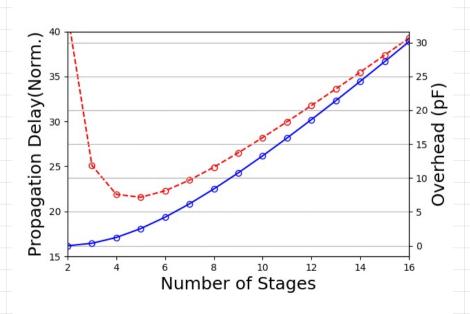


Table 5.3 t_{opt}/t_{p0} versus x for various driver configurations.

F	Unbuffered	Two Stage	Inverter Chain
10	11	8.3	8.3
100	101	22	16.5 🔀
1000	1001	65	24.8 5
10,000	10,001	202	33.1 7





CL = 20pF

