

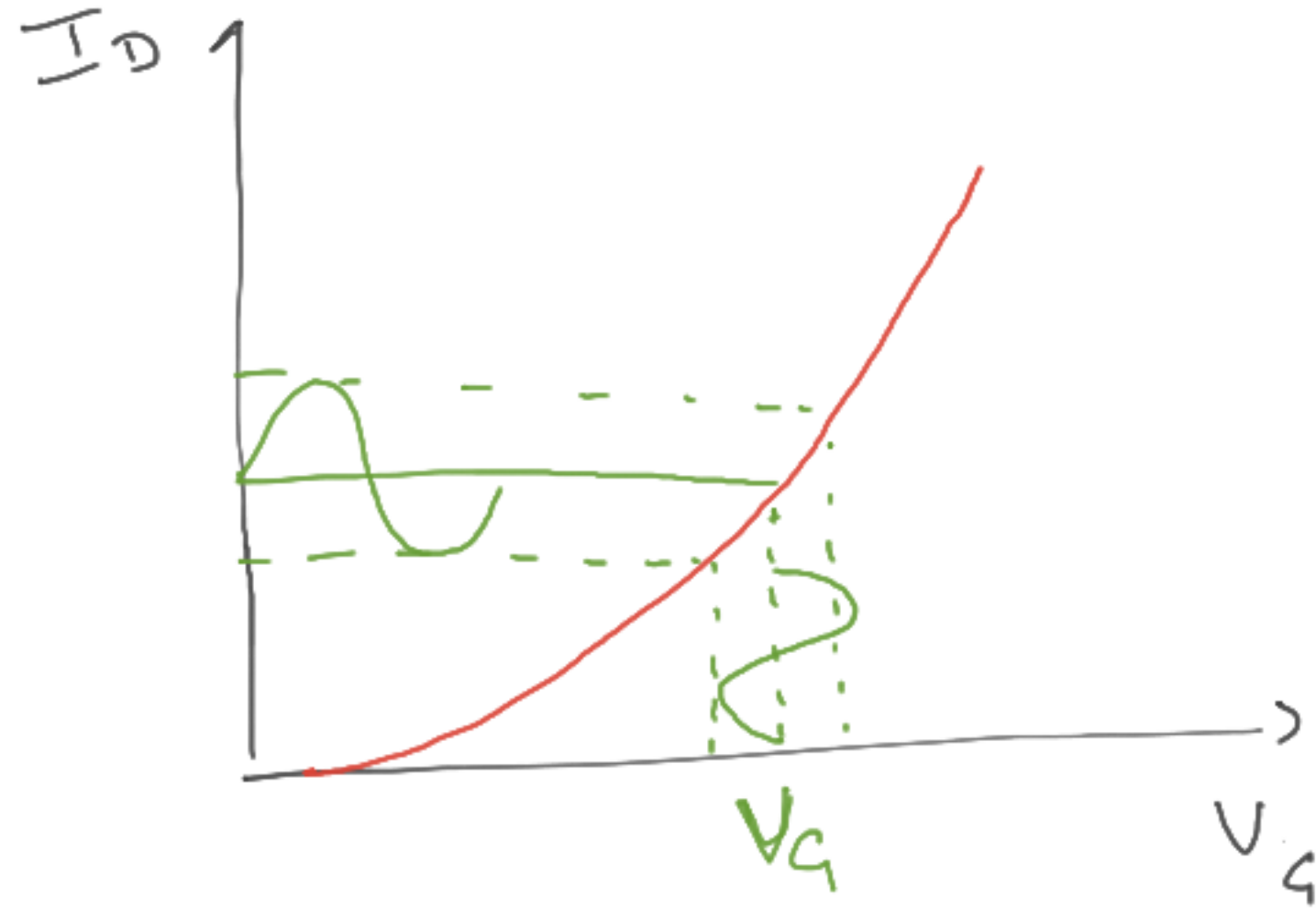
Design a CS amplifier such that it satisfies the following parameters use NMOS

$$G_{\text{noisy}} \left\{ \begin{array}{l} I_{DQ} = 2 \text{ mA} \\ \omega = 10 \mu \\ f_{in} = 2 \text{ kHz} \end{array} \right. \quad \begin{array}{l} I_{Dt} = 4 \text{ mA} \\ L = 0.5 \mu \\ V_{in \text{ peak}} = 100 \text{ mV} \end{array} \quad V_{DD} = 12 \text{ V} \quad R_{in} = 42 \text{ k}\Omega$$

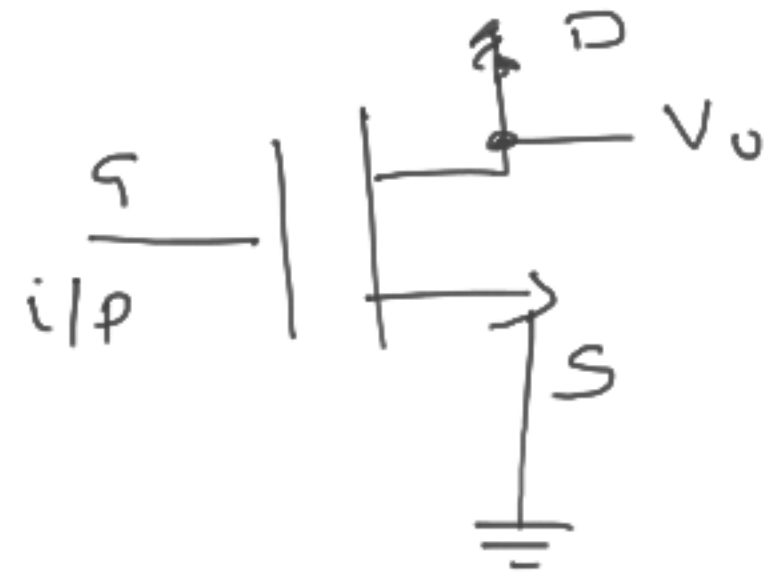
$$G_2 \left\{ \begin{array}{l} I_{DQ} = 1.4 \text{ mA} \\ \omega = 10 \mu \\ f_{in} = 2 \text{ kHz} \end{array} \right. \quad \begin{array}{l} I_{Dt} = 2.8 \text{ mA} \\ L = 0.5 \mu \\ V_{in \text{ peak}} = 100 \text{ mV} \end{array} \quad V_{DD} = 12 \text{ V} \quad R_{in} = 100 \text{ k}\Omega$$

Use basic MOSFET model $V_{TO} = 1 \text{ V}$
 $R_{is} = 0 \Omega$ Also calculate the gain of the amplifier.

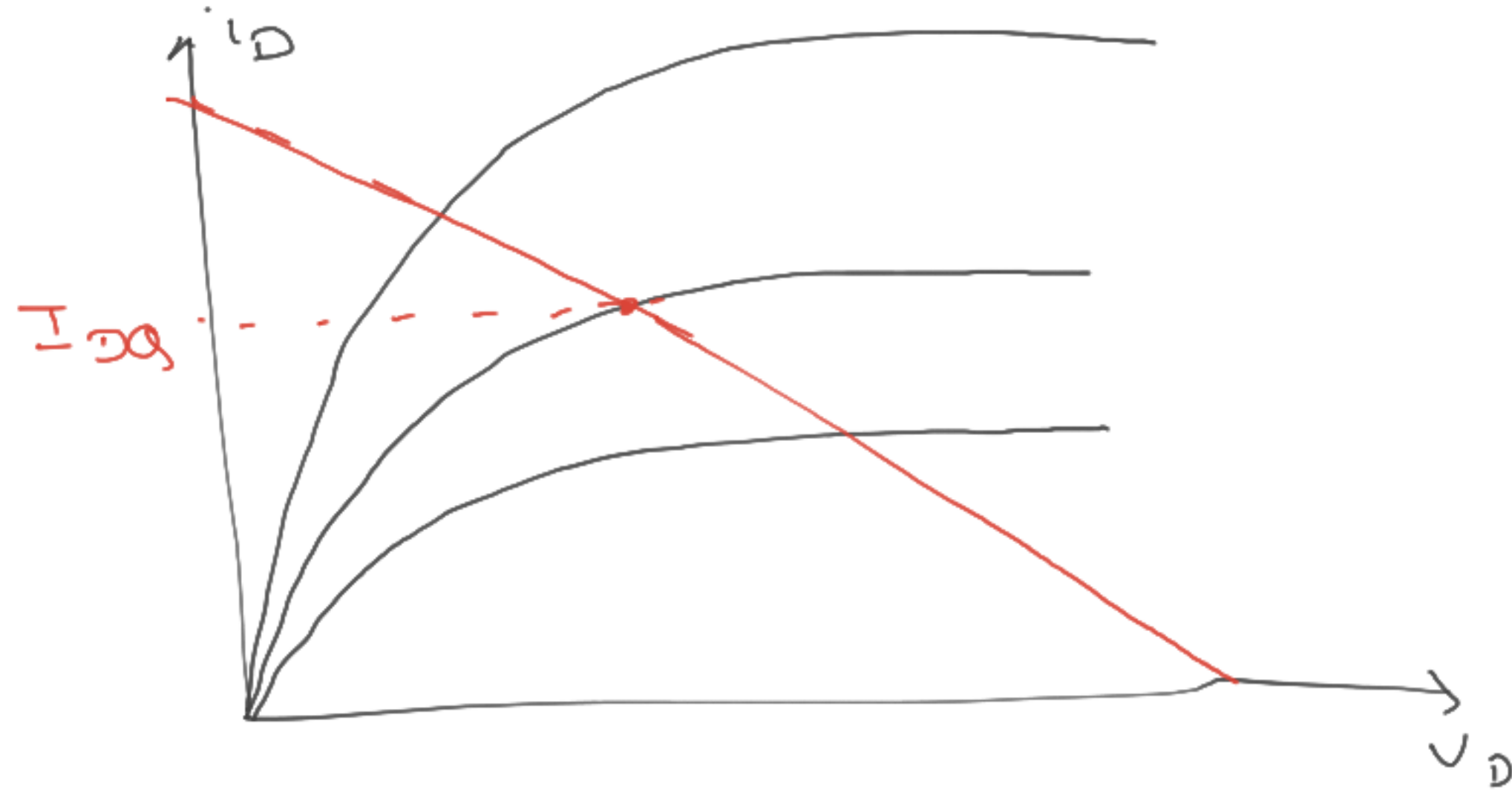
Transfer characteristic



$$i_D = \underbrace{g_m}_{\substack{\uparrow \\ \text{transconductance}}} v_g$$



Output Characteristics



$$v_{gs} = V_{GSQ} + v_{gs}$$

$$i_D = K_n (v_{gs} - V_{TO})^2$$

$$\downarrow$$

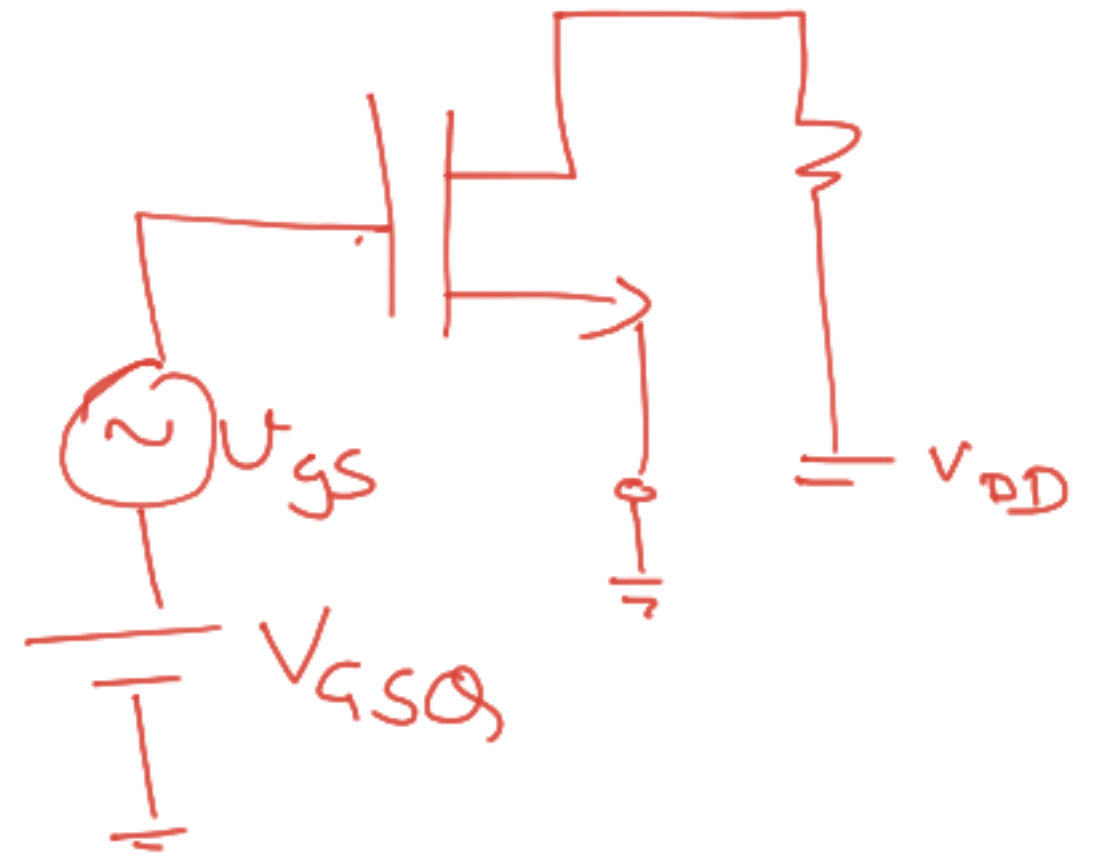
$$\frac{1}{2} \mu_n C_{ox} \frac{W}{L}$$

$$= K_n [V_{GSQ} + v_{gs} - V_{TO}]^2$$

$$= K_n [V_{GSQ} - V_{TO}]^2 + 2 K_n [V_{GSQ} - V_{TO}] v_{gs} + \cancel{K_n v_{gs}^2}$$

$$= I_{DSQ} + 2 K_n (V_{GSQ} - V_{TO}) v_{gs}$$

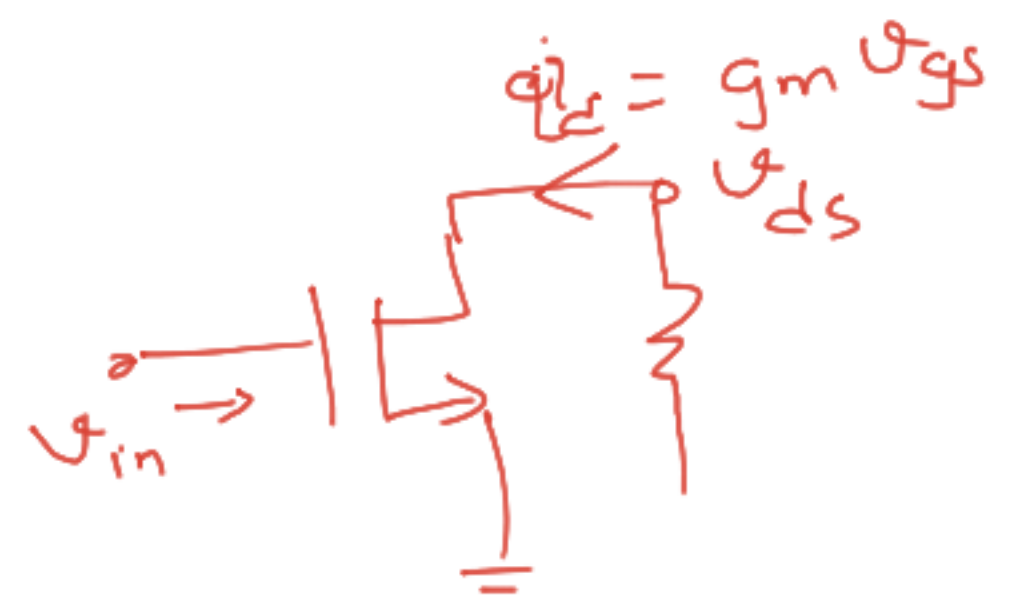
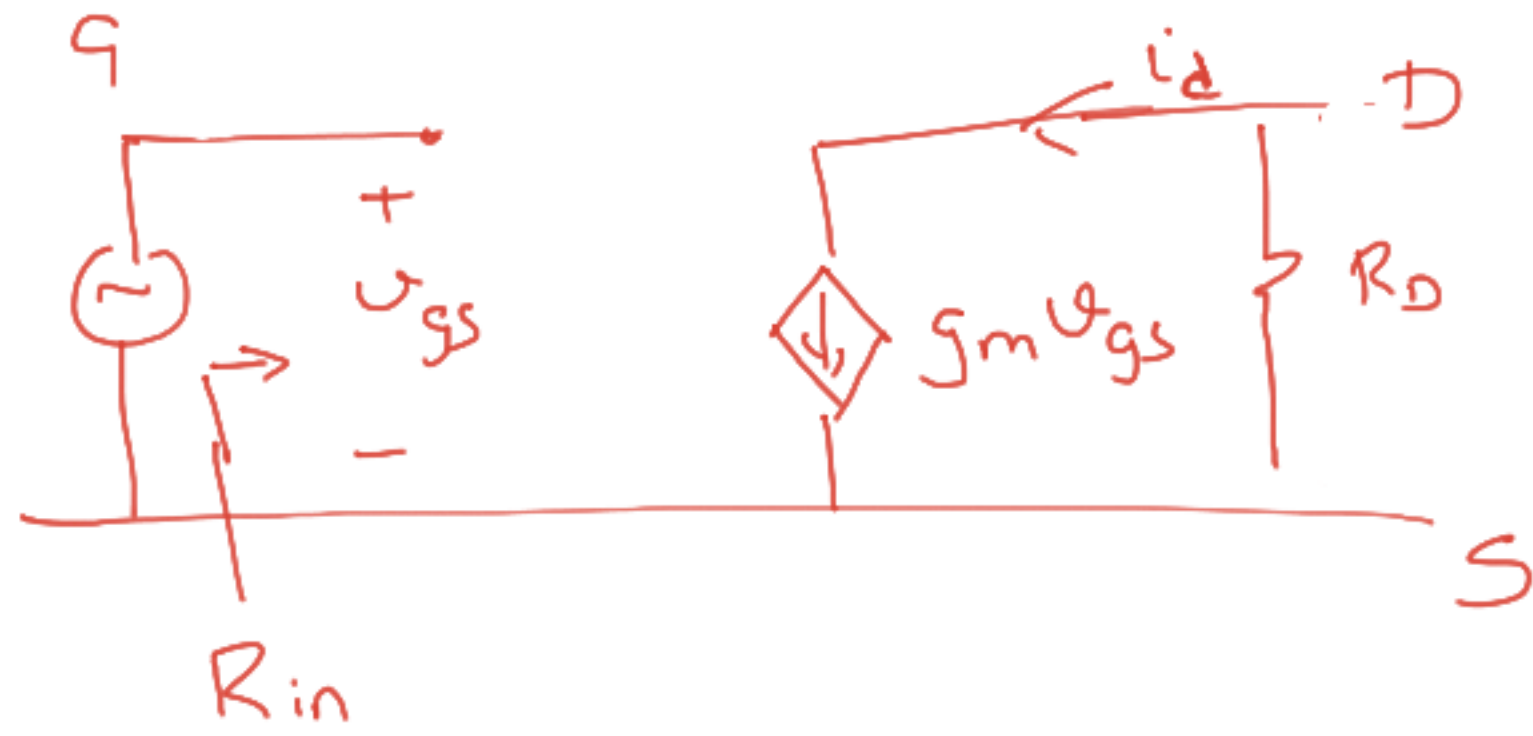
$$= I_D + i_d$$



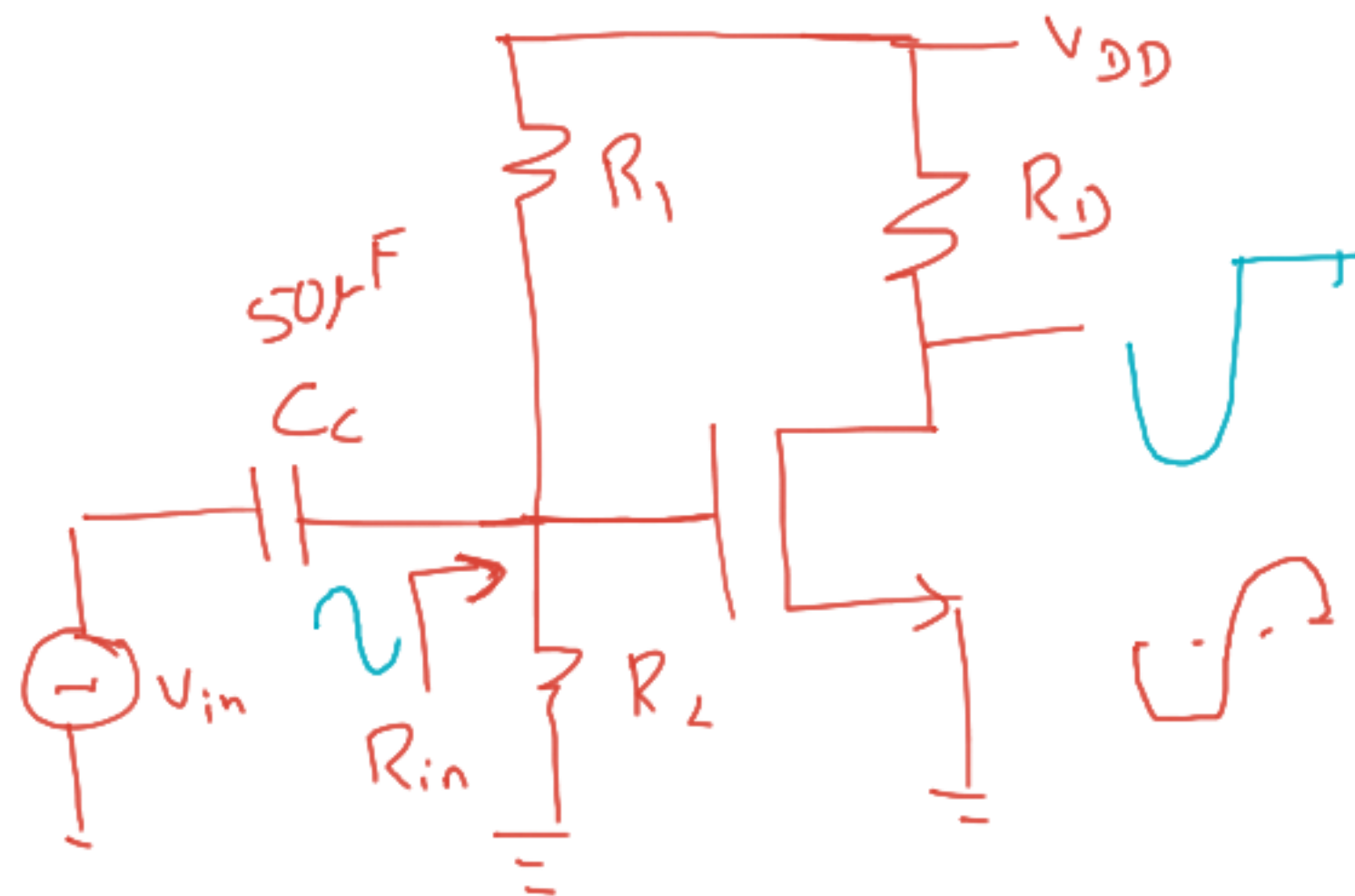
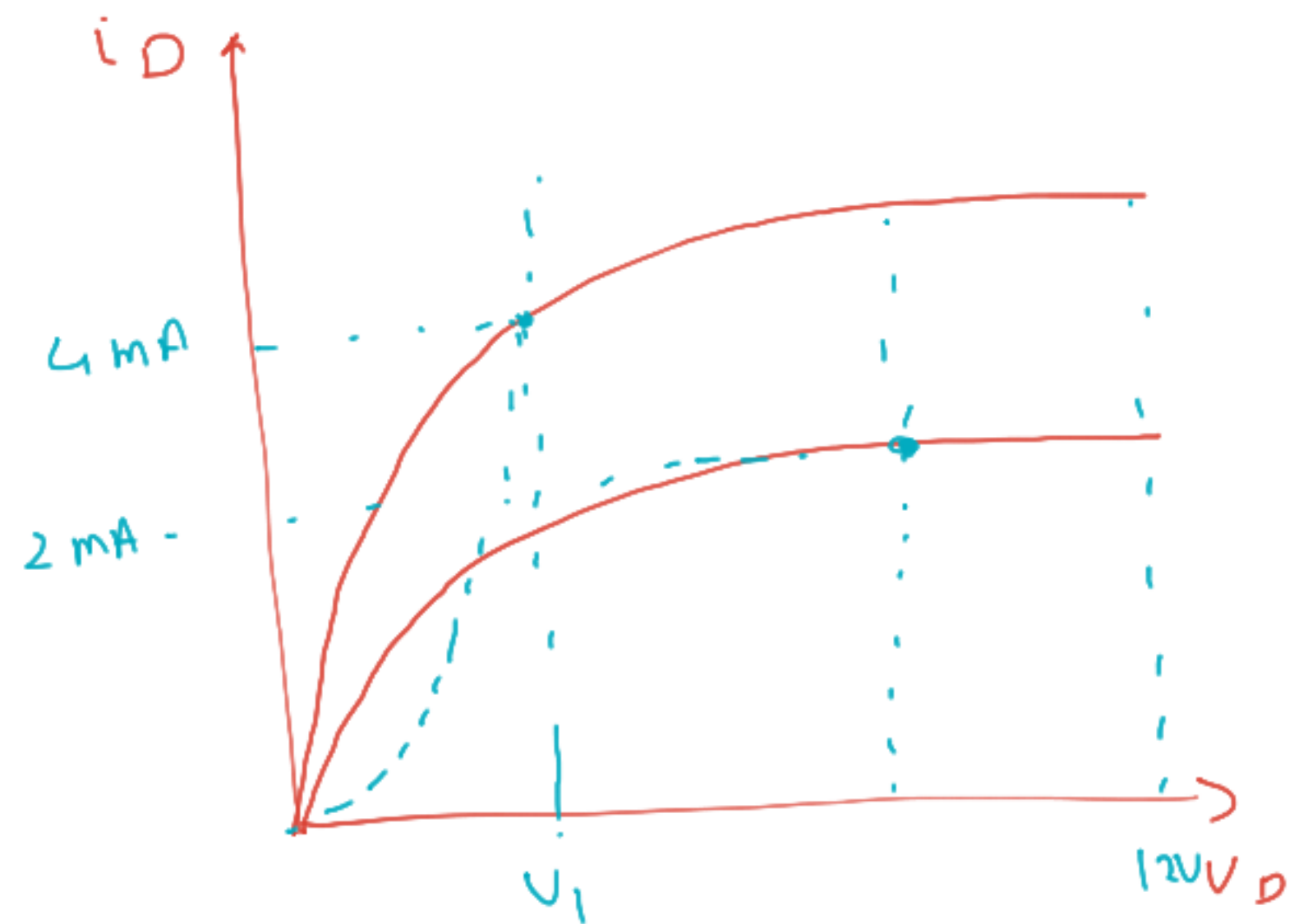
$$i_d = \underbrace{2 k_n (V_{GSQ} - V_{TO})}_{g_m} v_{gs}$$

$$g_m = \frac{i_d}{v_{gs}}$$

$$g_m = 2 \sqrt{k_n I_{DSQ}}$$



$$I_{DQ} = 2 \text{ mA}$$



$$V_{out} = g_m v_{gs} R_D$$

$$A_v = \frac{V_{out}}{v_{gs}} = g_m R_D$$

