Analog Lab 2022 EE2401

Experiment 3: PCB design

- 1. Design a PCB for the g_m -C filter implemented in Experiment 2.
 - Two layer PCB
 - 2-pin connectors for supply, input and output
 - Use only one CD4069 IC with six inverters
 - Through-hole package for all the components

CAD info:

- Getting started with KiCAD: https://docs.kicad.org/5.1/en/getting_started_in_kicad/getting_started_in_kicad.html
- Footprints: 2 pin banana jack for connectors, DIP14 (W=7.62mm) for 4069
- Capacitor types: https://www.electronics-tutorials.ws/capacitor/cap_2.html
- Resistor types: https://www.electronics-tutorials.ws/resistor/res_1.html

Submit the following:

- Schematic and layout (both layers individually) snapshots
- Snapshot of 3D view

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