## Experiment 7 EE2401 akumar@ee.iith.ac.in

- Design a buck-converter to meet the following requirements:
  - \* Switching freg = 1MHz \* Load -> 10052 in parallel with 100mA

\* Input supply = 5V, Output supply = 2.5V

- \* Output voltage ripple < 10mV geak-to-peak
  - \* Quality factor of L-C filter = 3
  - Submit the following:—
    (a) Mand calculation to find the component values.
- (b) Simulation test-bench in LTspice.
- (c) Transient simulation result showing initial transient in the output voltage.
- (d) Steady-State output voltage indicating ripple.