Sandeepani

CoreEL Technologies
Enabling Excellence

Certificate of Participation

This is to certify that

Pushpal Das

of

SRM Institute of Science and Technology

has successfully completed the Internship Program on

Functional Verification using SystemVerilog

held from 27th June to 26th July 2022, from

Sandeepani School of Embedded System Design, Bangalore.

Sadiya

Ms. Sadiya Arshad
Director Education and Skill Development

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