

Message Integrity HMAC-SHA-256

1. Alice computes verifier $v := f(m)$
2. Alice $m \xrightarrow{a, k} \text{Mallory } m' \xrightarrow{v'} \text{Bob}$
3. Bob verifies $v' = f(m')$

pseudorandom functions (PRF)

Start with 2^n functions: f_0, \dots, f_{2^n-1} .

Verify with k , only know to Alice and Bob, $f_k()$

Security definition:

1. Choose a secret k and random function $f(\cdot)$.

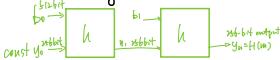
$$\xrightarrow{k} b_0, h_0 = f_0(\cdot)$$

2. Flip coin $\xrightarrow{k} b_1, h_1 = f_k(\cdot)$

3. Mallory choose k , he announces $h(x)$, Mallory gets b

Hash failures: MD5, SHA-1

Merkle-Damgård (MD) construction



Length-Extension Attack.

$m = \text{original message} + \text{original padding}$
 $m' = \text{original message} + \text{original padding} + \text{modified message}' + \text{padding}'$

Solution: HMAC
 $\text{HMAC}(m) = H(R \oplus C \| H(H(C \| m)))$

Randomeess and Pseudorandomness

Pseudorandom Generator (PRG)

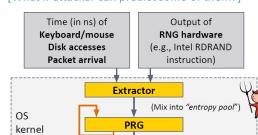
Getting Randomness

Randomness is an input to your program

Typically provided by the OS, via special APIs

OS continuously gathers inputs from physical sources that are hard for adversary to predict, "extracts" uniform bits from them.

[What if attacker can predict some of them?]



Confidentiality AES-128 in CTR mode

One-time pad $p_i := c_i \oplus k_i$ * No reuse

Use PRG: $s_i := g(k_i)$, $c_i := p_i \oplus s_i$

$s_i := g(c_i)$, $p_i := c_i \oplus s_i$ * No reuse k. output

AES Advanced Encryption Standard

Padding: M: MM MM MM MM, a block of 0f

Cipher-block chaining (CBC) mode

"Chains" ciphertexts to obscure later ones

Choose a random initialization vector IV

Encrypt: $c_i := IV; c_i := E_k(p_i, c_{i-1})$

Decrypt: $p_i := D_k(c_i) \oplus c_{i-1}$

[Why do we need the IV?]

Have to send IV with ciphertext

Can't encrypt blocks in parallel or out of order

Caution: Never reuse nonce for same k!

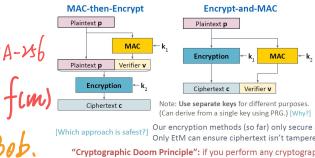
Initialization Vector (IV)

Plaintext

Key

Block cipher encryption

Ciphertext



Note: Use separate keys for different purposes.

Our encryption method (1) only secure against passive eavesdroppers.

Only CPU can compute ciphertext isn't tampered with before decryption.

"Cryptographic Doom Principle": if you perform any cryptographic operation on a message you've received before verifying the MAC, it will somehow inevitably lead to doom

Common flaw when using MAC-then-Encrypt

Suppose an implementation uses ECB mode.

Decryption involves the following steps:

1. $m \xrightarrow{a, k} \text{Ciphertext } c$
2. Check that pad is valid PKCS7.
3. Check that $v = MAC(m)$, else raise MacError

This is how TLS 1.0 worked. Seems reasonable?

Any method to distinguish these two error types (even tiny timing differences) leaks the plaintext!

Possible attack: Mallory submits any ciphertext and learns if last bytes of plaintext are a valid pad

Galois/Counter Mode (GCM)

Most widely used AEAD cipher mode

Developed by McGrew and Viega in 2004

Standardized by IETF, NIST, others

Non-generic composition (what kind?) of AES in CTR mode for encryption and a special MAC based on polynomials over finite ("Galois") fields.

Note: GCM violates principle of key separation [How?]
 (We can prove it's ok, but it's delicate.)

Warning: Can construct GCM ciphertexts that decrypt (differently, but without error) under many keys

Prov. Grubbs crafted one that decrypts under 131,072 different keys

Preferred modern approach:

Authenticated encryption with associated data (AEAD)

Integrity and encryption in a single primitive:

$c, v \xrightarrow{k, p, \text{associated_data}}$ encrypts plaintext p and returns ciphertext c and a verifier v (called a "tag").
 $p, err := Unseal(k, c, v, \text{associated_data})$ returns p or an error if v does not match the supplied k and associated_data

Optional associated_data is covered by verifier but not encrypted.

Useful for binding data to its context:
 e.g., counter, sender ID, etc.

AES-GCM ("Galois Counter Mode") hardware accelerated in recent CPUs

ChaCha20-Poly1305, common on mobile

Examples:

AES-GCM ("Galois Counter Mode") hardware accelerated in recent CPUs

ChaCha20-Poly1305, common on mobile

carriage return line feed indicates end of message

header lines

request line

header lines

data

status line

header lines

<p