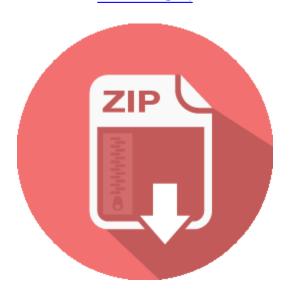
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Typos found in the 3 rd Edition of CMOS Circuit Design, Layout, and Simulation, ISBN 13: 978-0-470-88132-3, published August, 2010. How to tell the printing number is found here. Typos found in the 4 th, and later, printings of the 3 rd Edition

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