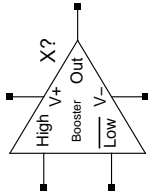
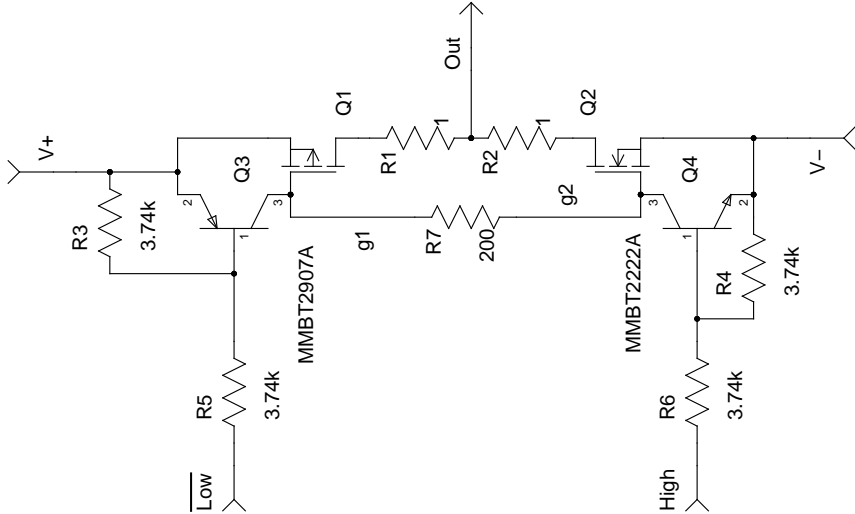


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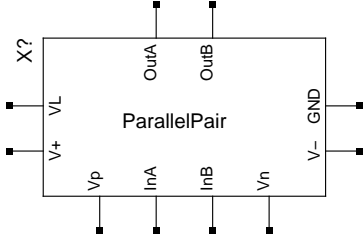
.subckt Booster %up

TITLE	Parallel clock current booster		
FILE:	Booster.sch		
DATE	Mon May 18 11:09:49 2015 -0600	REVISION:	FPE-6.1-RR7
PAGE	1	OF	1
		DRAWN BY:	jpd

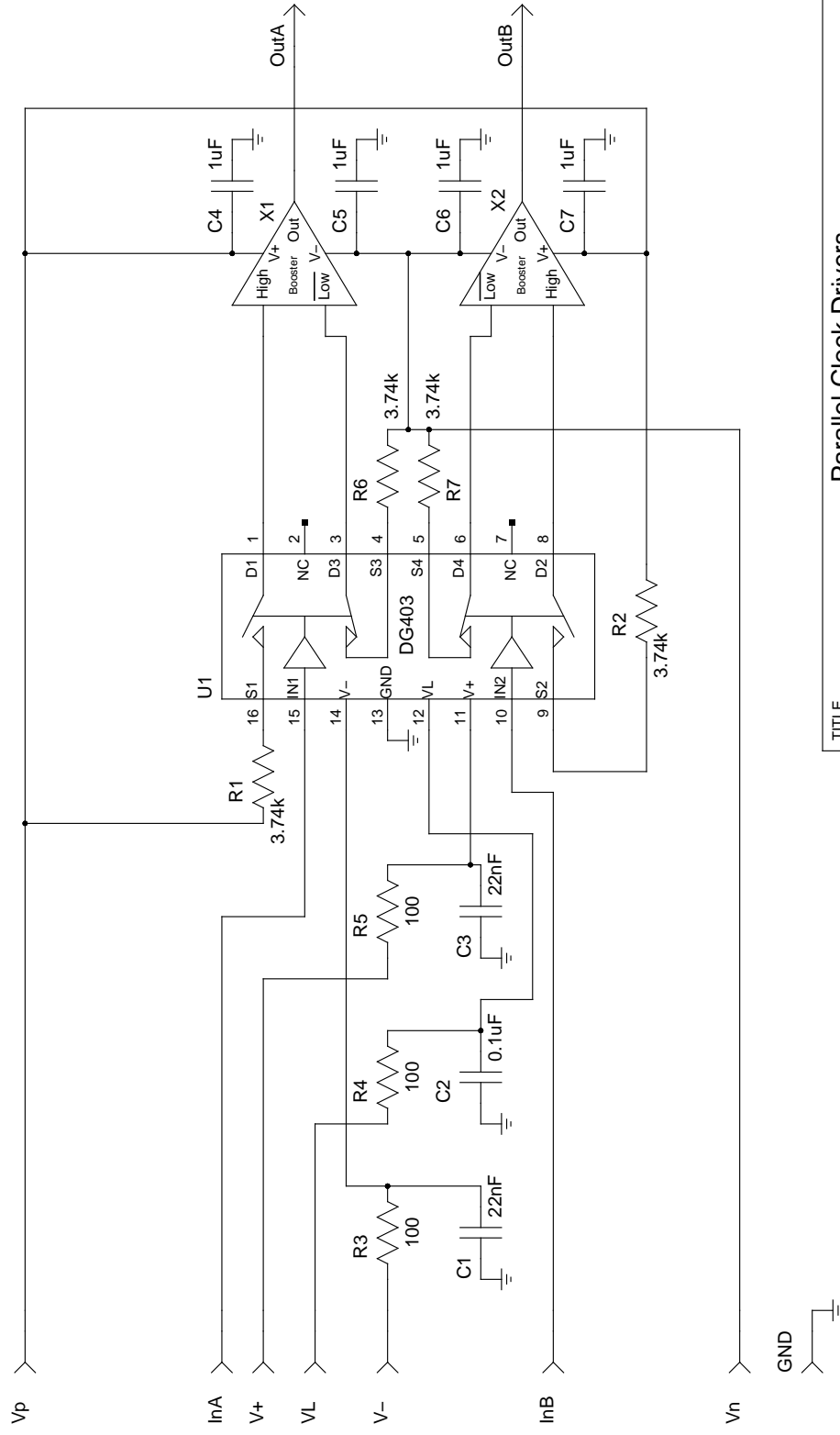
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.subckt ParallelPair %up



## Parallel Clock Drivers

TITLE ParallelPair.sch

FILE: ParallelPair.sch

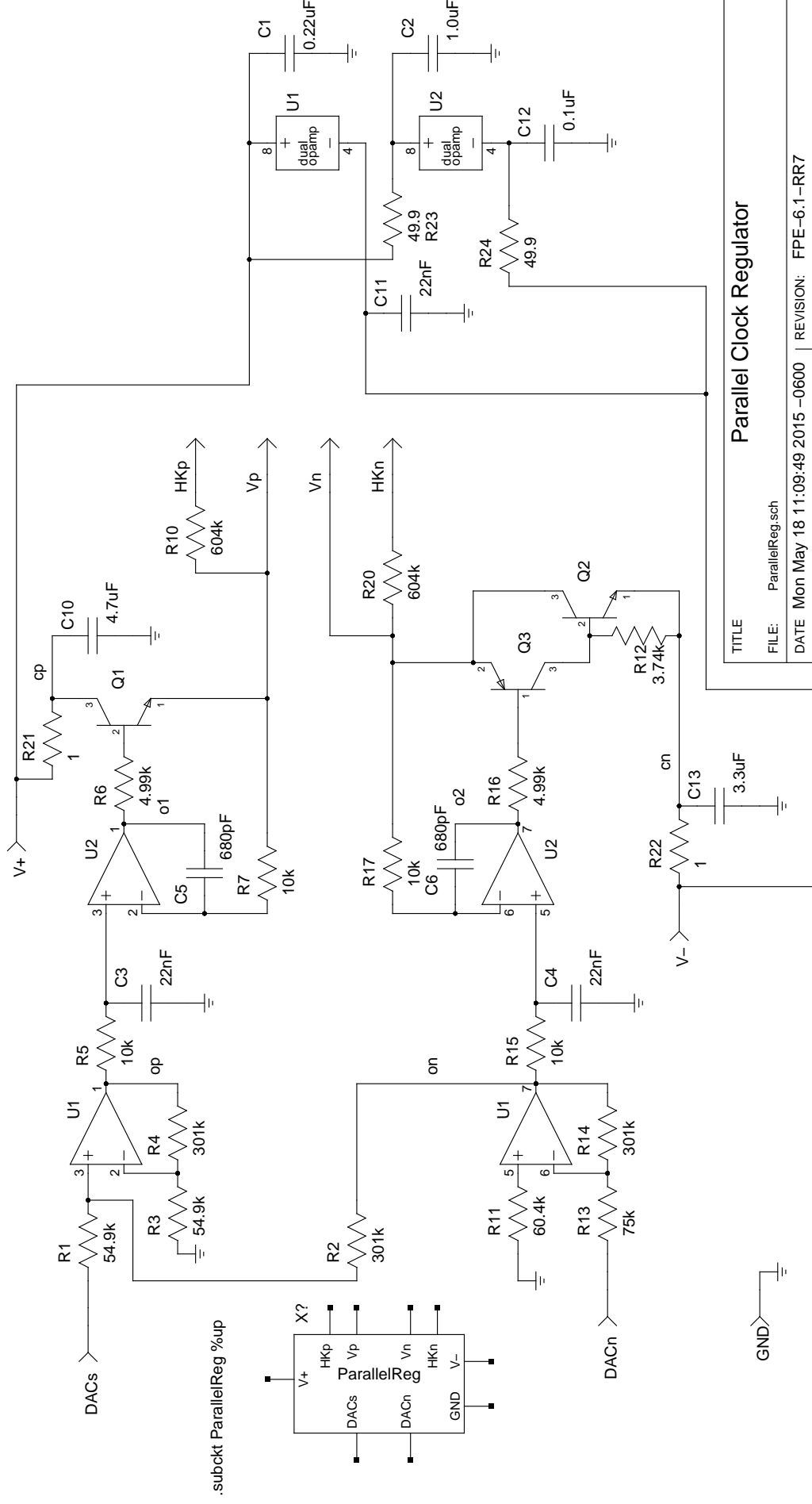
DATE Mon May 18 11:09:49 2015 -0600 REVISION: FPE-6.1-RR7

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TITLE ParallelReg.sch  
FILE: ParallelReg.sch

DATE Mon May 18 11:09:49 2015 -0600 REVISION: FPE-6.1-RR7

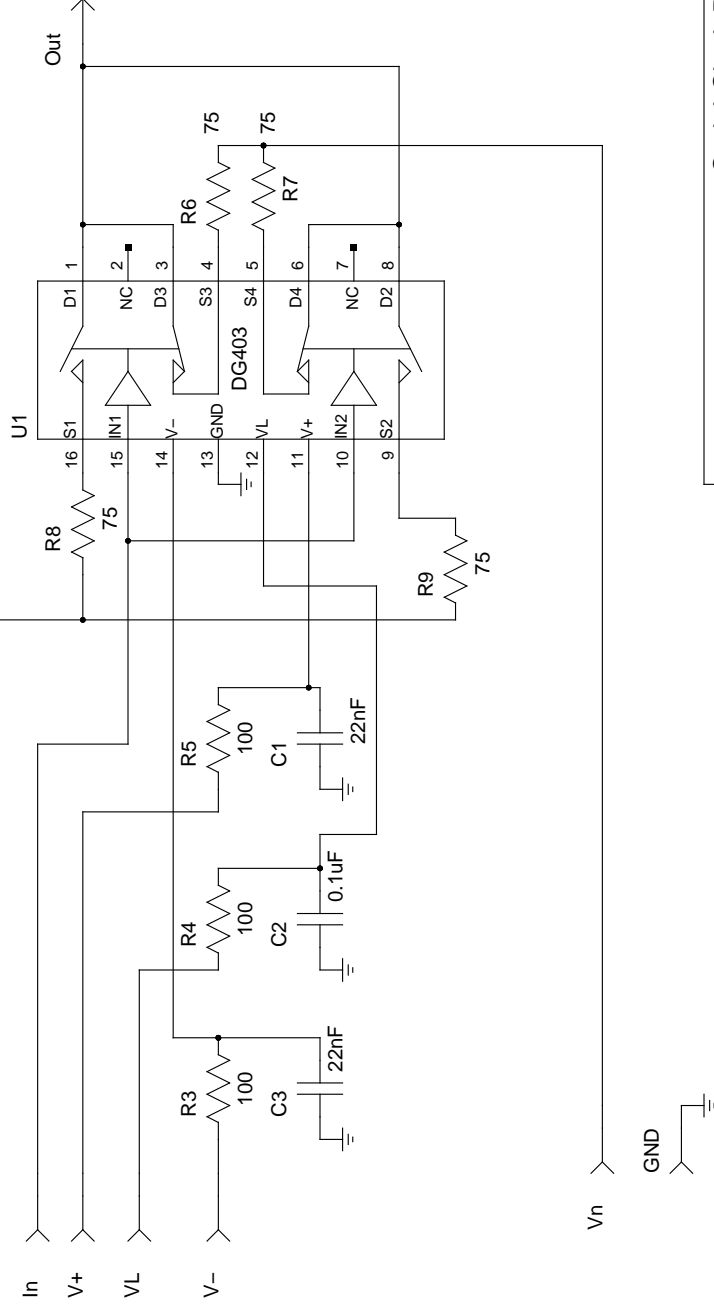
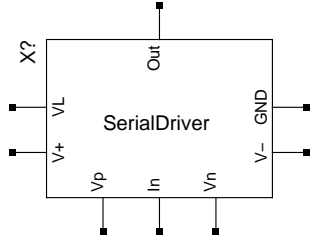
PAGE 1 OF 1 DRAWN BY: jpd

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.subckt SerialDriver %up



## Serial Clock Driver

TITLE SerialDriver.sch

FILE: SerialDriver.sch

DATE Mon May 18 11:09:49 2015 -0600

REVISION: FPE-6.1-RR7

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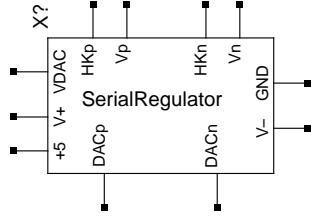
DRAWN BY:

jpd

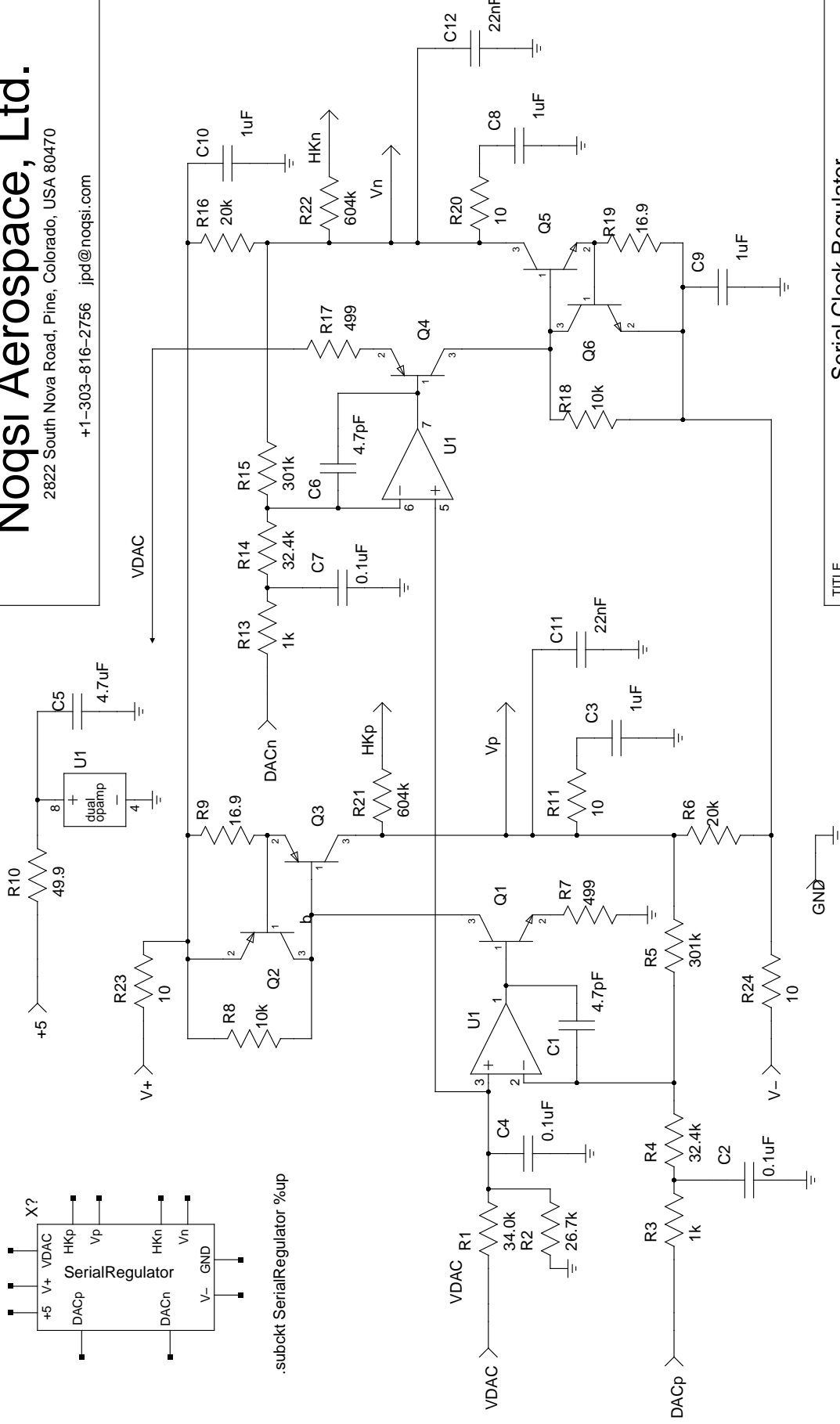
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.subckt SerialRegulator %up



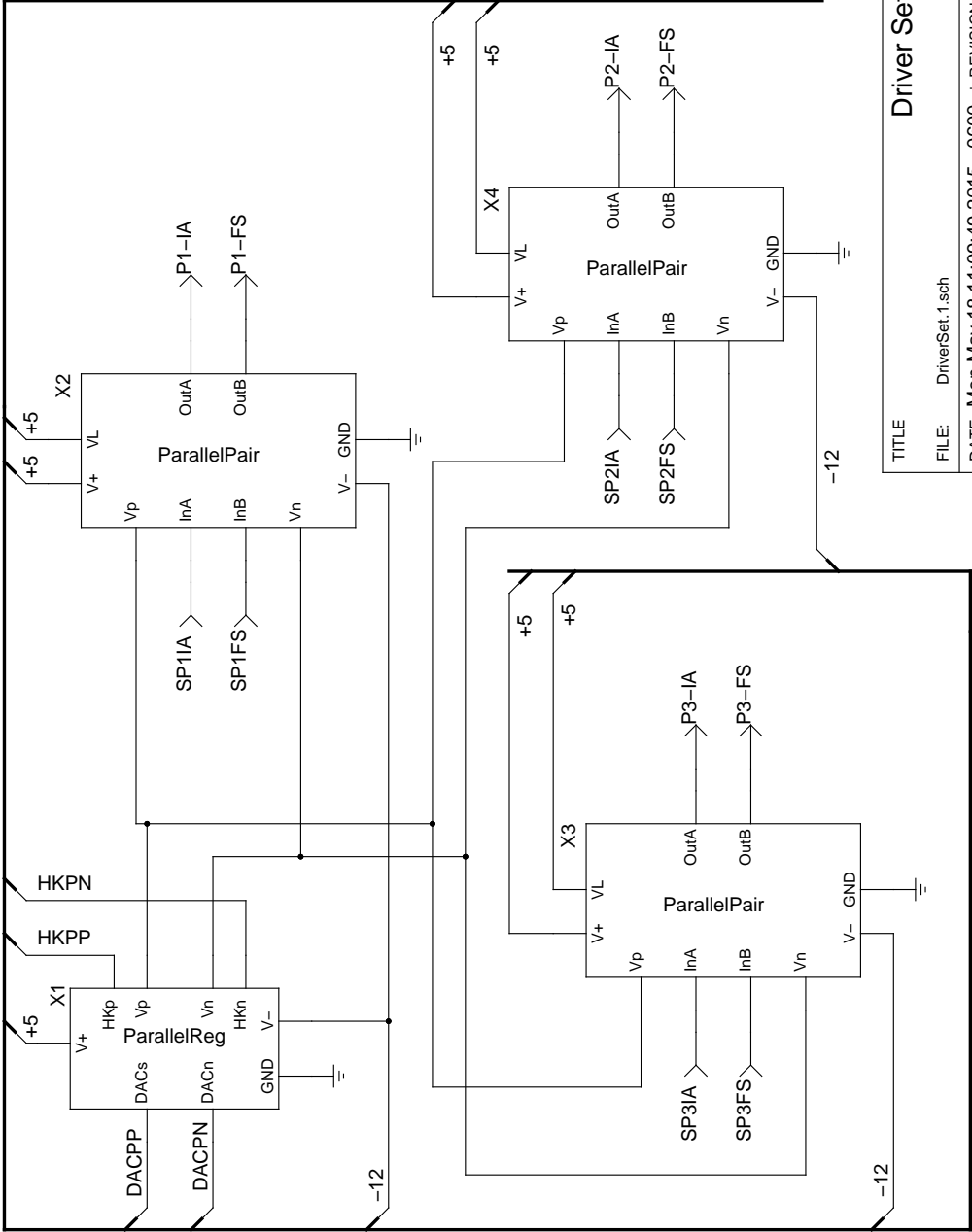
## Serial Clock Regulator

FILE: SerialRegulator.sch

DATE Mon May 18 11:09:49 2015 -0600 REVISION: FPE-6.1-RR7

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Parallel Clock Drivers



Driver Set

FILE: DriverSet.1.sch

DATE Mon May 18 11:09:49 2015 -0600 REVISION: FPE-6.1-RR7

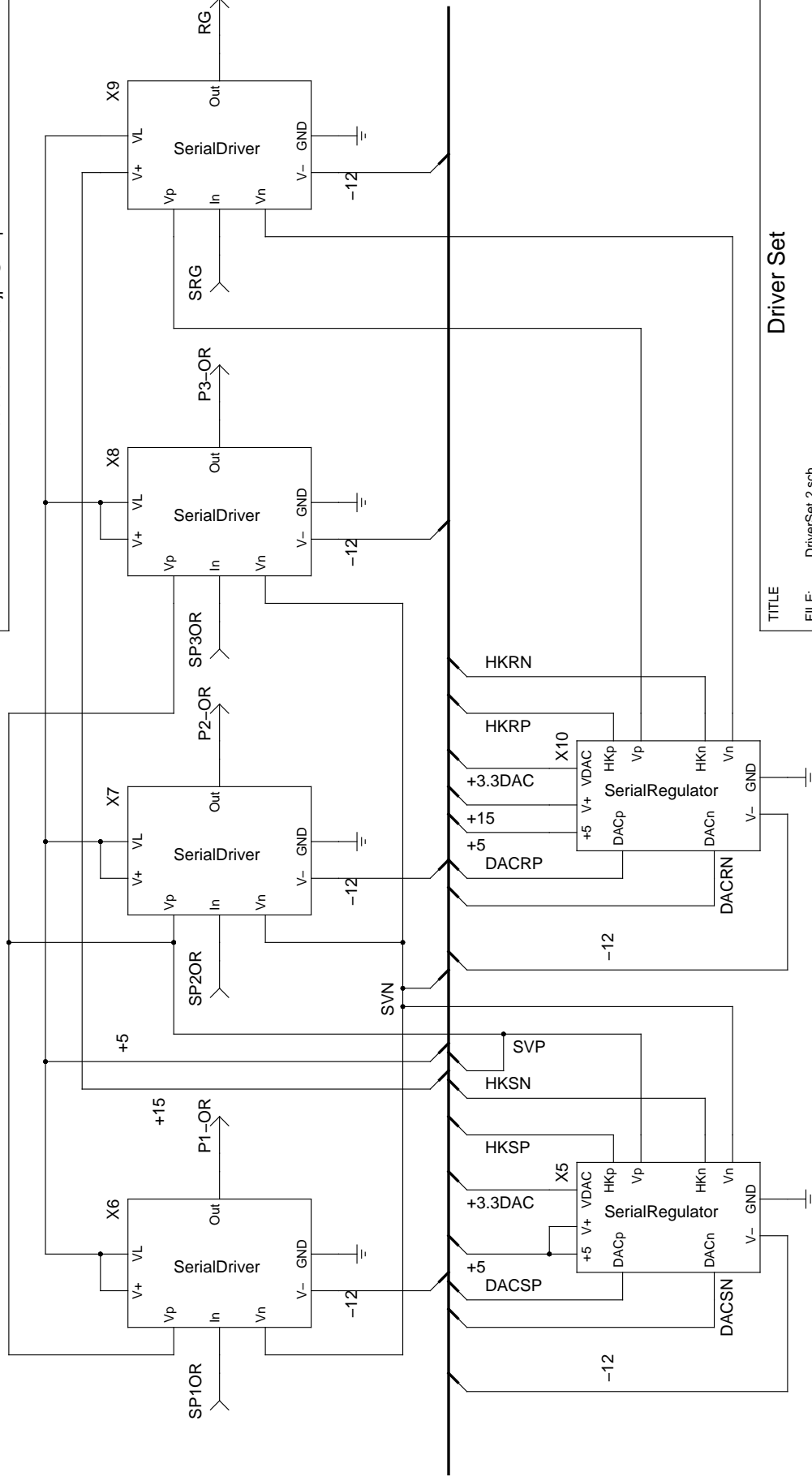
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## Serial Clock Drivers



## Driver Set

FILE: DriverSet.2.sch

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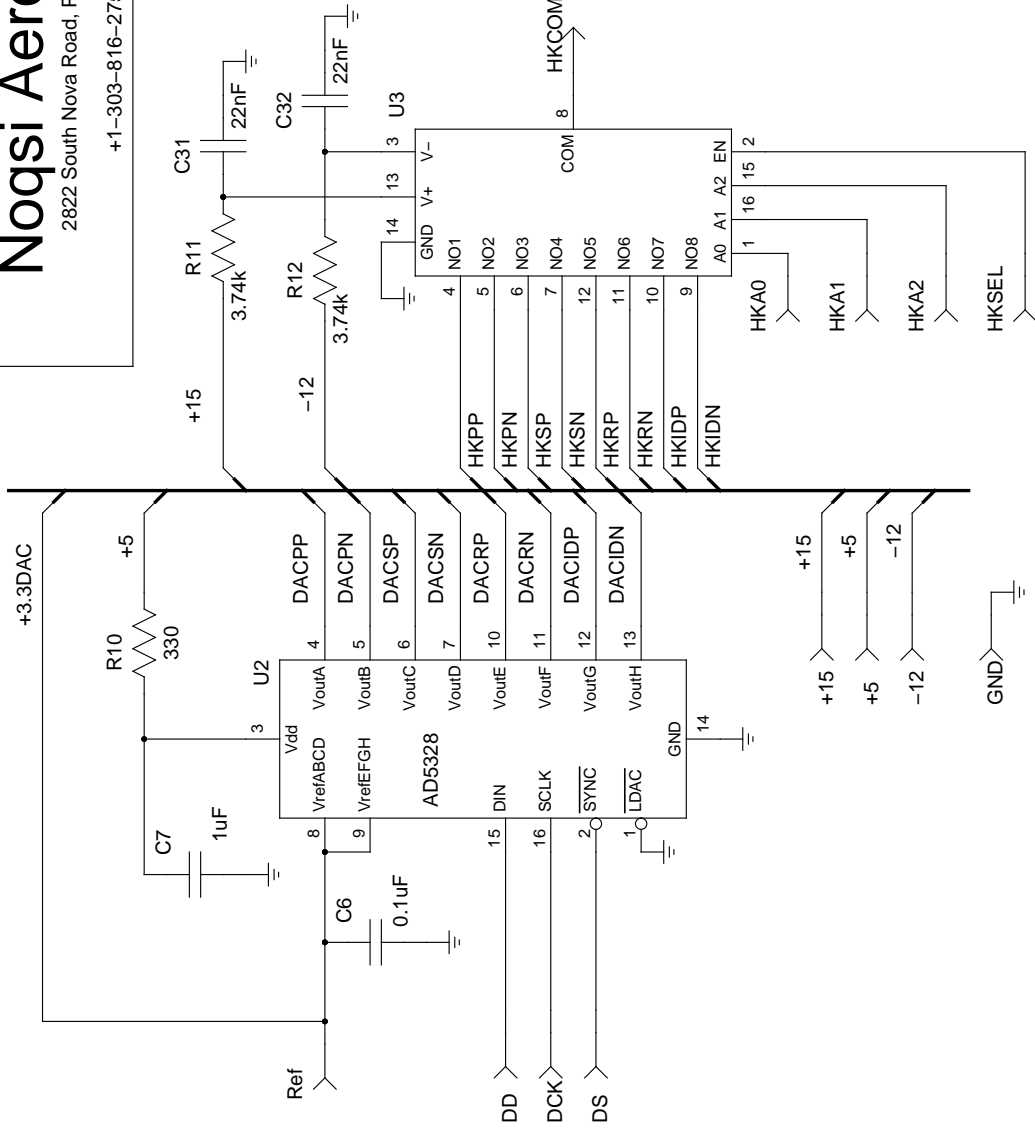




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## Driver Set

TITLE DriverSet.4.sch

FILE: DriverSet.4.sch

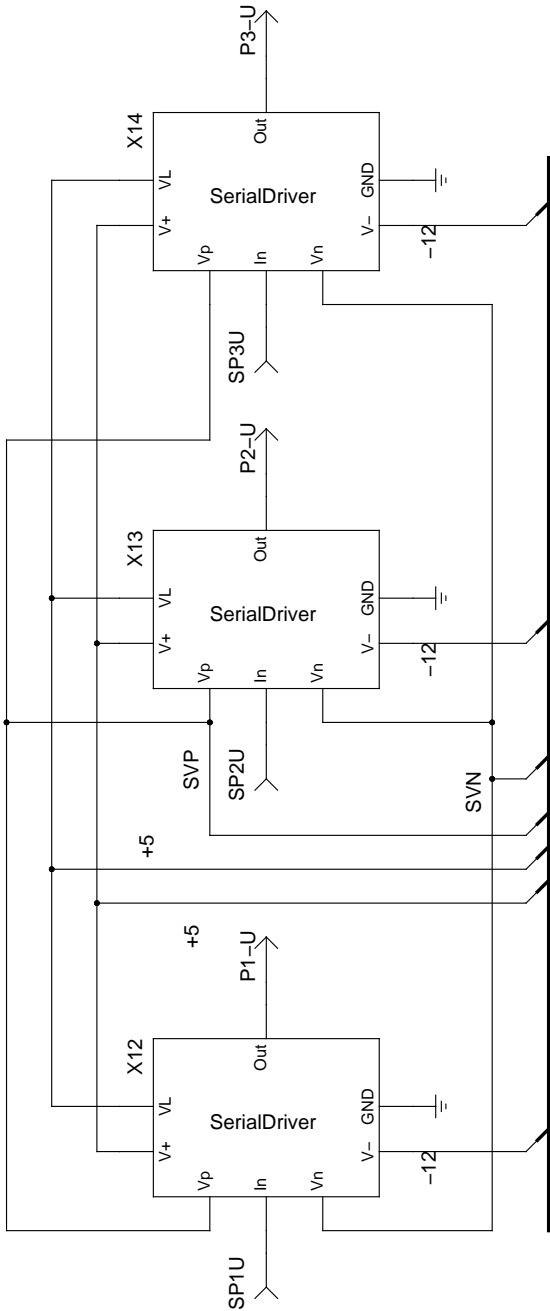
DATE Mon May 18 11:09:49 2015 -0600 REVISION: FPE-6.1-RR7

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## Driver Set

FILE: DriverSet.5.sch

DATE Mon May 18 11:09:49 2015 -0600 REVISION: FPE-6.1-RR7

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