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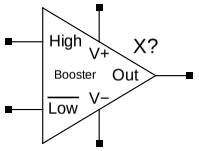

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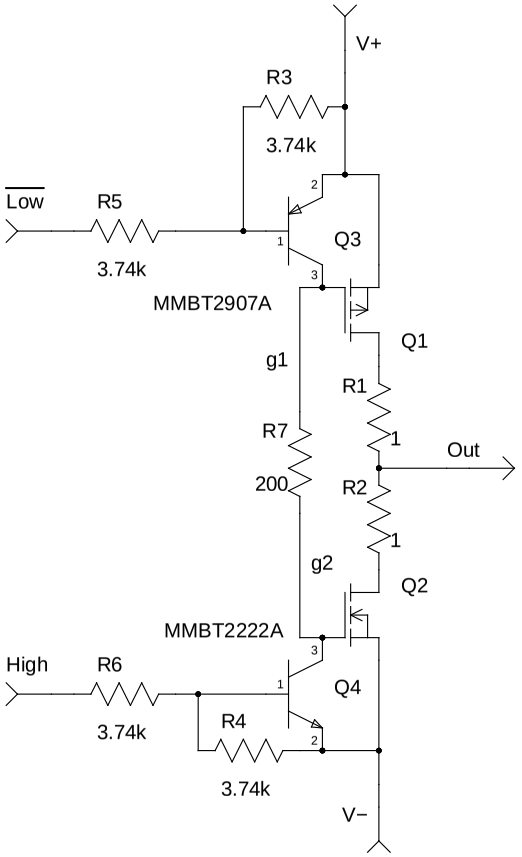
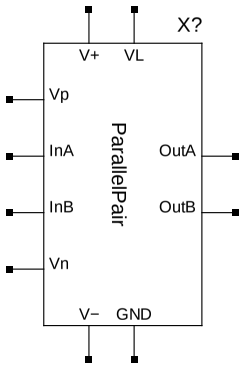


TITLE	Parallel clock current booster		
FILE:	Booster.sch		
DATE	Wed May 13 16:03:43 2015 −0600	REVISION:	FPE−6.1−RR6
PAGE	1 OF 1	DRAWN BY:	jpd



.subckt ParallelPair %up

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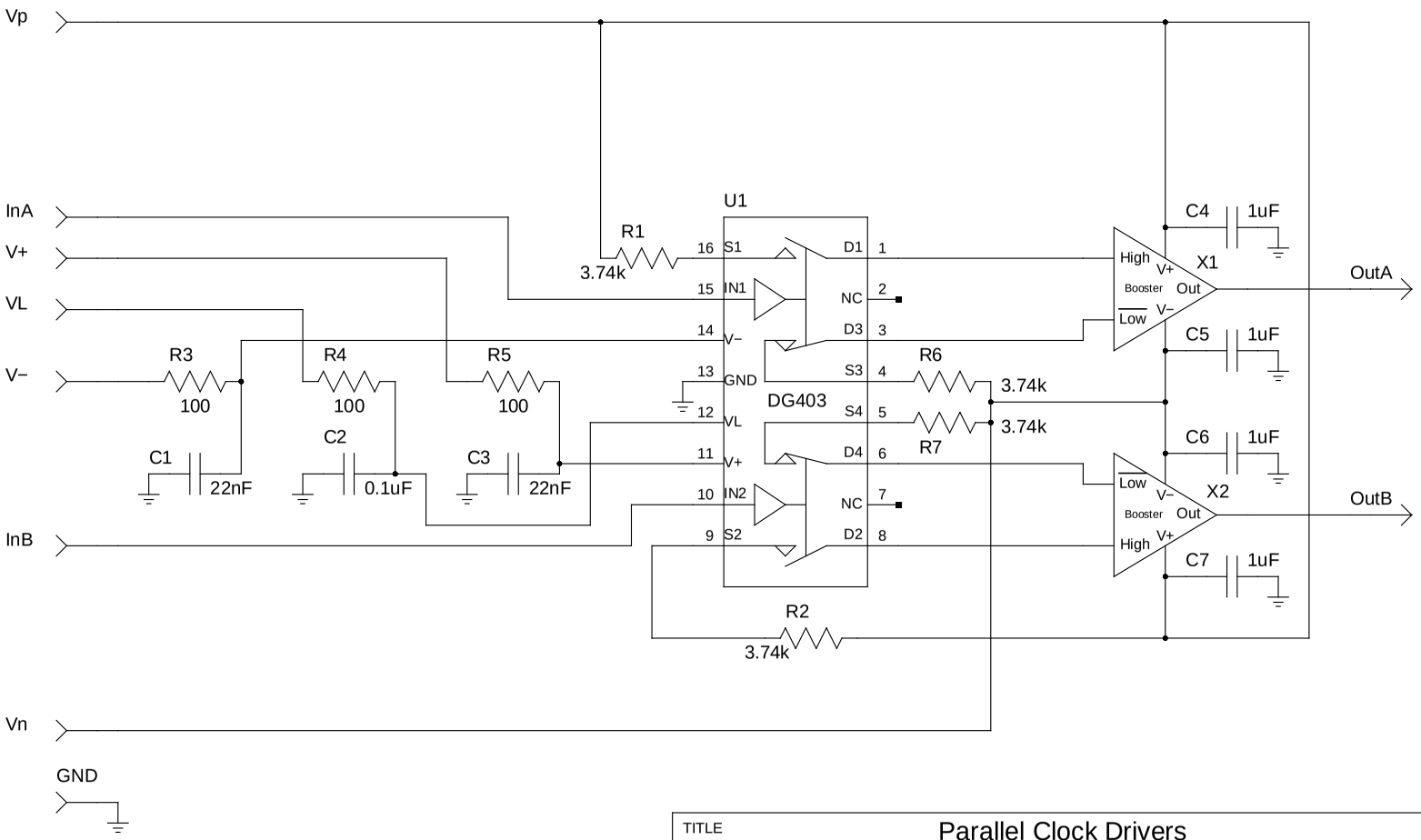

TITLE	Parallel Clock Drivers		
FILE:	ParallelPair.sch		
DATE	Wed May 13 16:03:43 2015 −0600	REVISION:	FPE−6.1−RR6
PAGE	1 OF 1	DRAWN BY:	jpd

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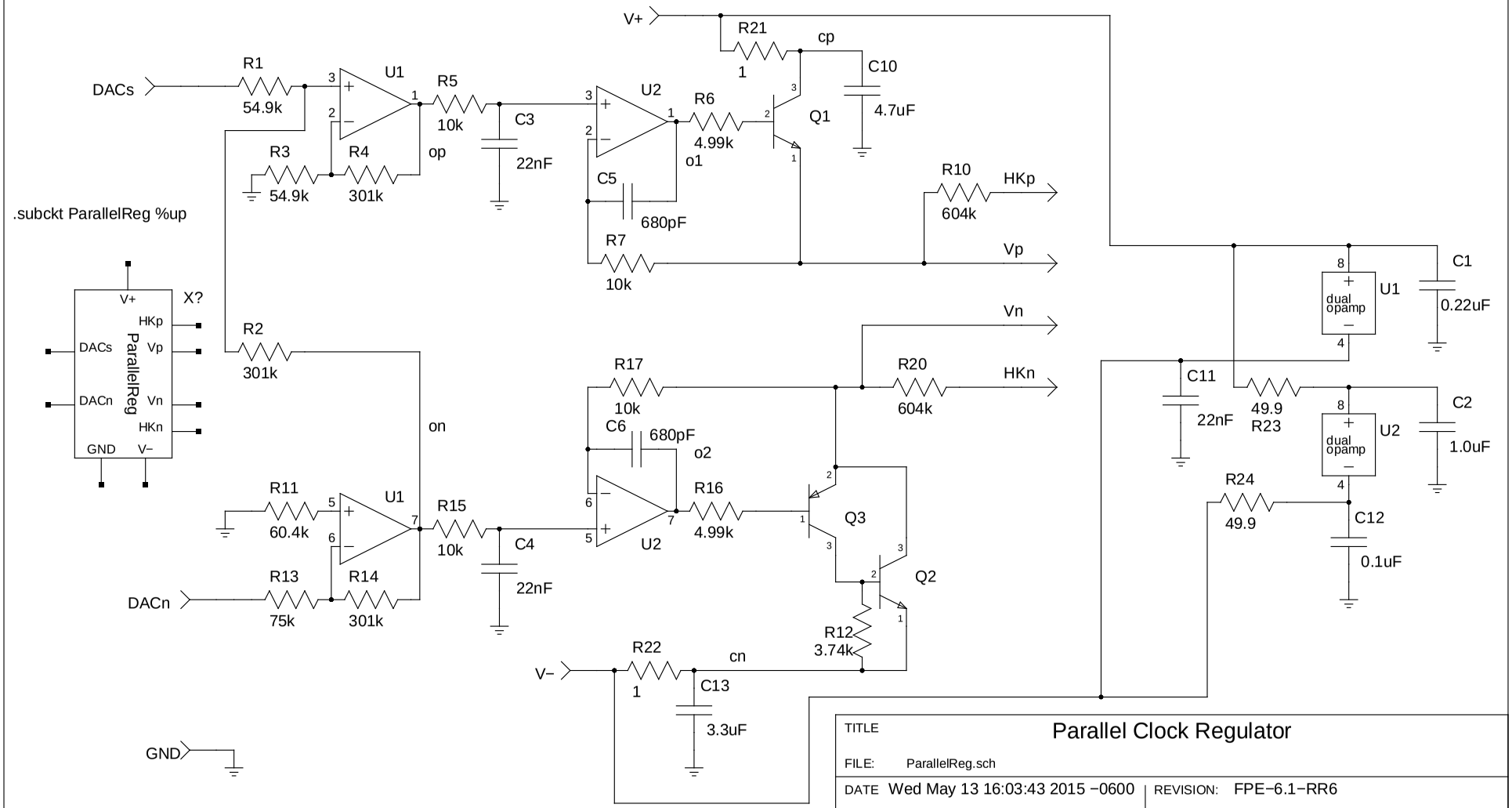

TITLE	Parallel Clock Regulator
FILE:	ParallelReg.sch
DATE	Wed May 13 16:03:43 2015 −0600
REVISION:	FPE−6.1−RR6
PAGE	1 OF 1
DRAWN BY:	jpd

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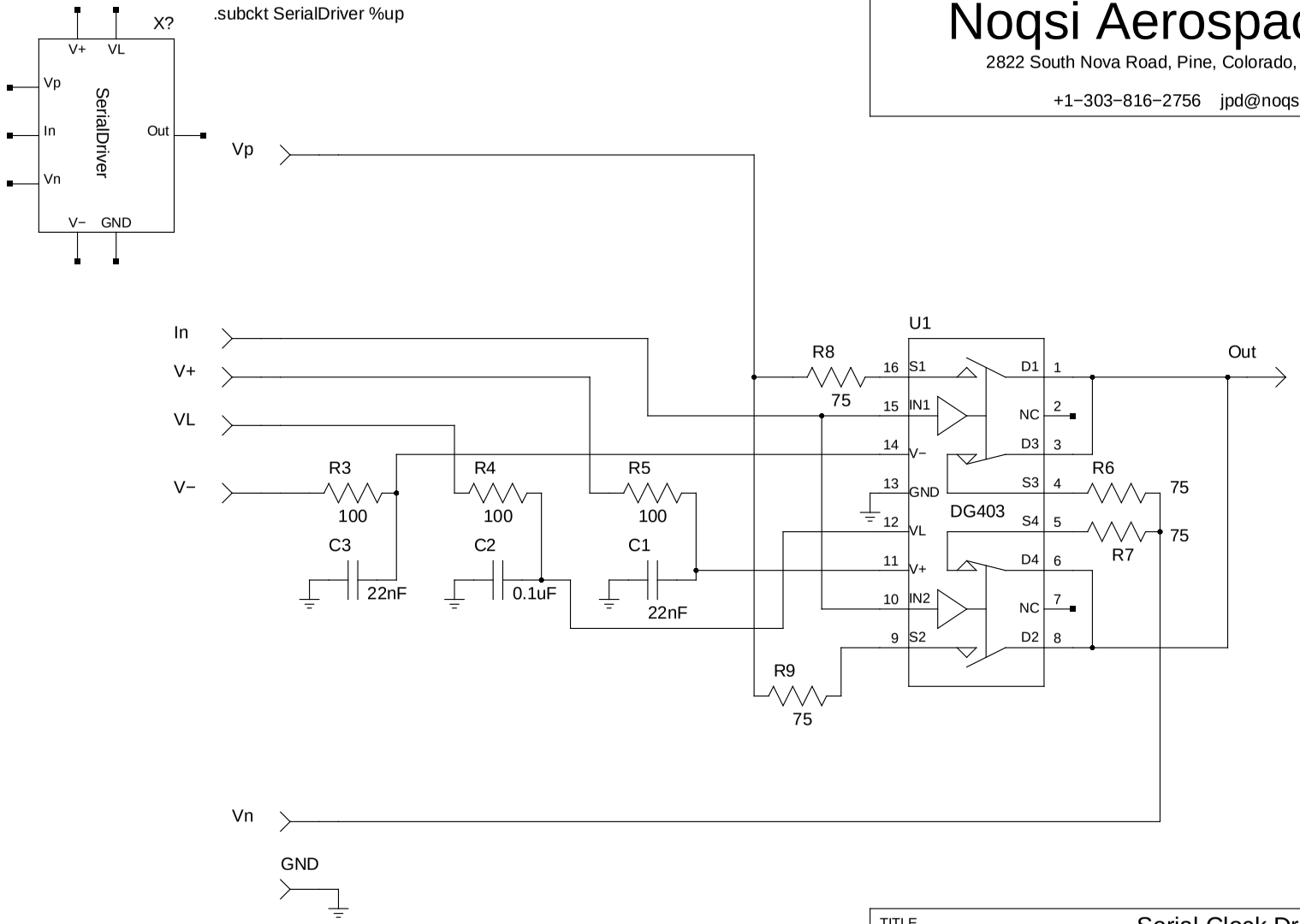
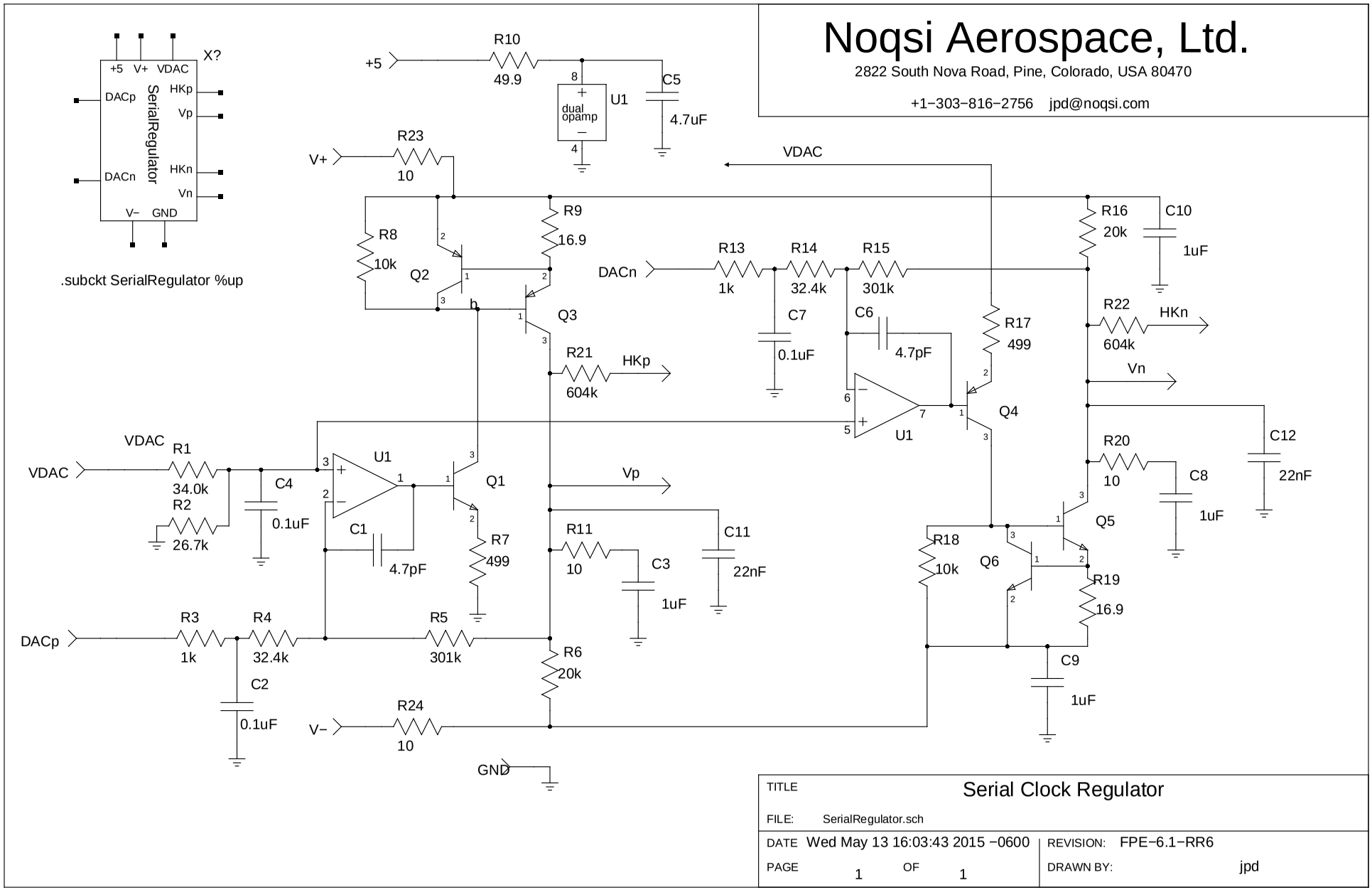


TITLE	Serial Clock Driver		
FILE:	SerialDriver.sch		
DATE	Wed May 13 16:03:43 2015 −0600	REVISION:	FPE−6.1−RR6
PAGE	1 OF 1	DRAWN BY:	jpd


X?
+5
V+
VDAC
DACp
HKp
Vp
SerialRegulator
DACn
HKn
Vn
V−
GND
.subckt SerialRegulator %up
R10
49.9
U1
dual opamp
C5
4.7uF
R23
10
R8
10k
R9
16.9
Q2
Q3
R21
604k
HKp
R13
1k
R14
32.4k
R15
301k
C7
0.1uF
C6
4.7pF
R17
499
U1
Q4
R16
20k
C10
1uF
R22
604k
HKn
Vn
C12
22nF
R20
10
C8
1uF
Q5
R18
10k
Q6
R19
16.9
C9
1uF
R1
34.0k
R2
26.7k
C4
0.1uF
U1
Q1
C1
4.7pF
R7
499
Vp
R11
10
C3
1uF
C11
22nF
R3
1k
R4
32.4k
C2
0.1uF
R5
301k
R6
20k
R24
10
GND
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TITLE
Serial Clock Regulator
FILE: SerialRegulator.sch
DATE Wed May 13 16:03:43 2015 −0600
REVISION: FPE−6.1−RR6
PAGE 1 OF 1
DRAWN BY: jpd

Parallel Clock Drivers

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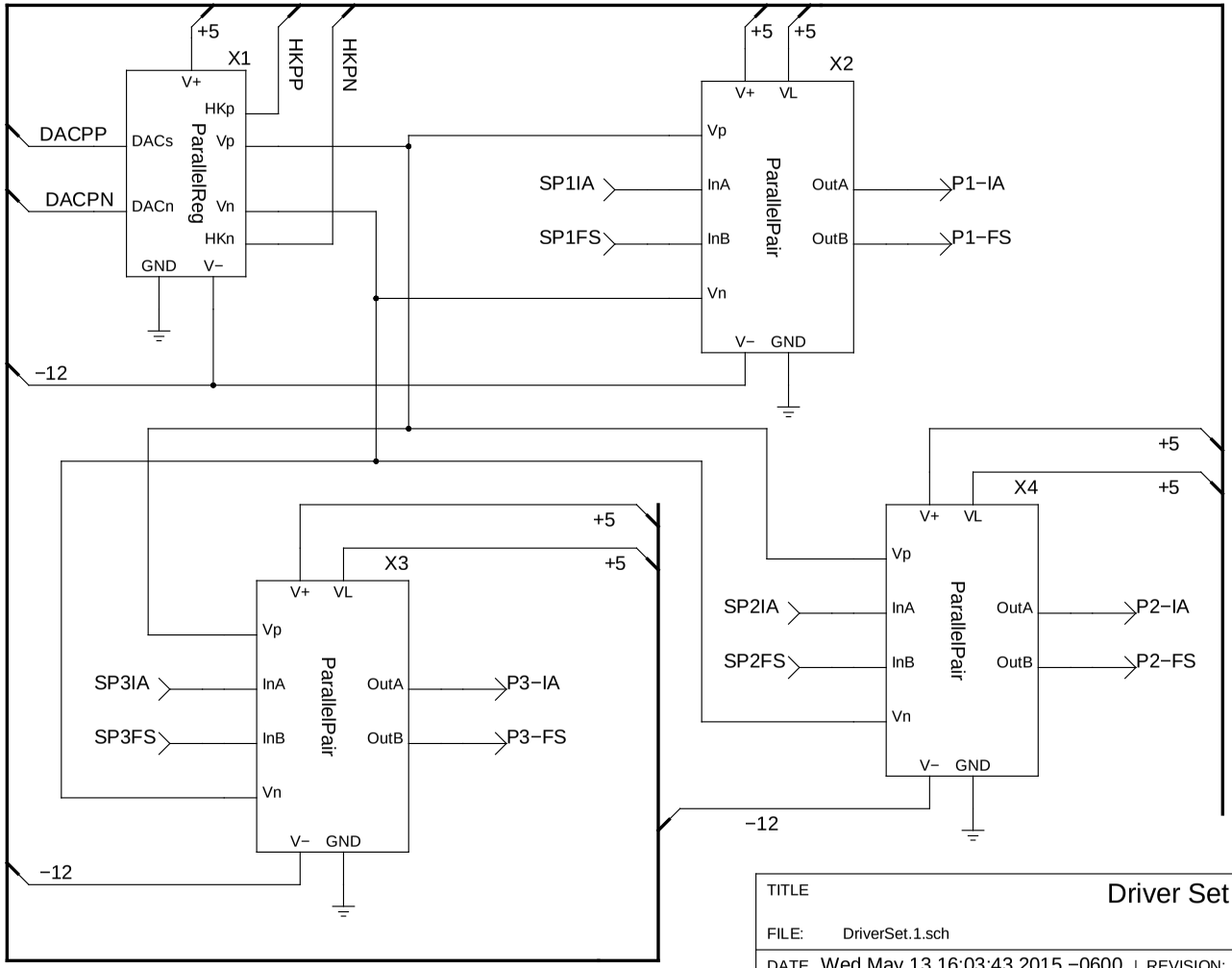

TITLE	Driver Set		
FILE:	DriverSet.1.sch		
DATE	Wed May 13 16:03:43 2015 −0600	REVISION:	FPE−6.1−RR6
PAGE	1 OF 5	DRAWN BY:	jpd

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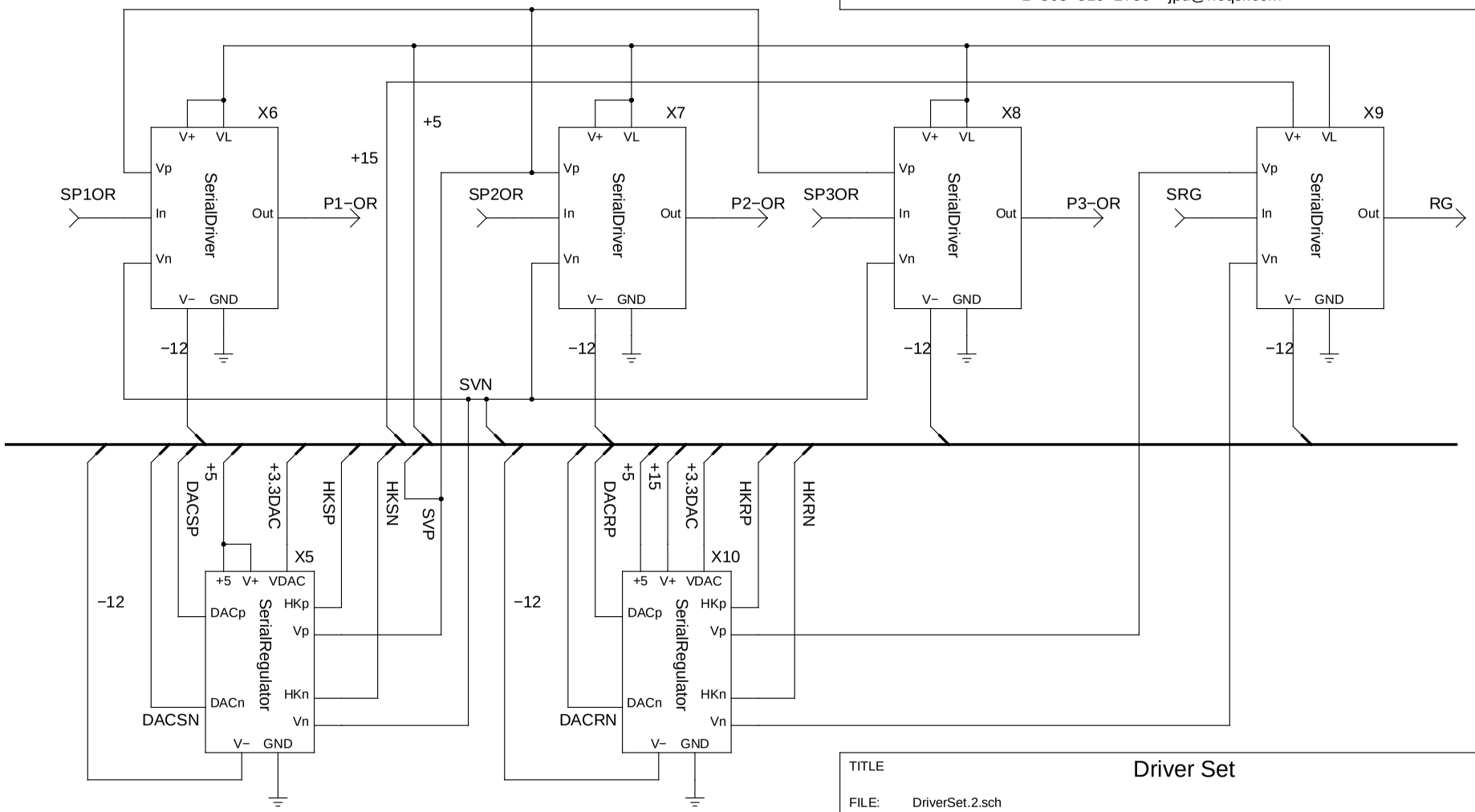

TITLE	Driver Set		
FILE:	DriverSet.2.sch		
DATE	Wed May 13 16:03:43 2015 −0600	REVISION:	FPE−6.1−RR6
PAGE	2 OF 5	DRAWN BY:	jpd

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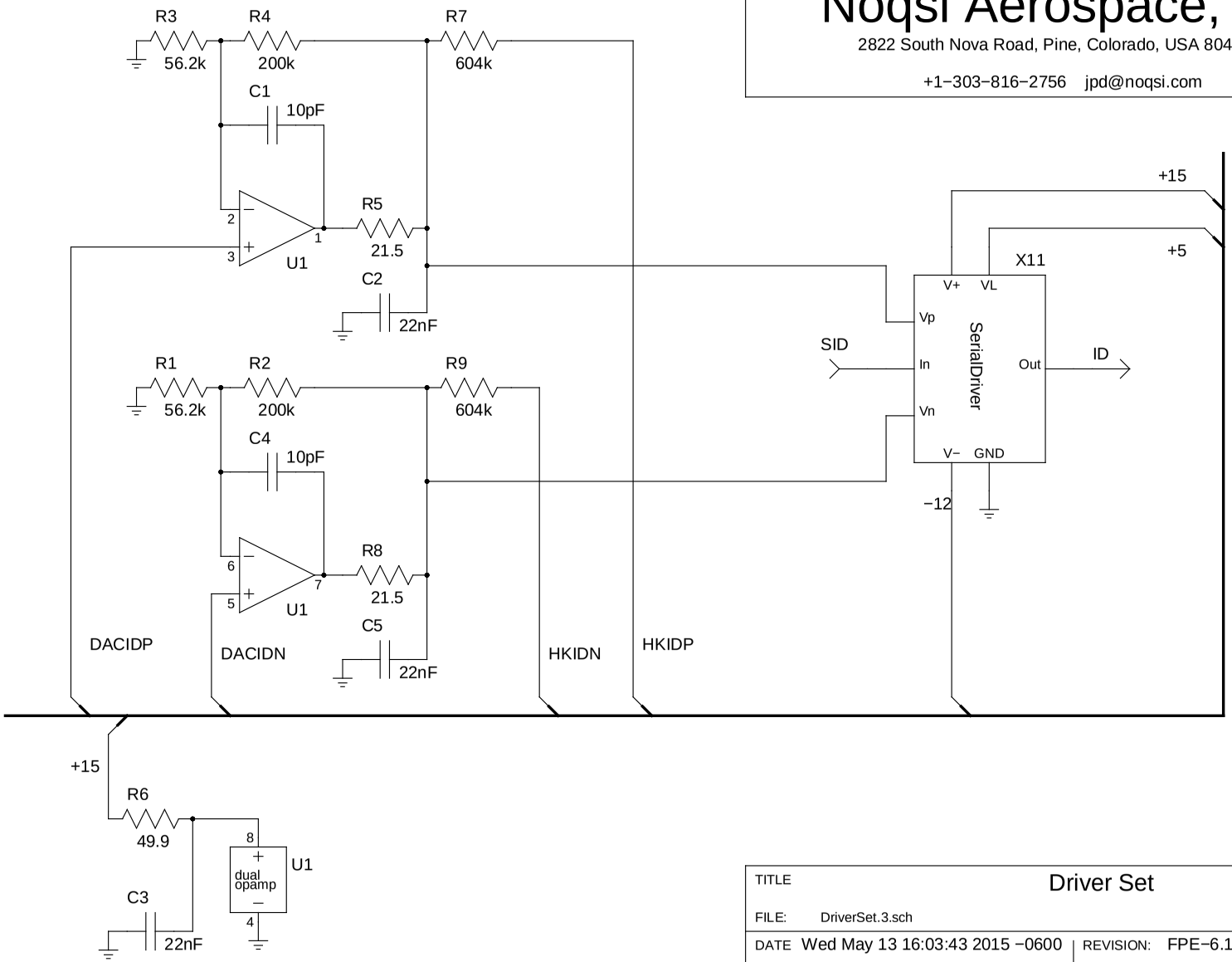

TITLE	Driver Set		
FILE:	DriverSet.3.sch		
DATE	Wed May 13 16:03:43 2015 −0600	REVISION:	FPE−6.1−RR6
PAGE	3 OF 5	DRAWN BY:	jpd

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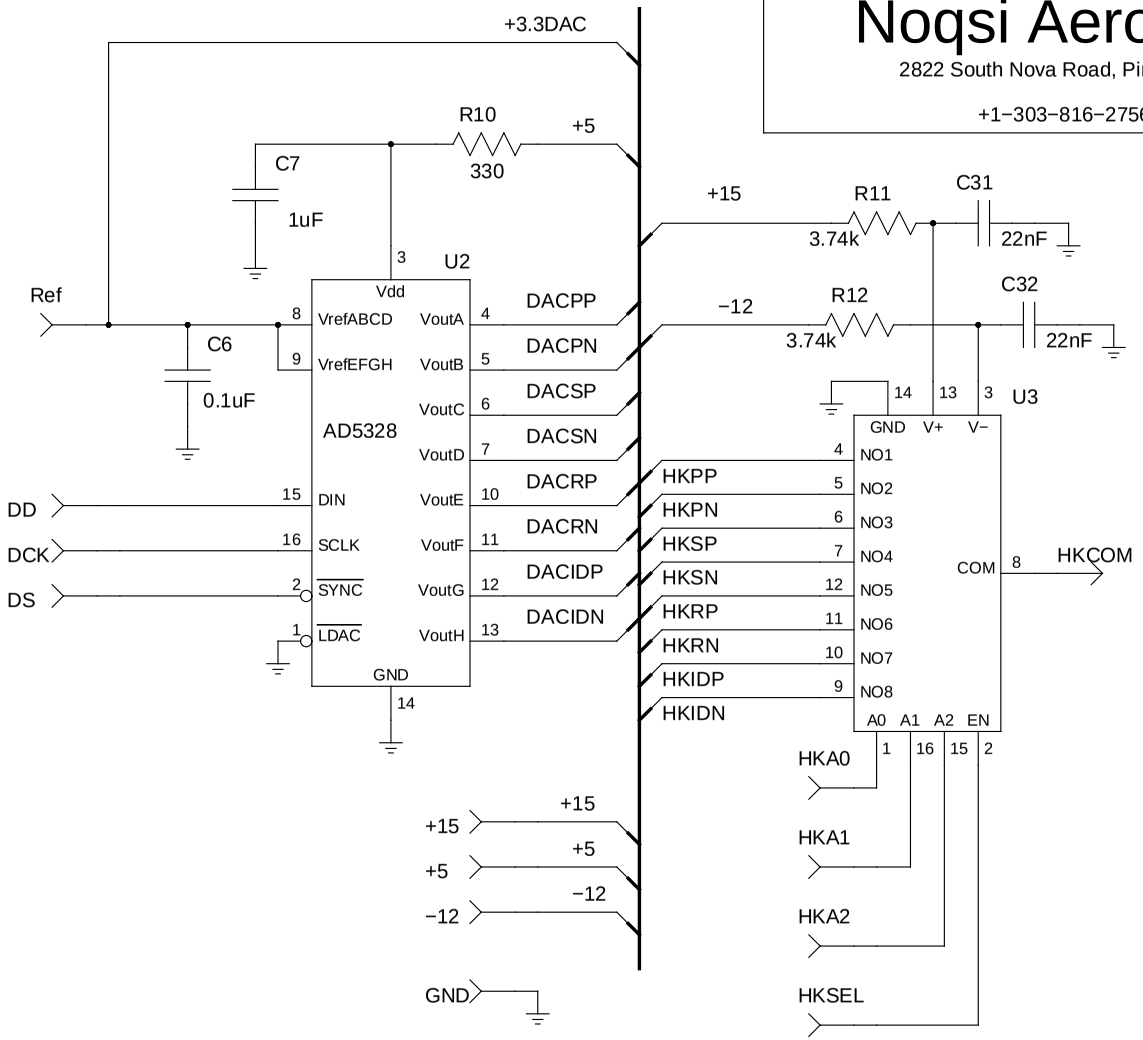

TITLE	Driver Set		
FILE:	DriverSet.4.sch		
DATE	Wed May 13 16:03:43 2015 −0600	REVISION:	FPE−6.1−RR6
PAGE	4 OF 5	DRAWN BY:	jpd

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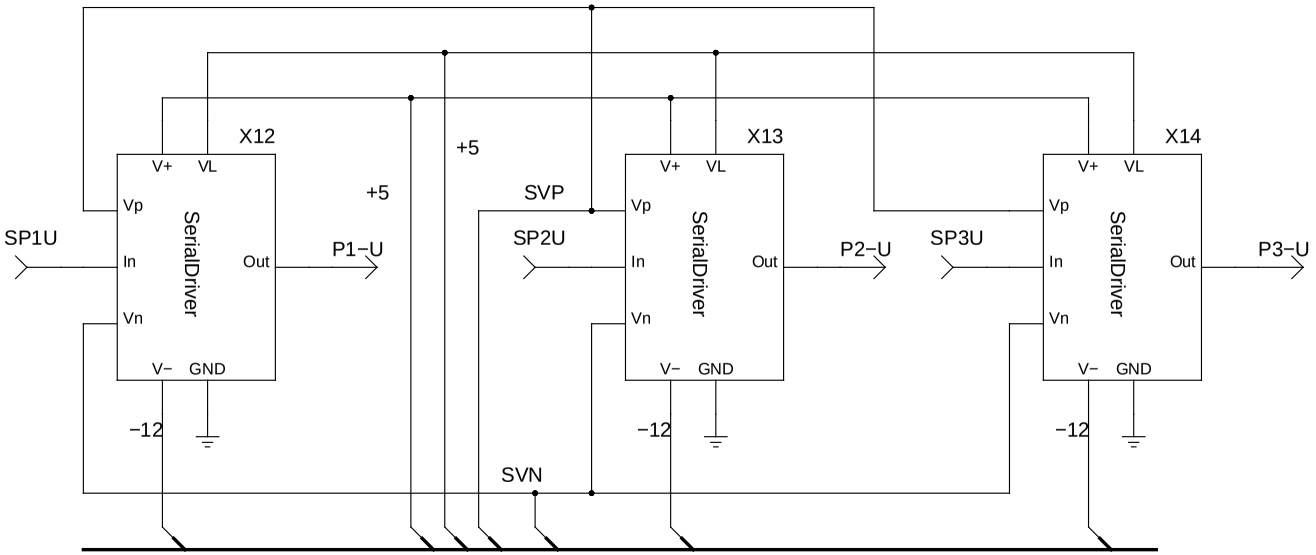

TITLE	Driver Set		
FILE:	DriverSet.5.sch		
DATE	Wed May 13 16:03:43 2015 −0600	REVISION:	FPE−6.1−RR6
PAGE 5	OF 5	DRAWN BY:	jpd

Table of Contents

 1 *Booster.ps.....* sheets 1 to 1 (1) pages 1– 1 1 lines
 2 *ParallelPair.ps....* sheets 1 to 2 (2) pages 1– 2 1 lines
 3 *ParallelReg.ps.....* sheets 2 to 3 (2) pages 2– 3 1 lines
 4 *SerialDriver.ps.....* sheets 3 to 4 (2) pages 3– 4 1 lines
 5 *SerialRegulator.ps..* sheets 4 to 5 (2) pages 4– 5 1 lines
 6 *DriverSet.1.ps.....* sheets 5 to 6 (2) pages 5– 6 1 lines
 7 *DriverSet.2.ps.....* sheets 6 to 7 (2) pages 6– 7 1 lines
 8 *DriverSet.3.ps.....* sheets 7 to 8 (2) pages 7– 8 1 lines
 9 *DriverSet.4.ps.....* sheets 8 to 9 (2) pages 8– 9 1 lines
10 *DriverSet.5.ps.....* sheets 9 to 10 (2) pages 9– 10 1 lines