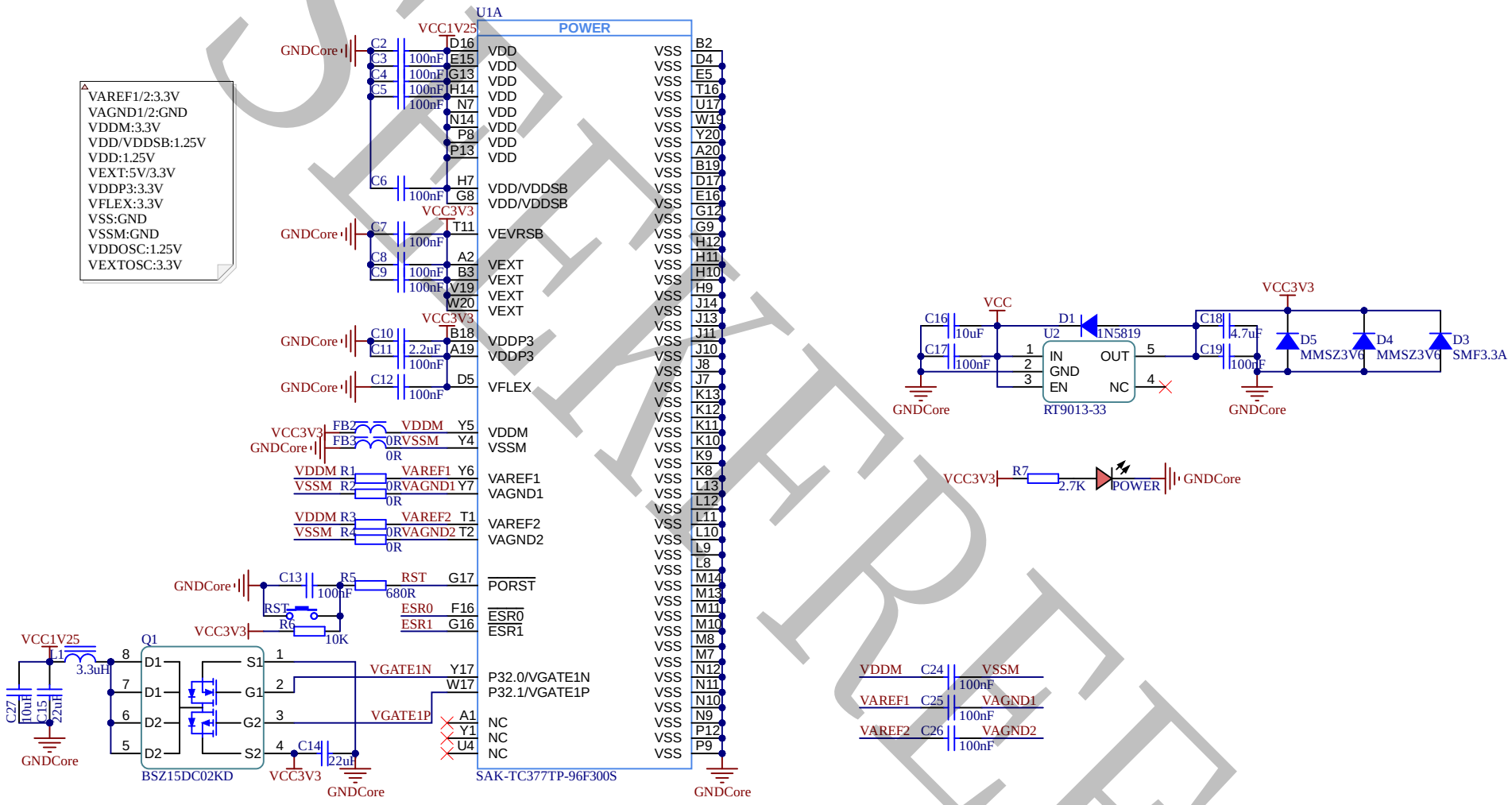
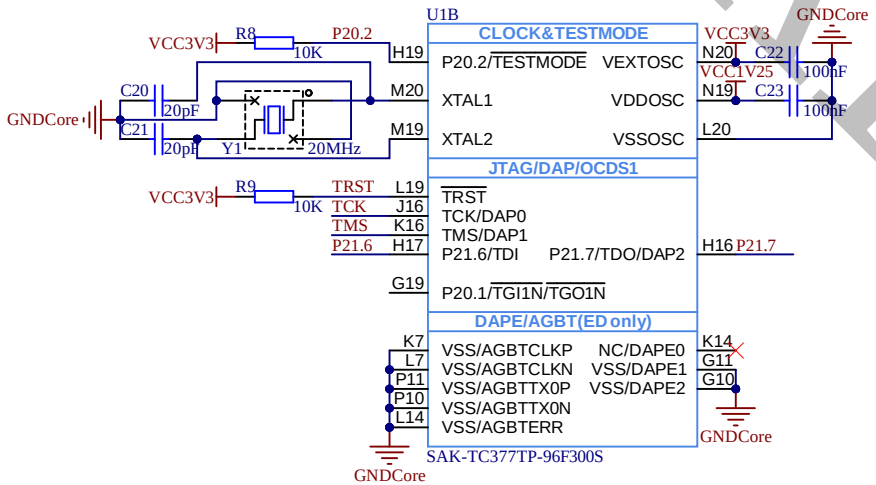


Title: TC377_CoreBoard_POWER			成都逐飞科技
Size: A4	Author: 猫肆管	Revision: 2.0	
Date: 2020/12/17	Time: 19:17:20	Sheet: 1 of: 4	

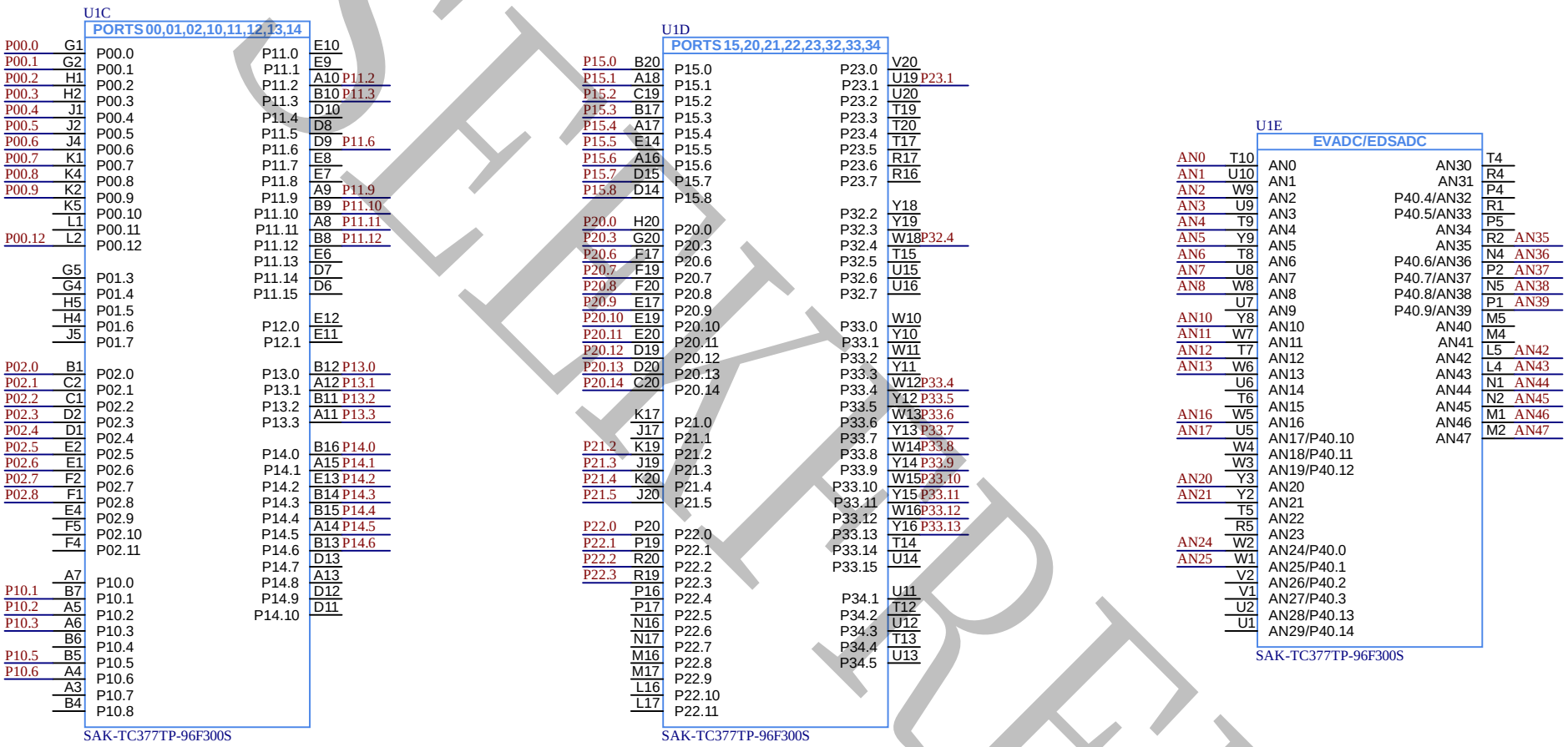
HWCFG [1] P14.5	HWCFG [2] P14.2	HWCFG [3] P14.3	HWCFG [4] P10.5	HWCFG [5] P10.6	HWCFG [6] P14.4
0 - EVR33OFF 1 - EVR33ON	0 - EVRCOFF 1 - EVRCON	0 - Boot from pins HWCFG [5:4] 1 - Flash BMI boot	HWCFG [4:5] [0 0]- Generic Bootstrap (P14.0/1) [0 1]- ABM, Generic Bootstrap on fail (P14.0/1) [1 0]- ABM, ASC Bootstrap on fail (P15.2/3) [1 1]- Internal start from Flash		Default Pad state 0 - Pins in tristate 1 - Pins with pull-up
(weak pull-up active on reset)	(weak pull-up active on reset)	(weak pull-up active on reset)	(weak pull-up active on reset)		(weak pull-up active on reset)

默认
HWCFG1=0
HWCFG2=1
HWCFG3=1
HWCFG4=1
HWCFG5=1
HWCFG6=1

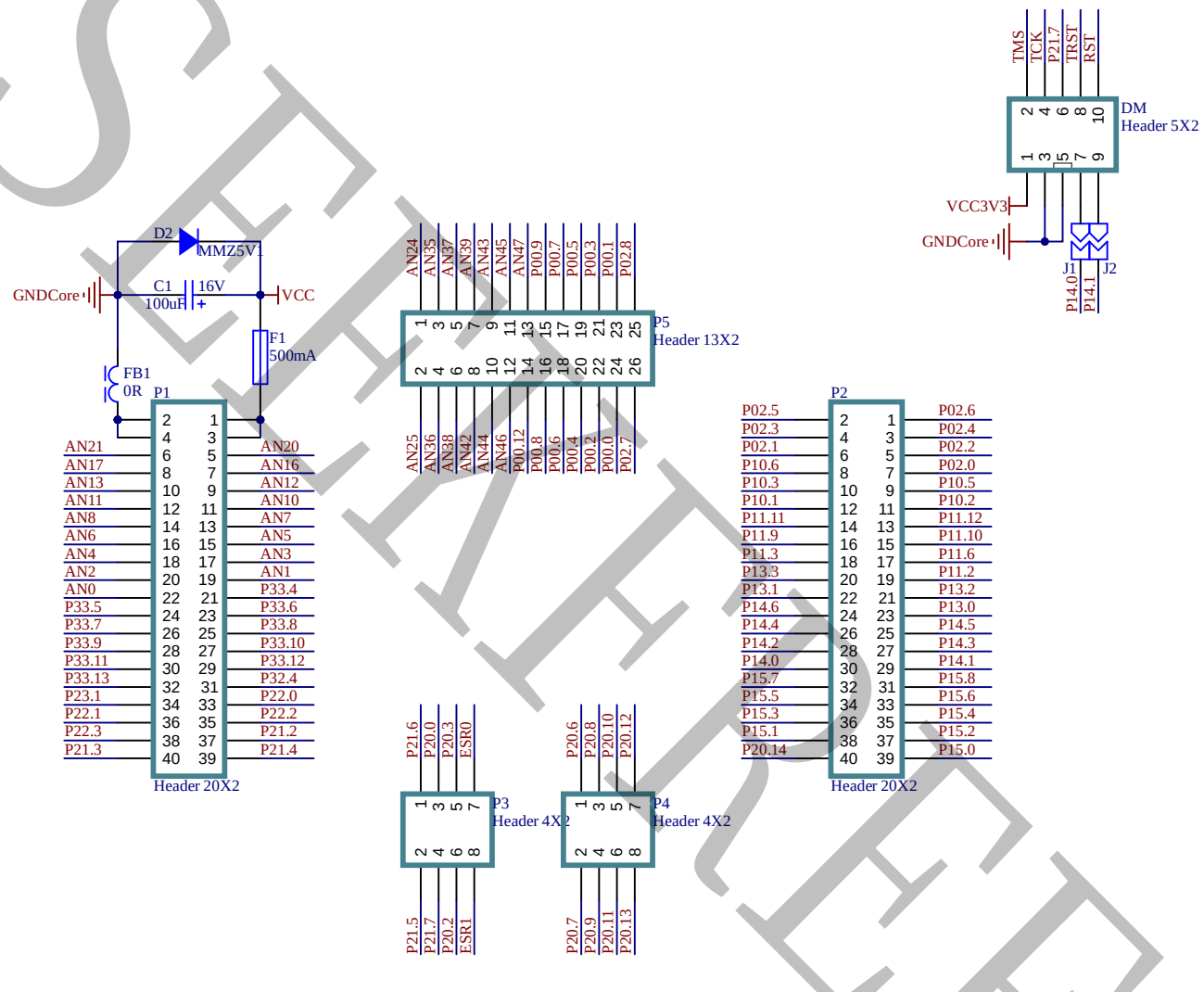
1.) HWCFG [1:6] has weak internal pull-up active at start-up if the pin is left unconnected.
2.) In xQFP80 and xQFP100 packages, HWCFG[2] and HWCFG[6] pins are not available and instead tied as follows:
HWCFG[2] is tied to 1 to ensure EVRC is enabled;
HWCFG[6] is tied to 0 to ensure pins are in tristate.



Title: TC377_CoreBoard_SYSCFG			成都逐飞科技
Size: A4	Author: 猫肆管	Revision: 2.0	
Date: 2020/12/17	Time: 19:17:21	Sheet: 2 of: 4	



Title: TC377_CoreBoard_PORTS			成都逐飞科技
Size: A4	Author: 猫肆管	Revision: 2.0	
Date: 2020/12/17	Time: 19:17:21	Sheet: 3 of: 4	



Title: TC377_CoreBoard_IO			成都逐飞科技
Size: A4	Author: 猫肆管	Revision: 2.0	
Date: 2020/12/17	Time: 19:17:21	Sheet: 4 of: 4	