

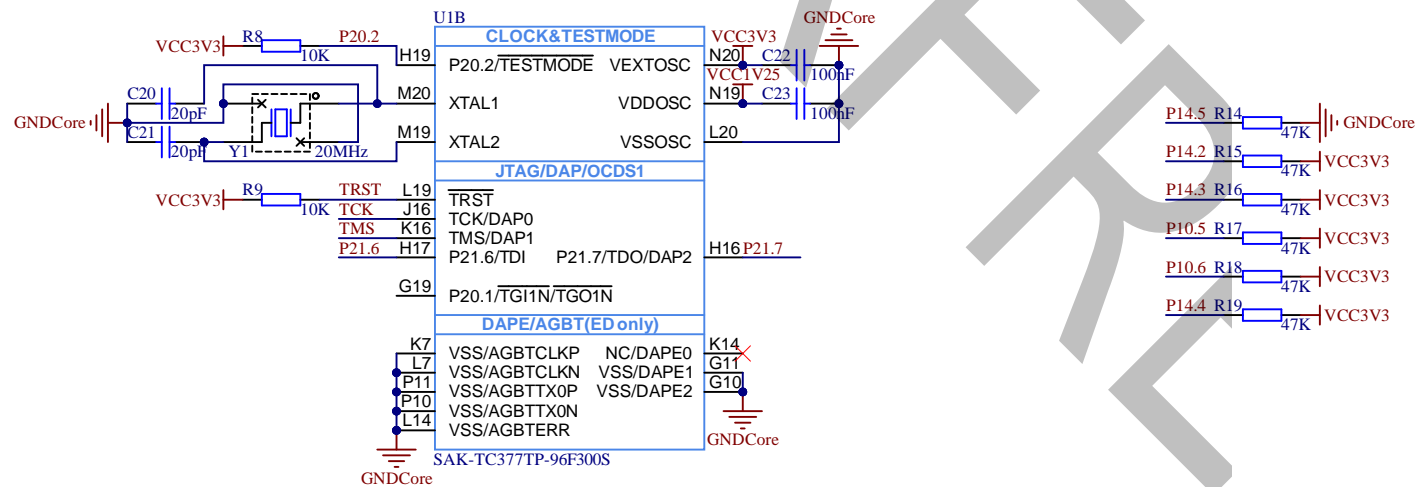
HWCFG [1]
P14.50 - EVR33OFF
1 - EVR33ON(weak pull-up
active on reset)HWCFG [2]
P14.20 - EVRCOFF
1 - EVRCON(weak pull-up
active on reset)HWCFG [3]
P14.30 - Boot from pins
HWCFG [5:4]
1 - Flash BMI boot(weak pull-up
active on reset)HWCFG [4]
P10.5HWCFG [4:5]
[0 0]- Generic Bootstrap (P14.0/1)
[0 1]- ABM, Generic Bootstrap on fail (P14.0/1)
[1 0]- ABM, ASC Bootstrap on fail (P15.2/3)
[1 1]- Internal start from Flash

(weak pull-up active on reset)

HWCFG [5]
P10.6HWCFG [6]
P14.4Default Pad state
0 - Pins in tristate
1 - Pins with pull-up(weak pull-up
active on reset)

默认
HWCFG1=0
HWCFG2=1
HWCFG3=1
HWCFG4=1
HWCFG5=1
HWCFG6=1

- 1.) HWCFG [1:6] has weak internal pull-up active at start-up if the pin is left unconnected.
2.) In xQFP80 and xQFP100 packages, HWCFG[2] and HWCFG[6] pins are not available and instead tied as follows:
HWCFG[2] is tied to 1 to ensure EVRC is enabled;
HWCFG[6] is tied to 0 to ensure pins are in tristate.



UIC

PORTS 00,01,02,10,11,12,13,14

P00.0	G1	P00.0	P11.0	E10
P00.1	G2	P00.1	P11.1	E9
P00.2	H1	P00.2	P11.2	A10 P11.2
P00.3	H2	P00.3	P11.3	B10 P11.3
P00.4	J1	P00.4	P11.4	D10
P00.5	J2	P00.5	P11.5	D8
P00.6	J4	P00.6	P11.6	D9 P11.6
P00.7	K1	P00.7	P11.7	E8
P00.8	K4	P00.8	P11.8	E7
P00.9	K2	P00.9	P11.9	A9 P11.9
	K5	P00.10	P11.10	B9 P11.10
	L1	P00.11	P11.11	A8 P11.11
P00.12	L2	P00.12	P11.12	B8 P11.12
			P11.13	E6
	G5	P01.3	P11.14	D7
	G4	P01.4	P11.15	D6
	H5	P01.5		
	H4	P01.6	P12.0	E12
	J5	P01.7	P12.1	E11
				B12 P13.0
P02.0	B1	P02.0	P13.0	A12 P13.1
P02.1	C2	P02.1	P13.1	B11 P13.2
P02.2	C1	P02.2	P13.2	A11 P13.3
P02.3	D2	P02.3	P13.3	
P02.4	D1	P02.4		B16 P14.0
P02.5	E2	P02.5	P14.0	A15 P14.1
P02.6	E1	P02.6	P14.1	E13 P14.2
P02.7	F2	P02.7	P14.2	B14 P14.3
P02.8	F1	P02.8	P14.3	B15 P14.4
	E4	P02.9	P14.4	A14 P14.5
	F5	P02.10	P14.5	B13 P14.6
	F4	P02.11	P14.6	D13
			P14.7	A13
	A7	P10.0	P14.8	D12
P10.1	B7	P10.1	P14.9	D11
P10.2	A5	P10.2	P14.10	
P10.3	A6	P10.3		
	B6	P10.4		
P10.5	B5	P10.5		
P10.6	A4	P10.6		
	A3	P10.7		
	B4	P10.8		

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UID

PORTS 15,20,21,22,23,32,33,34

P15.0	B20	P15.0	P23.0	V20
P15.1	A18	P15.1	P23.1	U19 P23.1
P15.2	C19	P15.2	P23.2	U20
P15.3	B17	P15.3	P23.3	T19
P15.4	A17	P15.4	P23.4	T20
P15.5	E14	P15.5	P23.5	T17
P15.6	A16	P15.6	P23.6	R17
P15.7	D15	P15.7	P23.7	R16
P15.8	D14	P15.8		
				Y18
				Y19
P20.0	H20	P20.0	P32.2	Y19
P20.3	G20	P20.3	P32.3	W18 P32.4
P20.6	F17	P20.6	P32.4	T15
P20.7	F19	P20.7	P32.5	U15
P20.8	F20	P20.8	P32.6	U16
P20.9	E17	P20.9	P32.7	
P20.10	E19	P20.10		W10
P20.11	E20	P20.11	P33.0	Y10
P20.12	D19	P20.12	P33.1	W11
P20.13	D20	P20.13	P33.2	Y11
P20.14	C20	P20.14	P33.3	W12 P33.4
			P33.4	Y12 P33.5
	K17	P21.0	P33.5	W13 P33.6
	J17	P21.1	P33.6	Y13 P33.7
P21.2	K19	P21.1	P33.7	W14 P33.8
P21.3	J19	P21.2	P33.8	Y14 P33.9
P21.4	K20	P21.3	P33.9	W15 P33.10
P21.5	J20	P21.4	P33.10	Y15 P33.11
		P21.5	P33.11	W16 P33.12
			P33.12	Y16 P33.13
P22.0	P20	P22.0	P33.13	T14
P22.1	P19	P22.1	P33.14	U14
P22.2	R20	P22.2	P33.15	
P22.3	R19	P22.3		U11
	P16	P22.4		T12
	P17	P22.5	P34.1	U12
	N16	P22.6	P34.2	T13
	N17	P22.7	P34.3	U13
	M16	P22.8	P34.4	
	M17	P22.9	P34.5	
	L16	P22.10		
	L17	P22.11		

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UIE

EVADC/EDSADC

AN0	T10	AN0	AN30	T4
AN1	U10	AN1	AN31	R4
AN2	W9	AN2	P40.4/AN32	P4
AN3	U9	AN3	P40.5/AN33	R1
AN4	T9	AN4	AN34	P5
AN5	Y9	AN5	AN35	R2 AN35
AN6	T8	AN6	P40.6/AN36	N4 AN36
AN7	U8	AN7	P40.7/AN37	P2 AN37
AN8	W8	AN8	P40.8/AN38	N5 AN38
	U7	AN9	P40.9/AN39	P1 AN39
AN10	Y8	AN10		M5
AN11	W7	AN11	AN40	M4
AN12	T7	AN12	AN41	L5 AN42
AN13	W6	AN13	AN42	L4 AN43
	U6	AN14	AN43	N1 AN44
	T6	AN15	AN44	N2 AN45
AN16	W5	AN16	AN45	M1 AN46
AN17	U5	AN17	AN46	M2 AN47
	W4	AN17/P40.10	AN47	
	W3	AN18/P40.11		
	W3	AN19/P40.12		
AN20	Y3	AN20		
AN21	Y2	AN21		
	T5	AN22		
	R5	AN23		
AN24	W2	AN24/P40.0		
AN25	W1	AN25/P40.1		
	V2	AN26/P40.2		
	V1	AN27/P40.3		
	U2	AN28/P40.13		
	U1	AN29/P40.14		

SAK-TC377TP-96F300S

Title: TC377_CoreBoard_PORTS

Size: A4

Author: 猫肆管

Revision: 2.0

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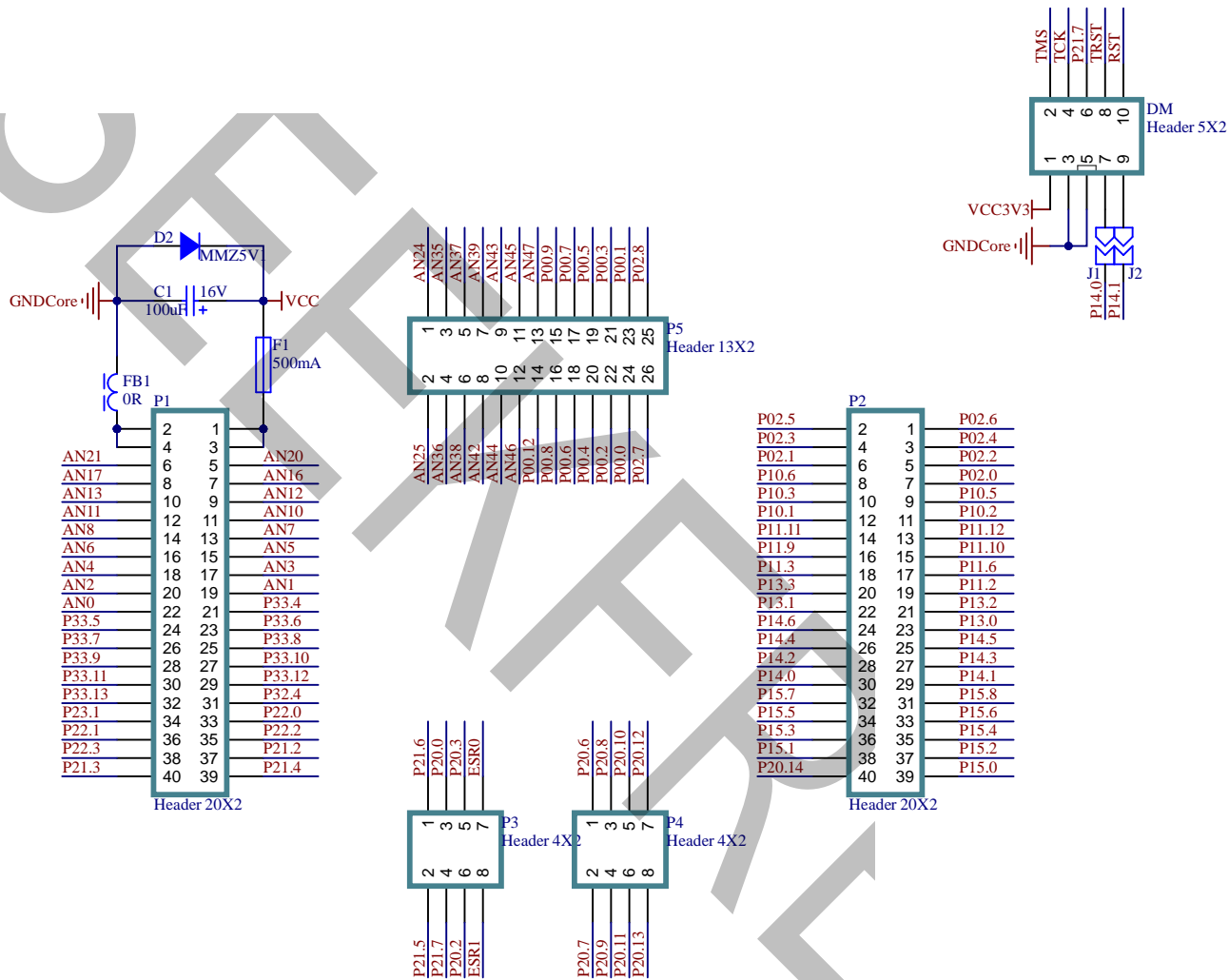


Date: 2020/12/17

Time: 19:17:21

Sheet: 3

of: 4



Title: TC377_CoreBoard_IO

Size: A4

Author: 猫肆管

Revision: 2.0

Date: 2020/12/17

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Sheet: 4 of: 4

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