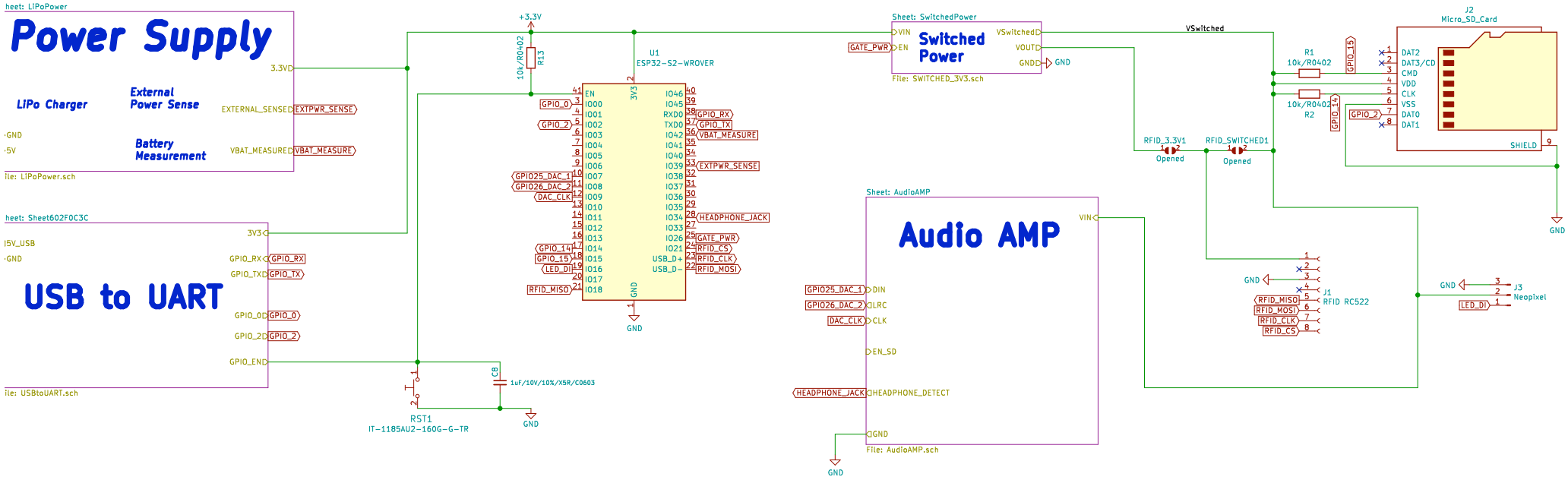

Internal Bootstrapping Resistors	
MTDI/GPIO12:	Pull-Down
GPIO0:	Pull-Up
GPIO2:	Pull-Down
GPIO4:	Pull-Down
MTDO/GPIO15:	Pull-Up
GPIO5:	Pull-Up

Bootstrapping Pins Settings

Voltage of internal LDO(VDD_SDIO)			
Pin	Default	3.3V	1.8V
GPIO12/MTDI	Pull-Down	0	1

Booting Mode			
Pin	Default	SPI Flash Boot	Download Boot
GPIO0	Pull-Up	1	0
GPIO2	Pull-Down	Don't-care	0

Debugging Log on U0TXD During Booting			
Pin	Default	U0TXD Toggling	U0TXD Silent
GPIO15/MTDO	Pull-Up	1	0

Timing of SDIO Slave					
Pin	Default	Falling-edge:Output	Falling-edge:Input Rising-edge:Output	Rising-edge:Input Falling-edge:Output	Rising-edge:Input Rising-edge:Output
GPIO15/MTDO	Pull-Up	0	0	1	1
GPIO5	Pull-Up	0	1	0	1

Fiducials:

FID1 Fiducial FID2 Fiducial FID3 Fiducial FID4 Fiducial

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Sheet: /
File: QUino-Dogbox.sch

Title: ESPuino Complete

Size: A3	Date: 2021-02-16	**Rev: 0.1**
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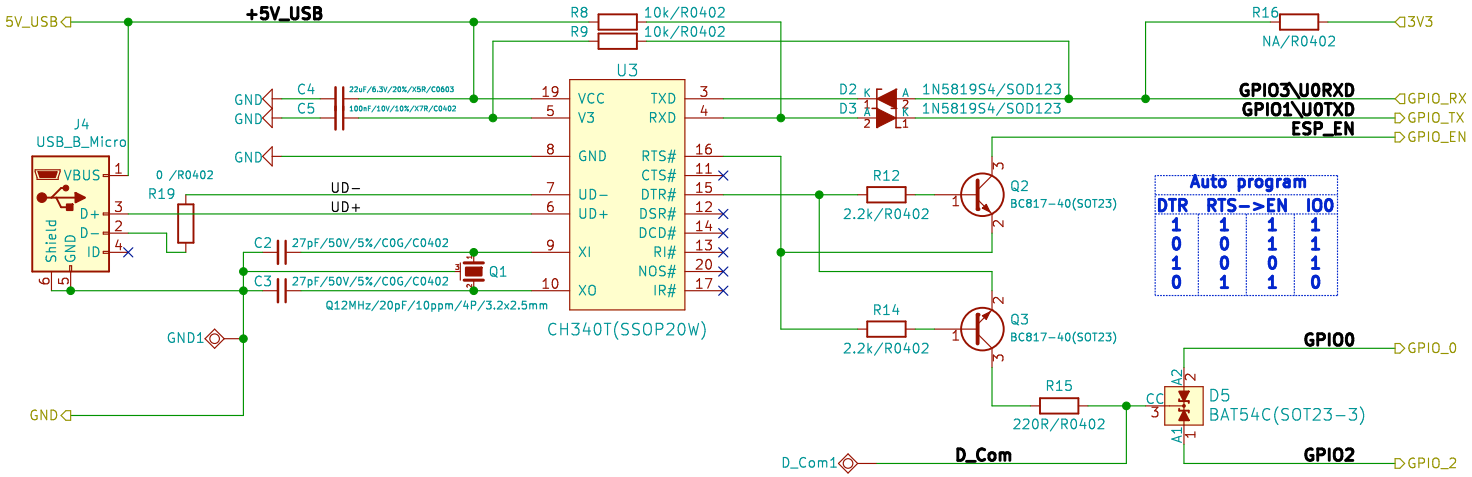
USB to UART



https://datasheet.lcsc.com/szlcsc/1811151532_WCH-Jiangsu-Qin-Heng-CH340T_C8689.pdf

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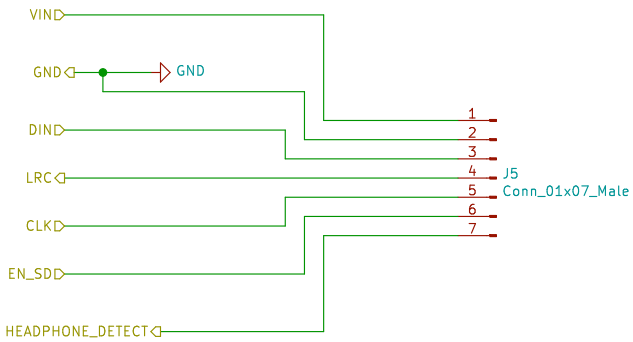
Audio AMP



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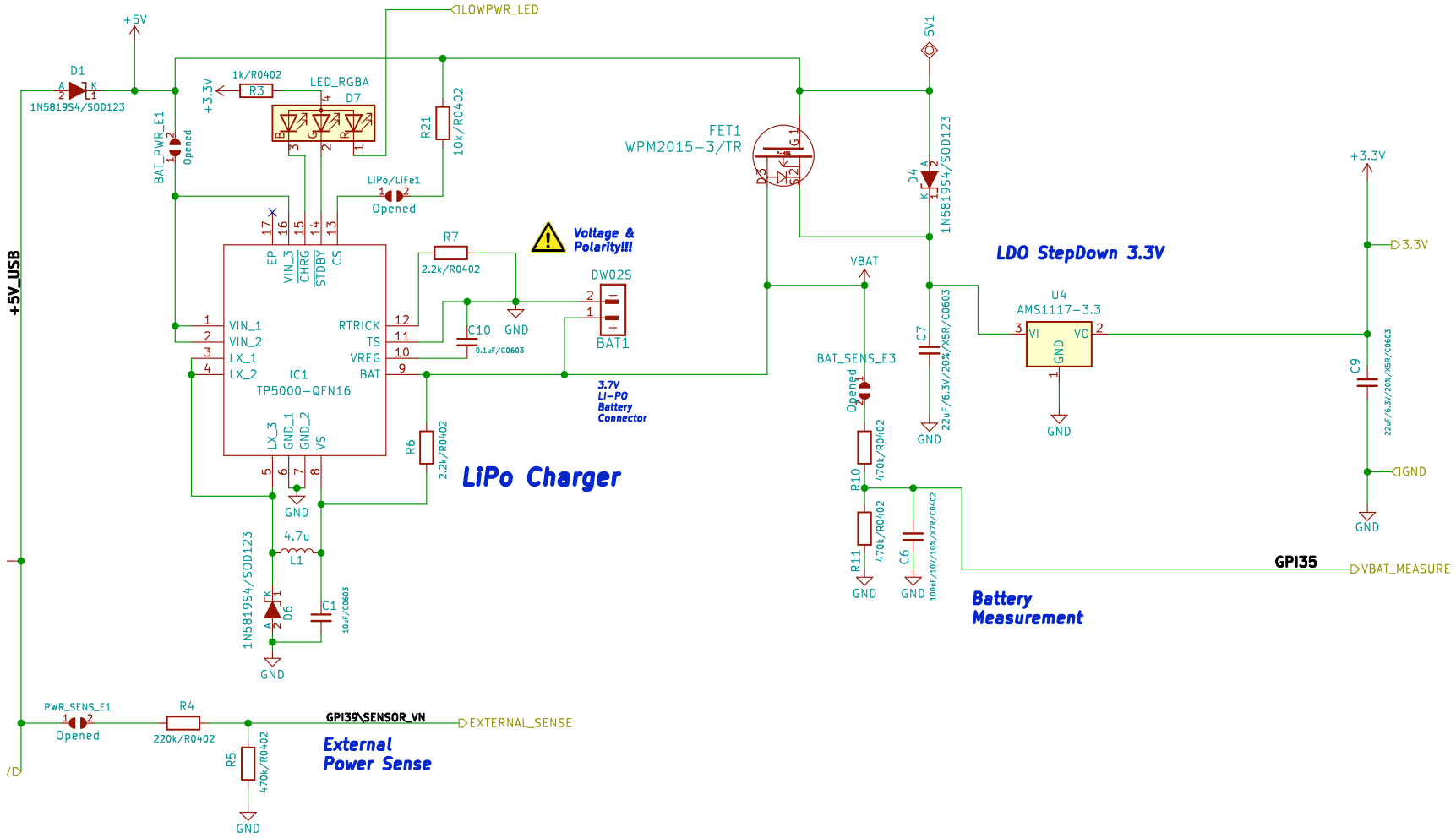
Power Supply



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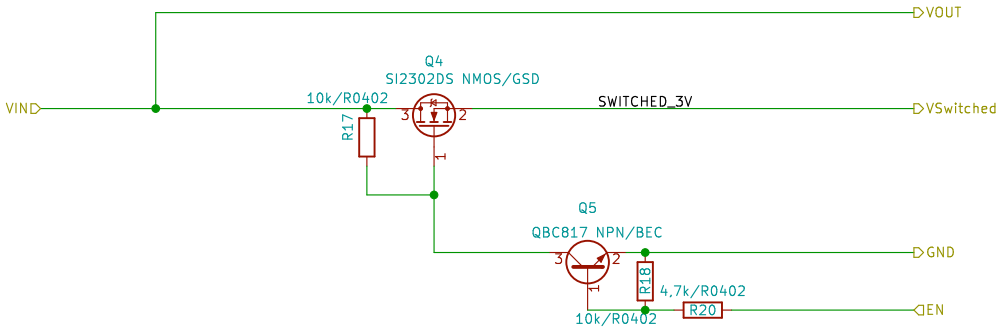
Switched Power



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