CMPE 364 (L51)
Final Exam
Spring 2017
June 13th, 2017

Instructions: Read carefully through the entire exam first, and plan your time accordingly. Note the relative weights of each segment. Make sure that you have all pages of the exam. If you are missing any pages, inform the instructor immediately.

During this exam you may use the book *Introduction to Microprocessor Based Systems Using the Arm Processor* by Kris Schindler as well as any notes handwritten by you. The use of any other reference material will result in the confiscation of your exam and you receiving a score of 0. No electronic devices are allowed.

Write your answers on this exam. **Do not write on the back of any pages.** If you need additional space, use the extra pages at the end of the exam. Anything written on the backside of a page will not be graded.

When answering questions that require calculations, be sure to show your work and give your answer in a simplified form. Answers without appropriate work will not receive credit. When answering questions that request an explanation, keep your explanation short and correct. Explanations containing incorrect or irrelevant information will be marked wrong, even if correct information is also included.

When you are done, present your completed exam to the instructor at the head table. If leaving before the exam period is concluded, please leave as quietly as possible as a courtesy to your neighbors.

TL;DR:

- **You can use the book and handwritten notes, but nothing else.**
- **Don't write on the back of any pages, use the extra pages at the end.**
- **Simplify your answers to math problems.**
- **Show your work or you won't get points.**
- **Keep justifications concise and correct.**

Name:

Student ID Number:

Signature:

1. Basic Assembly

   (a) (4 points) What is the value of register r3 after the following instruction has executed? Assume that r1 and r2 contain `0x5fb2158b` and `0x30be16ce` respectively.

   ```
   ADD r3, r2, r1
   ```

   Solution:
   0x90702C59

   (b) (4 points) Write a single assembly language instruction equivalent to the following:

   ```
   LDR r0, [r1]
   ADD r1, r1, #4
   ```

   Solution:
   LDR r0, [r1], #4

   (c) (5 points) Write a short program which implements the following line of high level language pseudocode. Assume all source registers may be used as scratch registers. You may not use any multiply instructions. You may only use 4 registers. Only programs of six or less instructions will receive full points. (Longer programs that are correct will still receive the majority of the points.)

   ```
   r0 := ((r1 * 129) / 32 + (r2 + 56) / 8 + r3 / 16) * 2
   ```

   Solution:

   ```
   ADD r1, r1, r1, LSL #7
   MOV r1, r1, LSR #4
   ADD r2, r2, #56
   ADD r0 ,r1, r2, ASR #2
   ADD r0, r0, r3, ASR #3
   ```

2. Memory

(a) (5 points) Consider the following map of memory. What are the contents of registers r2 and r4 after the execution of the following instruction? Assume that r2 and r4 initially contain `0x4f790786` and `0x80084020` respectively. Also assume that the processor is configured in little-endian mode.

```
LDR r2, [r4], #4
```

Address	Contents
0x80084024	0xb9
0x80084023	0xfd
0x80084022	0x2d
0x80084021	0x90
0x80084020	0x2b
0x8008401F	0x24
0x8008401E	0x17
0x8008401D	0x98

Solution:

`r2 = 0xfd2d902b`

`r4 = 0x80084024`

(b) (5 points) Consider the following map of memory. Determine the contents of registers r2, r3, r4, and `sp` after the execution of the following instruction, given an initial value of `0x80084018` for `sp`.

```
LDMED sp!, {r4-r2}
```

Address	Contents
0x80084024	0xaa3333aa
0x80084020	0x99555599
0x8008401C	0x88dddd88
0x80084018	0x66000066
0x80084014	0x88222288
0x80084010	0x77111177
0x8008400C	0x44dddd44

Solution:

`r4 = 0xaa3333aa`

`r3 = 0x99555599`

`r2 = 0x88dddd88`

`sp = 0x80084024`

3. Serial Communication

(a) (3 points) How do you determine the device address for an I_2C device?

Solution: You check the datasheet for the device which should specify it.

(b) (3 points) SPI is described as full-duplex, as opposed to I_2C which is half-duplex. In the context of SPI, what does full-duplex mean and what makes SPI full-duplex?

Solution: Full-duplex means data can be sent and received at the same time. In SPI, one bit is sent and one bit is received every clock cycle.

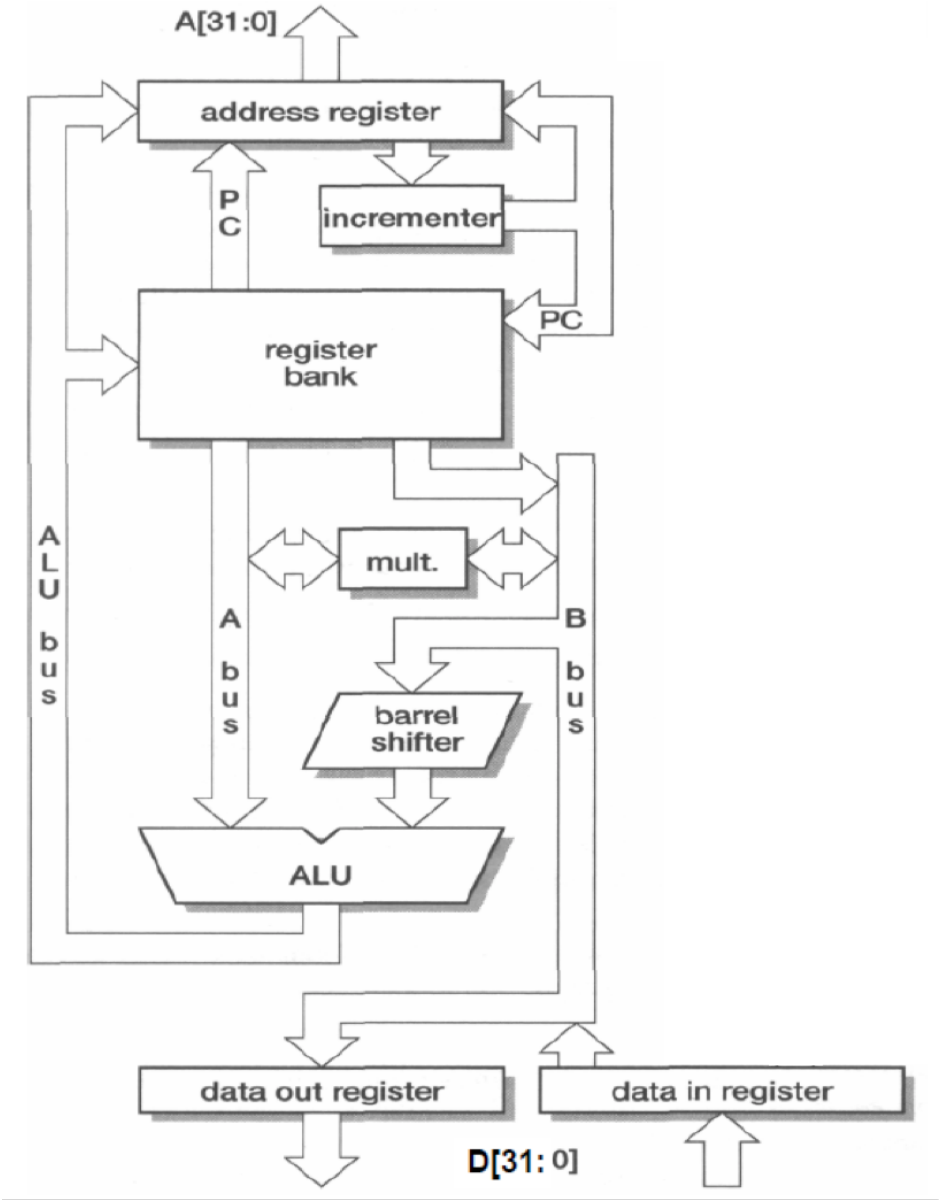
(c) (3 points) When using UART communication, how does the receiver know how many bits the sender will send in a UART frame?

Solution: They are both configured the same way beforehand.

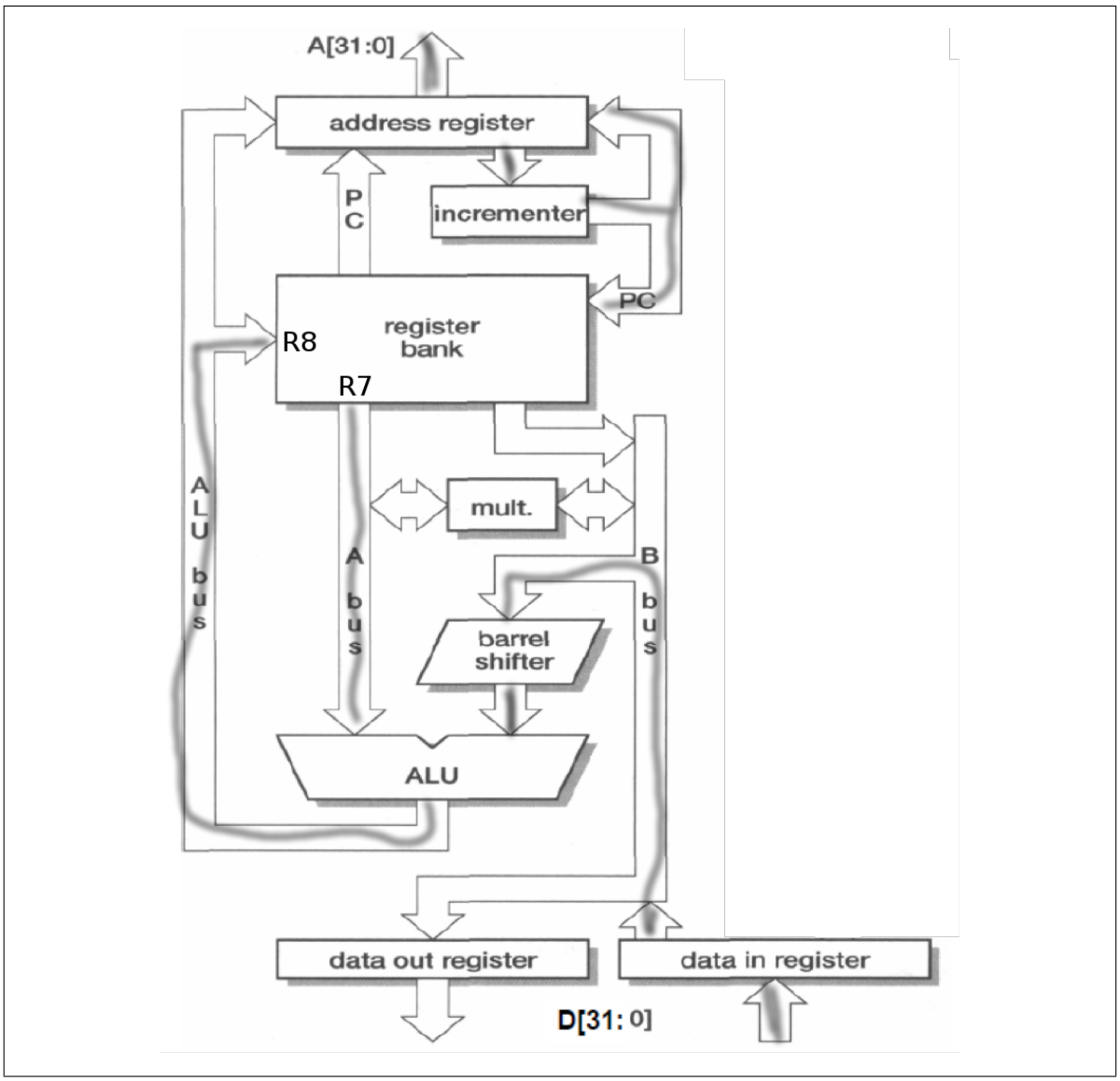
4. (6 points) Datapath

On the datapath diagram below, *shade in the portions of the datapath* used when the following instruction executes. Also, *label the read and write ports* on the register bank with which register is being outputted on that port.

```
RSB R8, R7, #10
```



Solution:


A[31:0]
address register
incrementer
PC
register bank
R8
R7
PC
ALU bus
A bus
B bus
mult.
barrel shifter
ALU
data out register
data in register
D[31: 0]

5. (10 points) Trial Subtraction

   Compute both the *quotient and remainder* of $1608 \div 9$ using trial subtraction in 8-bits. Remember that you must show all work. In addition, explicitly *label your final answer.*

Solution:

$9 * 2^7 < 1608$ $[1608 - 9 * 2^7 = 456]$

$9 * 2^6 > 456$

$9 * 2^5 < 456$ $[456 - 9 * 2^5 = 168]$

$9 * 2^4 < 168$ $[168 - 9 * 2^4 = 24]$

$9 * 2^3 > 24$

$9 * 2^2 > 24$

$9 * 2^1 < 24$ $[24 - 9 * 2^1 = 6]$

$9 * 2^0 > 6$

Therefore...

$q = 0b10110010 = 0xB2 = 178$

$r = 6$

6. Diagramming Pipeline Stalls

Consider the following lines of assembly code:

```
add r4, r4, #4
ldrb r0, [r4]
sub r3, r3, r4
rsb r5, r5, r0
mov r6, #45
```

(a) (8 points) *Fill in the following diagram* showing the progression of the code through the pipeline. *If there are any stalls, circle them.* You should complete the diagram until there are no more instructions in the pipeline.

Cycle #	Fetch	Decode	ALU	LS1	LS2

Solution:

Cycle #	Fetch	Decode	ALU	LS1	LS2
1	add				
2	ldrb	add			
3	sub	ldrb	add		
4	rsb	sub	ldrb	add	
5	mov	rsb	sub	ldrb	add
6		mov	rsb	sub	ldrb
7		mov	rsb		sub
8			mov	rsb	
9				mov	rsb
10					mov

(b) (2 points) How many cycles does it take to fully execute these instructions?

Solution: 10 cycles

7. Analog Sensors

Imagine you are interfacing a HIH-4030 analog humidity sensor to a microprocessor that has a 10-bit ADC unit. Both your microprocessor and the sensor use a V_{cc} of 5 volts. The system is operating in an environment that is 70°C. An exert from the datasheet for the HIH-4030 is included as an addendum to this exam.

The ADC is configured as follows:

- $V_{ref+} = 5V$
- $V_{ref-} = 0V$

(a) (6 points) After connecting the humidity sensor and interfacing to it with the ADC, you perform a reading and receive the following 10-bit value: `0100000000`.

What is the analog voltage being output by the sensor?

Solution:
$\frac{5V-0V}{2^{10}} * 2^8 = 1.25V$

(b) (6 points) What is the humidity that corresponds to this voltage? (Be sure to include the units of your answer.)

Solution: 18% Relative Humidity

8. (15 points) Optimization

Consider the following assembly subroutine:

```
; The Doubler: A simple subroutine to double the value of every
;              integer in an array.
; r1 contains the address of any array of integers
; r0 contains the number of items in the array
doubler:
    ; Counter for the loop
    mov r2, #0

    dloop:
        cmp r2, r0
        bge end
        ldr r3, [r1]
        mov r3, r3, LSL #1
        str r3, [r1], #+4
        add r2, r2, #1
        b dloop
    end:
        bx lr
```

Write an optimized version of this subroutine. The more optimized your solution, the more points you will receive. (Note, however, that code that doesn't work properly will receive low points, regardless of how optimal it is. So ensure your program functions properly)

You should also write a few sentences summarizing the optimizations you did.

Nothing written on this page will be graded, write your answer on the next page.

Answer for Question 8.

Solution:

```
; The Doubler: A simple subroutine to double the value of every integer in an array.
; r1 contains the address of any array of integers
; r0 contains the number of items in the array
doubler:
    ldr r3, [r1]
    subs r0, r0, #1
    mov r3, r3, LSL #1
    str r3, [r1], #+4
    bne doubler
    bx lr
```

My first optimization is that I changed from an incrementing counter to a decrementing one, and used subs instead of a normal sub. The removed a cmp and a conditional branch.

My second optimization is that I removed the stall by moving the subs to be right after the ldrb.

9. (15 points) Strings

   Since the dawn of time, people have loved cats. It isn't clear why, but they do. (Personally, I only tolerate cats. But that's not really relevant to this question.)

   Write a subroutine called `find_cat` that returns `1` if a given null-terminated string contains "CAT" and `0` otherwise. Your subroutine should follow the conventions specified by the AAPCS. Your answer should include only the subroutine.

   Parameters for `find_cat`:

   - Address of a null-terminated string.

   Return value of `find_cat`:

   - `1` if the string contains "CAT"
   - `0` if the string does not contain "CAT"

   When writing your answer, correctness should be your first priority. However, among correct solutions efficiency will also be considered when assigning points.

   Examples:

   1. When given the string "CATALOG", the subroutine should return `1`.
   2. When given the string "blahCATblah", the subroutine should return `1`.
   3. When given the string "ABCEDFG", the subroutine should return `0`.
   4. When given the string "catalog", the subroutine should return `0`.

Nothing written on this page will be graded, write your answer on the next page.

Answer for Question 9.

Solution:

```
find_cat:
    stmfd sp!, {r4}
    loop:
        ldrb r0, [r4]
        ldrb r1, [r4, #1]
        ldrb r2, [r4, #2]
        add r4, r4, #1

        cmp r0, #'C'
        cmpeq r1, #'A'
        cmpeq r2, #'T'
        beq success

        cmp r0, #0
        cmpne r1, #0
        cmpne r2, #0
        bne loop
    mov r0, #0
    ldmfd sp!, {r4}
    bx lr

success:
    mov r0, #1
    ldmfd sp!, {r4}
    bx lr
```

End of Exam.

Extra Work Page 1.
If you need more space to write your answer to a problem, then write it here and make a note on the problem's main page referring to this space.

Extra Work Page 2.
If you need more space to write your answer to a problem, then write it here and make a note on the problem's main page referring to this space.